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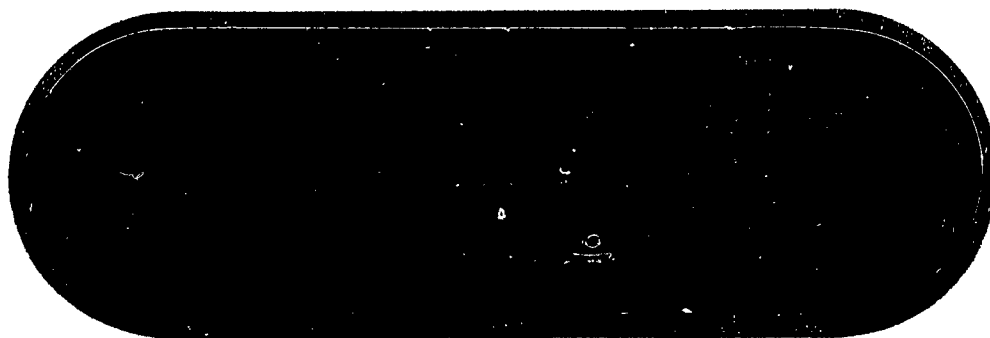
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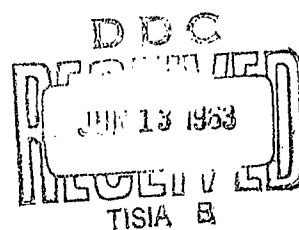
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SEATTLE, WASHINGTON

THE BOEING COMPANY

NUMBER T2-2548 Vol. 2

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Integration Test No. Two (EDIT-II-1) Report (II)

MODEL NO. WS-133A CONTRACT NO. AF 04(647)-289

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VOL. 2 NO. T2-2548
SEC. 1 PAGE 1 OF 46

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U3 4288 1000

BOEING

VOL. 2

NO. 112 2549

SEC. 1

PAGE 2

TABLE OF CONTENTS

		<u>Page</u>
 <u>Section I</u>		
1.0.0	Purpose and Scope	5
1.1.0	Equipment in Test	5
2.0.0	Summary	5
3.0.0	Report	6
4.0.0	Conclusion	46

Section II

Manufacturing & Inspection Record
Test Log

Section III

Photographs of Equipment

Section IV

1.0.0	Purpose	2
1.1.0	Equipment	2
2.0.0	Summary	2
3.0.0	Report	4
4.0.0	Conclusion	43

REVISED

2/22/63

U3 4286 2000

BOEING

VOL 2

NO. T2-2548

SEC. 1

PAGE

3

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Section V

Manufacturing and Inspection Record
Test Log
Fig.

Section VI

Photographs of Equipment

FIGURE INDEX

		<u>Section</u>	<u>Page</u>
Figure 3.1.1.2-1	Self-Test Connection	1	8
Table 3.1.3.2-1	Voltage Requirements	1	13
Figure 3.2.1.3-1	P/G Test Connection	1	21
Table 3.2.2.2-1	Voltage Requirements	1	24
Table 3.2.2.4-1	Voltage Requirements	1	25
Table 3.1.1.2-4	Self-Test Connection	4	5
Table 3.1.3.2-4	Voltage Requirements	4	11
Table 3.2.1.3-4	P/G Test Connection	4	19
Table 3.2.2.2-4	Voltage Requirements	4	22
Table 3.2.2.4-4	Voltage Requirements	4	23

1.0.0 PURPOSE AND SCOPE

1.0.1 The purpose of this volume is to present the test report for Engineering Development Integration Test - II on Block Change -1 equipment (EDIT II-1). The EDIT II-1 test was conducted in accordance with the Test Procedures Document D2-13308 Volume 2 and the EDIT Program Plan, D2-13307.

1.1.0 EQUIPMENT IN TEST

Programmer Group, Figure A 1201

Programmer Group Test Set (Portable) Figure A 3092

Relay Assembly, Dummy Decoder, Figure A 3113

400 Cycle Power Control Box (25-34035)

EDIT Power Supply Cable (Power supply to 400 cycle control box).

2.0.0 SUMMARY

2.0.1 A self test of the Programmer Group (P/G) Test Set was completed with one minor discrepancy noted. In the voltage checks of paragraph 3.1.3.2 -(3) at Selector Switch positions 15 and 18, voltages were found to be slightly out of tolerance. The purpose of these voltage checks was to verify continuity. The tolerances given are being changed to allow for diode voltage drops which were larger than anticipated.

REVISED _____

U3 4288 2000

BOEING

VOL

2

NO.

T2-2548

SEC.

1

PAGE

5

Compatibility tests between the P/G and its Test Set resulted in No-Go's from approximately 15 cards. It was found that the phase supply voltage rise time of the P/G was operating near the lower limit of tolerance. The P/G model specifications state that rise time of the phase supply voltage should be measured in the range from 10 to 90 percent of the pulse magnitude of 12 volts. The P/G functional test was set up using this 10 to 90 percent range for phase supply voltage testing. When functionally tested, the P/G was found to be within tolerance. The P/G Test Set, however, was calibrated to measure rise time in the range from 3 to 10 volts (25 to 83.3 percent) of the pulse magnitude. In other words, the P/G Test Set measured the rise time of the phase supply voltage over a narrower range than was used in the P/G functional test. As a result, the phase supply voltage was slightly out of tolerance and the P/G Test Set initiated the No-Go's.

The P/G Test Set evaluators were recalibrated to measure the phase supply voltage rise time over a wider range. After recalibration the compatibility tests were repeated with acceptable results.

3.0.0

REPORT

3.0.1

The EDIT II-1 test was performed according to the Test Procedure Document D2-13308, Volume 2. These procedures and the recorded results are given in paragraph 3.1.0.0 through 3.3.8.3 which follow.

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.0.0 <u>P/G TEST SET SELF-TEST PROCEDURES</u> 3.1.1.0 Test Set-Up 3.1.1.1 The P/G Test Set must be isolated from all electronic equipment with the exception of the Test Set Power case. 3.1.1.2 Connect the regulated power supply suitcase to the Test Set electronic component and control suitcase as shown in Figure 3.1.1.2-1.			

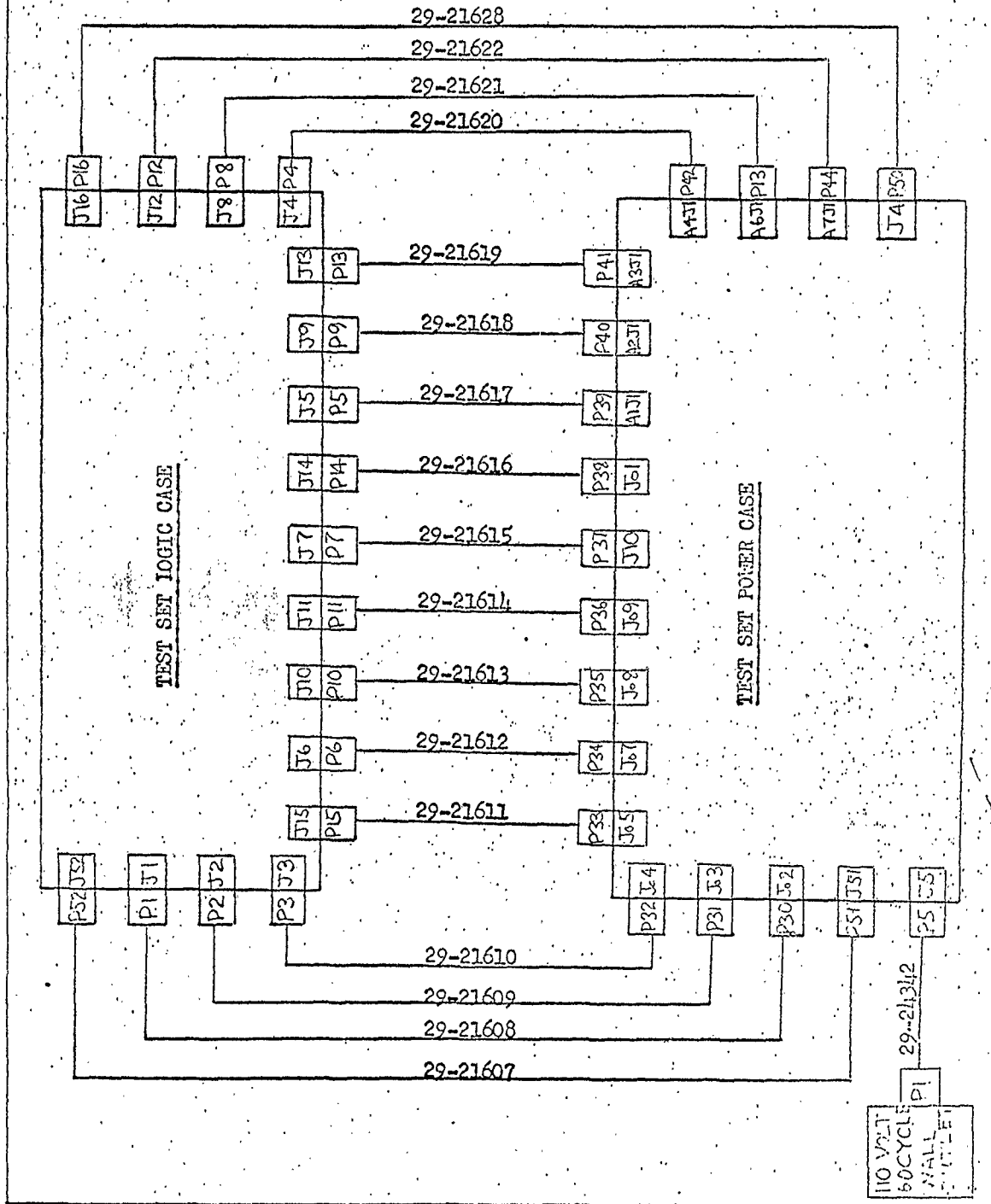
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VOL	2	NO. T2-2548
SEC.	1	PAGE 7

Figure 3.1.1.2-1
Self-Test Connection



PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.1.3 Connect power cable to 120 volt, 60 cps outlet.			
3.1.1.4 Press the TEST SET POWER ON switch on the Test Set. <u>NOTE:</u> The Test Set must be ON for 30 seconds before running any card tests.	TEST SET POWER ON lamp turns ON. The AFS ALARM and W/H ALARM lamps turn ON. The WARNING lamp starts flashing at 4 cps. These conditions exist until the 13th card.	OK OK OK	
3.1.1.5 Press the P/G POWER ON switch.	P/G POWER ON lamp ON.	OK	

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.2.0 Card Tests 3.1.2.1 Insert selected punched cards in the Test Set CARD READER face up and beveled corner out. Push CARD READER HANDLE fully down to engage electrical contacts. 3.1.2.2 Push START TEST button on the Test Set. NOTE: The START TEST switch must be held down until the START TEST lamp turns ON. 3.1.2.3 When GO lamp illuminates and the TEST IN PROCESS lamp goes OFF, lift the card reader handle and remove card.	START TEST lamp ON. TEST IN PROCESS lamp OFF. GO or NO-GO lamp ON.	OK OK OK	

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US 4298 7000 (MAY 1943) (AC 4131D)

ENGINE

VOL 2
SEC 1

NO T2-2548
PAGE 10

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.2.3. (Continued) <u>NOTE:</u> Additional instructions, when needed, are given for each card test. 3.1.2.4 Record each test result in appropriate column allocated for each card or voltage test in this document. 3.1.2.5 Repeat paragraphs 3.1.2.1 thru 3.1.2.4 for each succeeding test. 3.1.3.0 Self-Test 3.1.3.1 Select punched cards (25-30947-125 thru 25-30947-147) for self-test of the Test Set.			

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US 4260 2000 (WAS BAC 4131D)

BEING

VOL	2	NO	T2-2548
SEC	1	PAGE	11

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.3.2 Follow paragraphs 3.1.2.1 thru 3.1.2.5 starting with card 25-30947-125. (1) Card 25-30947-125. After the GO lamp is ON, set the Range Switch at 3, and rotate the Selector Switch successively through the positions indicated in Table 3.1.3.2-1. Read the voltmeter at each position of the Selector Switch. (2) Card 25-30947-126. Follow same procedure as number 1 above.	GO lamp ON. The voltmeter shall read 10 ± 0.5 volts at each setting of the Selector Switch. NO-GO lamp ON. The voltmeter shall read 10 ± 0.5 volts at each setting of the Selector Switch except at position 14 which reads 0 ± 0.5 volts.	OK See Table 3.1.3.2-1 OK See Table 3.1.3.2-1	

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US 4710 2000 (WAS DAC 4131D)

REVISION

VOL 2

NO T2-2548

SEC 1

PAGE 12

Table 3.1.3.2-1

Voltmeter Switching Requirements Self-Test Procedure

Selector Switch Position	Card-125 Acpt. Volts	Card-125 Volts Read	Card-126 Acpt. Volts	Card-126 Volts Read	Card-127 Acpt. Volts	Card-127 Volts Read	Card-128 Acpt. Volts	Card-128 Volts Read
1	10 ± 0.5 V	10.0 V	10 ± 0.5 V	10.0 V	10 ± 0.5 V	10.0 V		
2								
3								
4								
5								
6								
7								
8								
9								
10								
11								
12	10 ± 0.5 V	10.0 V		10.0 V		10.0 V		
13			10 ± 0.5 V	10.0 V	10 ± 0.5 V	10.0 V		
14	10 ± 0.5 V	10.0 V	0 ± 0.5 V	0.0 V				
15			10 ± 0.5 V	10.0 V	10 ± 0.5 V	-9.3 V		
16								
17	10 ± 0.5 V	10.0 V	10 ± 0.5 V	10.0 V				
18	10 ± 0.5 V	10.0 V			10 ± 0.5 V	-9.3 V		

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U3 4288 2000

DDG INC

VOL 2

NO. T2-2548

SEC. 1

PAGE 13

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
(3) Card 25-30947-127. Follow same procedure as number 1 above.	GO lamp ON. The voltmeter shall read 10 ± 0.5 volts at each setting of the Selector Switch.	OK See Table 3.1.3.2-1.	At Selector switch position #15 & 18, 9.3 V were indicated on the voltmeter. The tolerances of voltages given will be changed since the purpose of these voltage checks is to verify continuity. The voltage drops across the diodes were of greater magnitude than originally anticipated.
(4) Card 25-30947-128. Follow same procedure as number 1 above.	NO-GO lamp ON. The voltmeter shall read 10 ± 0.5 volts at each setting of the Selector Switch.	OK See Table 3.1.3.2-1.	
(5) Card 25-30947-129. NOTE: This test requires 66 seconds for completion.	GO lamp ON.	OK	This card resulted in a GO but the test did not require 66 seconds. This was due to a mispunched card which checked the timer in the P/G. Using a new card, the test was repeated with acceptable results.

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U.S. 4700, 2000 (FAS) DAC 41310)

BOEING

VOL	2.	NO, T2-2548
SEC.	1	PAGE 14

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
(6) Card 25-30947-130	GO lamp ON.	OK	
(7) Card 25-30947-131	NO-GO lamp ON.	OK	
(8) Card 25-30947-132	GO lamp ON.	OK	
(9) Card 25-30947-133	GO lamp ON.	OK	
(10) Card 25-30947-134	NO-GO lamp ON.	OK	
(11) Card 25-30947-135	GO lamp ON. The stepping switch will scan 4 times during test with an audible pause between scans.	OK	
Repeat test but this time push the P/G POWER OFF switch during first scan of stepping switch.	NO-GO lamp ON.	OK	

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
(12) Card 25-30947-136	GO lamp ON.	OK	
(13) Card 25-30947-137. Repeat test, but this time push the P/G POWER OFF switch during test.	GO lamp ON. NO-GO lamp ON. The W/H ALARM, AFS ALARM and WARNING lamps will turn OFF when the P/G POWER OFF switch is depressed. The lamps will turn back ON when switch is released.	OK OK OK	
(14) Card 25-30947-138.	GO lamp ON. The AFS ALARM lamp turns OFF during test.	OK OK OK	

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US 4200 2000 (was DAC-41310)

ENDING

VOL

2

NO T2-2548

SEC.

1

PAGE 16

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
(15) Card 25-30947-159. Initiate test and observe the START TEST, AFS SAFETY, W/H ALARM, and WARNING lamps.	The following events take place: A. Shortly after pressing the START TEST switch: (1) The AFS ALARM lamp turns OFF momentarily. (2) The START TEST lamp turns ON. B. After the START TEST lamp turns ON: (1) The W/H ALARM turns OFF. (2) The WARNING lamp starts flashing at 25% duty cycle.	OK OK OK OK	

REVISED

U.S. 4200 2000 (WAS DAC 4131D)

WARNING

VOL 2
SEC 1

NO T2-2548
PAGE 17

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
(15) (Continued)	C. When the W/H ALARM turns OFF, the AFS ALARM lamp starts flashing at 8 cps. D. When the stepping switch starts to scan, the W/H ALARM lamp turns ON, the AFS ALARM lamp goes back to steady ON state, and the WARNING lamp resumes flashing at 4 cps. GO lamp ON.	OK	
(16) Card 25-30947-1.40	NO-GO lamp ON.	OK	
(17) Card 25-30947-1.1	GO lamp ON.	OK	

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
(18) Card 25-30947-142	GO lamp ON.	OK	
(19) Card 25-30947-143	GO lamp ON.	OK	
(20) Card 25-30947-144	GO lamp ON The W/H ALARM, AFS ALARM and WARNING lamps will turn OFF when the card reader handle is opera- ted UP. The START TEST lamp turns ON. When the stepping switch starts its scan, the lamps will turn back ON. AFS alarm OFF. GO lamp ON. GO lamp ON. GO lamp ON.	OK OK OK OK OK OK OK OK OK	
(21) Card 25-30947-145			
(22) Card 25-30947-146			
(23) Card 25-30947-147			

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U1 4240 2000

UAC 41310)

REFERENCES

VOL 2

NO. T2-2548

SEC. 1

PAGE 19

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.0.0. <u>COMPATIBILITY TEST OF P/G WITH THE</u> <u>P/G TEST SET.</u> 3.2.1.0 Test Set-Up. 3.2.1.1 The P/G must be isolated from all electronic equipment with the exception of its SOURCE POWER SUPPLY and associated P/G Test Set equipment. 3.2.1.2 Apply cooling air to P/G (ref. 2.1.2) 3.2.1.3 Make the cable connections shown in Figure 3.2.1.3-1.			

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BEING

VOL

2

NO

T2-2548

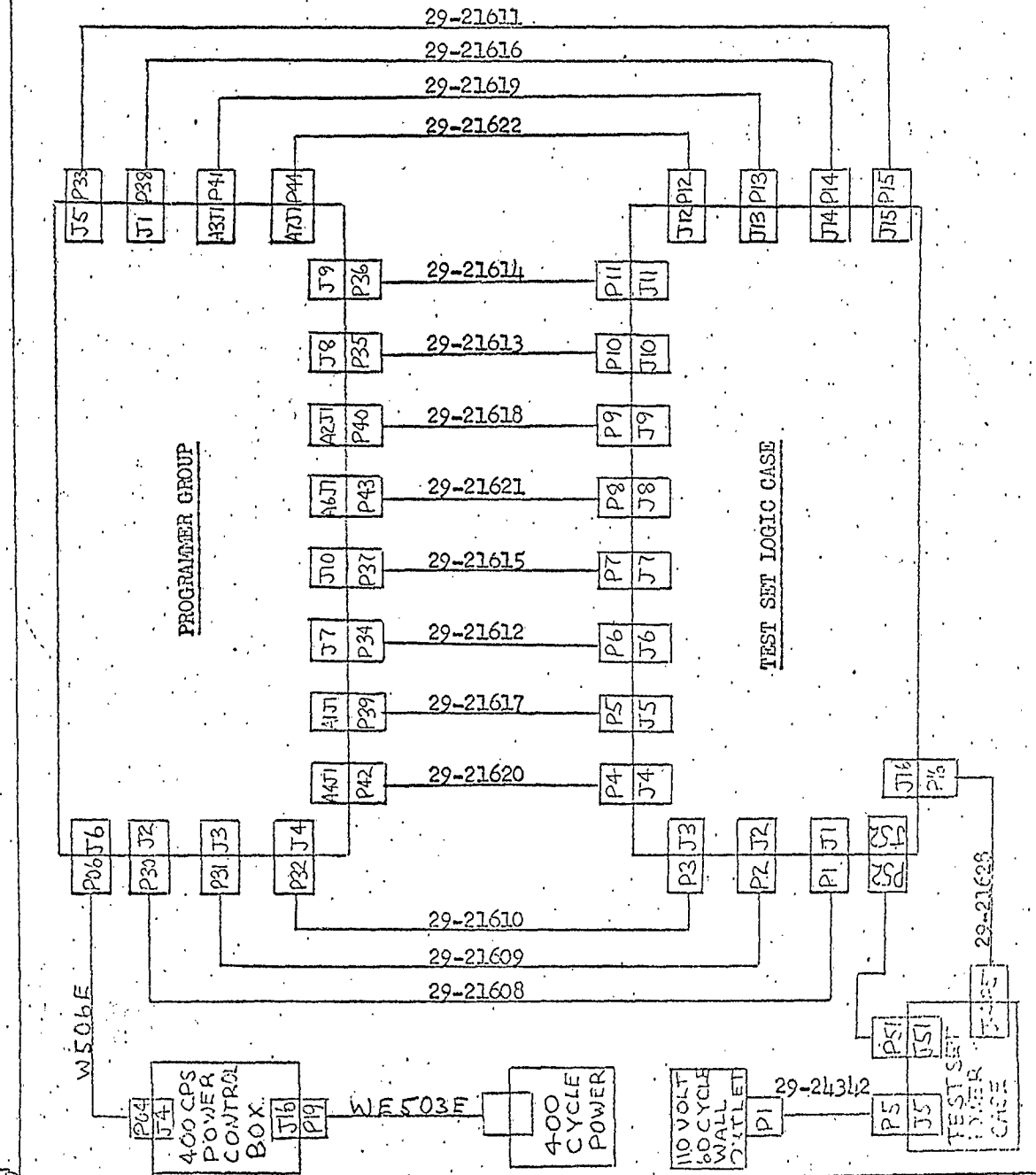
SEC.

1

PAGE

20

Figure 3.2.1.3-1.
P/G Test Connection



PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.1.4 The Dummy Decoder is inserted into Launch Drawer No. 1 of the P/G.			
3.2.1.5 Press the TEST SET POWER ON switch. <u>NOTE:</u> The MONITOR POWER ON lamp on the P/G will have been ON continuously during preceding tests. It will remain ON until the main LF circuit breaker is tripped OFF to replace a faulty P/G drawer.	TEST SET POWER ON lamp ON.	OK	
3.2.1.6 Press the P/G POWER ON switch.	P/G POWER ON lamps ON.	OK	

REVISED

US 42nd 2000 (WAS DAC 4131D)

BOEING

VOL 2
SEC. 1

NO T2-2548
PAGE 22

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.1.4 The Dummy Decoder is inserted into Launch Drawer No. 1 of the P/G.			
3.2.1.5 Press the TEST SET POWER ON switch. <u>NOTE:</u> The IGNITOR POWER ON lamp on the P/G will have been ON continuously during preceding tests. It will remain ON until the main IF circuit breaker is tripped OFF to replace a faulty P/G drawer.	TEST SET POWER ON lamp ON.	OK	
3.2.1.6 Press the P/G POWER ON switch.	P/G POWER ON lamps ON.	OK	

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~~SECRET~~

VOL 2
SEC 1

NO T2-2548
PAGE 22

REVISED

U.S. 4200 2000 (WAS D4C 4131D)

~~DEFINING~~

VOL 2

NO T2-2548

SEC 1

PAGE 23

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.2.0 Voltage Level Check 3.2.2.1 P/G. Monitor Power ON Only 3.2.2.2 Measure the voltages at the Range and Selector positions on the Test Set as specified in Table 3.2.2.2-1. Record the results of each test on specified Table. 3.2.2.3 P/G Monitor and Test Power ON 3.2.2.4 Measure the voltages at the Range and Selector positions on the Test Set as specified in Table 3.2.2.4-1. Record	See Table 3.2.2.2-1. See Table 3.2.2.4-1.		

SELECTOR	RANGE											
	+20 Volts DC						-20 V DC			+40 V DC		
	1	2	3	4	5	6	1	2	3	4	5	6
1	Accept Volts	Accept Volts	Accept Volts	Accept Volts	Accept Volts	Accept Volts	Accept Volts	Accept Volts	Accept Volts	Accept Volts	Accept Volts	Accept Volts
2	Volts Read	Volts Read	Volts Read	Volts Read	Volts Read	Volts Read	Volts Read	Volts Read	Volts Read	Volts Read	Volts Read	Volts Read
3	0 to 2	0 to 2	0 to 2	0 to 2	0 to 2	0 to 2	0 to 2	0 to 2	0 to 2	0 to 2	0 to 2	0 to 2
4	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5
5	10 ± 0.5	10.3	10.3	10.3	10.3	10.3	10 ± 0.5	10.3	10.3	10.3	10.3	10.3
6	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5
7	10 ± 0.5	10.3	10.3	10.3	10.3	10.3	10 ± 0.5	10.3	10.3	10.3	10.3	10.3
8	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5
9	10 ± 0.5	10.3	10.3	10.3	10.3	10.3	10 ± 0.5	10.3	10.3	10.3	10.3	10.3
10	10 ± 0.5	10.3	10.3	10.3	10.3	10.3	10 ± 0.5	10.3	10.3	10.3	10.3	10.3
11	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5	0 to 0.5
12	11.5 ± 0.5	11.4	11.4	11.4	11.4	11.4	11.5 ± 0.5	11.4	11.4	11.4	11.4	11.4
13	17 ± 2	17.5	17.5	17.5	17.5	17.5	17 ± 2	17.5	17.5	17.5	17.5	17.5
14	17 ± 2	17.5	17.5	17.5	17.5	17.5	17 ± 2	17.5	17.5	17.5	17.5	17.5
15	17 ± 2	17.5	17.5	17.5	17.5	17.5	17 ± 2	17.5	17.5	17.5	17.5	17.5
16	17 ± 2	17.5	17.5	17.5	17.5	17.5	17 ± 2	17.5	17.5	17.5	17.5	17.5
17												
18												

TABLE 3.2.2.2-1

REVISED

U3 4268 2000

BOEING

VOL 2

NO T2-2548

SEC 1

PAGE 24

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
(1) 25-31059-106 <u>NOTE:</u> This Test shall be performed prior to any other test.	GO lamp ON	OK	
(2) 25-31059-107		OK	
(3) 25-31059-108		OK	
(4) 25-31059-109		OK	
(5) 25-31059-110 3.2.4.0 End to End Test 3.2.4.1 Select punched cards (25-26642-165 thru 25-26642-232) for End-to-End Test.	GO lamp ON	OK	

REVISED

U.S. 42nd 2000 (WAS 4131D)

REVIEWED

VOL 2
SEC. 1

NO T2-2548
PAGE 27

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.2.4 (Continued) the results of each test on specified table. 3.2.3.0 Phase Supply Voltage Test 3.2.3.1 Select punched cards (25-31059-106 thru 25-31059-110) to check the phase supply voltage in each of the five drawers. 3.2.3.2 Follow paragraphs 3.1.2.1 thru 3.1.2.5 of this volume starting with card 25-31059-106. <u>NOTE:</u> For more detail on function of these cards, see D2-12636 Volume I.			

REVISED

U3 4766 2000 (WAS DAC 4131D)

VOL 2
 SEC 1
 NO T2-2548
 PAGE 26

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.4.2 Follow paragraphs 3.1.2.1 thru 3.1.2.5 of this volume and additional instructions stated in the procedure for each card, starting with card 25-26642-165. <u>NOTE:</u> For more detail on function of these cards, see D2-12636 Volume II. (1) 25-26642-165 (2) 25-26642-166 (3) -168 (4) -169 (5) -170 (6) -171 (7) -172 (8) -173 (9) -174 (10) 25-26642-175	GO lamp ON GO lamp ON.	OK OK OK OK OK OK OK OK OK	

REVISED

U.S. 42nd 2000-1-45 BAC 41310

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.4.2 (Continued) (11) 25-26642-176 (12) -177 (13) -178 (14) -179 OMIT (15) -180 (16) -181 (17) -182 (18) -183 (19) -184 (20) -185 (21) -186 OMIT (22) -187 (23) -188 (24) -1899 (25) -190 (26) -191 (27) 25-26642-192 OMIT	GO lamp ON →	OK →	

REVISED _____
 US 4200 2000 (WAS OAC 41210)

VOL 2
 SEC. 1
 NO. T2-2548
 PAGE 20

31

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.4.6 Wait 10 ± 2 minutes and then push START TEST button.	NO-GO lamp ON.	OK	
3.2.4.7 Open and close the card reader and wait another 10 ± 2 minutes; then follow paragraphs 3.1.2.1 thru 3.1.2.4.	GO lamp ON.	OK	
3.2.4.8 Card 25-26642-217			
3.2.4.9 Select punched card 25-26642-217.			
3.2.4.10 Follow paragraph 3.1.2.1.			

REVISED

U1 4200 2000 (WAS DAC 4131D)

DOING	VOL 2	NO T2-2548
	SEC 1	PAGE 32

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.4.11 Depress the SHUT DOWN LAUNCH FACILITY button on the P/g. <u>CONTINUE TO COLD BUTTON DOWN THROUGHOUT</u> <u>THIS CARD TEST.</u> Release only after GO or NO-GO is received.			
3.2.4.12 Follow paragraphs 3.1.2.2 thru 3.1.2.4. GO lamp ON.		OK	
3.2.4.13 Card 25-26612-218.			
3.2.4.14 Select punched card 25-26612-218.			
3.2.4.15 Follow paragraph 3.2.1.			

REVISED
 U1 4285 2000 (WAS OAC 4131D)

VOL 2
 SEC. 1
 NO T2-2548
 PAGE 33

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.4.16 Depress the P/G POWER OFF switch on the EGS-74 to initiate shut down. <u>KEEP DEPRESSED FOR DURATION OF TEST.</u>			
3.2.4.17 Follow paragraphs 3.1.2.1 thru 3.1.2.4. NOTE: To start up after the foregoing test, depress P/G POWER ON switch.	GO lamp ON.	OK	
3.2.4.18 Select punched cards (25-26642-219 thru 25-26642-232).			
3.2.4.19 Follow paragraphs 3.1.2.1 thru 3.1.2.5 of this volume starting with card 25-26642-219.			

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U3 4288 2000 8WAS MAC 413101

DEFEND

VOL. 2
SEC. 1

NO T2-2548
PAGE 34

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
(1) 25-26642-219 (2) -220 (3) -221 (4) -222 (5) -223 (6) -224 (7) -225 (8) -226 (9) -227 (10) 25-26642-228 (11) -229 (12) -230 (13) -231 (14) 25-26642-232 3.2.5.0 Timer Sequential Drawer Test 3.2.5.1 Select punched cards (25-26643-120 thru 25-26643-138)	GO lamp ON OK GO lamp ON OK	OK OK	

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101 4754 2100 (WAS BAC 41310)

DEFINING

VOL

2

NO

T2-2548

SEC

1

PAGE

35

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.5.2 Follow paragraphs 3.1.2.1 thru 3.1.2.5 of this volume starting with card 25-26643-120. <u>NOTE:</u> For more detail on function of these cards, see D2-12636, Volume III. (1) 25-26643-120 (2) -121 (3) -122 OMIT (4) -123 (5) -124 (6) -125 (7) -126 (8) -127 (9) -128 (10) -129 (11) 25-26643-130	GO lamp ON. OK	OK	

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REVISION

VOL	2	NO T2-2548
SEC	1	PAGE 36

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.5.2 (Continued) (12) 25-26643-131 (13) -132 (14) -133 (15) -134 (16) -135 (17) -136 (18) -137 (19) 25-26643-138	GO lamp ON. OK	OK	
3.2.6.0 Programmer Launch Sequence Drawer Test 3.2.6.2 Select punched cards (25-26644-128 thru 25-26644-155) 3.2.6.2 Follow paragraphs 3.1.2.1 thru 3.1.2.5 of this volume starting with 25-26644-128.	GO lamp ON.	OK	

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US 4288 7000 (WAS OAC 4131D)

REPLACING

VOL

2

NO. T2-2548

SEC.

1

PAGE

37

34

REVISED
11 2 69 2000 (WAS HAC 4131D)

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.6.2 (Continued) NOTE: For more detail on function of these cards, see D2-12636 Vol. IV. (1) 25-26644-128 (2) -129 (3) -130 (4) -131 (5) -132 (6) -133 (7) -134 (8) -135 (9) -136 (10) -137 (11) -138 (12) -139 (13) -140 (14) 25-26644-141	GO Lamp ON. OK GO lamp ON. OK		

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.7.0 Programmer Calibrator Test Drawer Test 3.2.7.1 Select punched cards (25-26645-128 thru 25-26645-157) 3.2.8.2 Follow paragraphs 3.1.2.1 thru 3.1.2.5 of this volume, starting with card 25-26645-128. NOTE: For more detail on function of these cards, see D2-12636, Volume V. (1) 25-26645-128 (2) -129 (3) -130 (4) -131 (5) -132 (6) 25-26645-133	GO lamp ON GO lamp ON	OK OK	

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.7.2 (Continued) (7) 25-26645-134 (8) -135 (9) -136 (10) -137 (11) -138 (12) -139 (13) -140 (14) -141 (15) -142 (16) -143 (17) -144 (18) -145 (19) -146 (20) -147 (21) -148 (22) 25-26645-149	GO lamp ON. ↗ ↘ GO lamp OH.	OK ↗ ↘ OK	

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U.S. 4200 2000 (Rev. 4-13-10)

REPLACING

VOL 2

NO T2-2548

SEC. 1

PAGE 41

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.7.2 (Continued) (23) 25-26645-150 (24) -151 (25) -152 (26) -153 (27) -154 (28) -155 (29) -156 (30) 25-26645-157 3.2.8.0 Monitor Launcher Missile Status Drawer Test	GO lamp ON. OK	OK	
3.2.8.1 Select punched cards (25-26646-137 thru 25-26646-172) 3.2.8.2 Follow paragraphs 3.1.2.1 thru 3.1.2.5 of this volume, starting with card 25-26646-137.	GO lamp ON..	OK	

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.8.2 (Continued) (1) 25-26646-137 (2) -138 OMIT (3) -139 (4) -140 (5) -141 (6) -142 (7) -143 (8) -144 (9) -145 (10) -146 (11) -147 OMIT (12) -148 (13) -149 OMIT (14) -150 (15) -151 OMIT (16) -152 OMIT (17) 25-26646-153	GO lamp ON A GO lamp ON	OK A OK	

REVISED
 03 0000 0000 (PAC 4131D)

VOL 2
 SEC 1
 NO T2-2548
 PAGE 43

REVISED
 01 4750 2000 (PAC 4131D)

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.8.2 (Continued)			
(18) 25-26646-154	GO lamp ON.	OK	
(19) -155			
(20) -156			
(21) -157			
(22) -158			
(23) -159			
(24) -160			
(25) -161 OMIT			
(26) -162			
(27) -163			
(28) -164			
(29) -165			
(30) -166			
(31) -167			
(32) -168			
(33) -169			
(34) 25-26646-170	GO lamp ON	OK	

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.8.2 (Continued)			
(35) 25-26646-170	GO lamp ON.	OK	
(36) 25-26646-171	GO lamp ON.	OK	
(37) 25-26646-172	GO lamp ON.	OK	
3.3.8.3			
Test Complete.			

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U3 4760 2000 (WAS BAC 4131D)

VOL 2 NO T2-2548
 SEC. 1 PAGE 45

4.0.0

CONCLUSION

4.0.1

The EDIT II-1 tests demonstrated that the Programmer Group and Programmer Group Test Set are physically, functionally, and electrically compatible. The problems noted in the summary of this report, while not serious, have been corrected.

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U3 4288 2000

BOEING

VOL 2

NO. T2-2548

SEC. 1

PAGE 46

THE BOEING COMPANY

NUMBER T2-2548 MODEL NO. WS-133A

TITLE Engineering Development Integration Test No. Two

(EDIT II-1) - Test Log

2-5142

SECTION TITLE PAGE U3 4288 0000 REV. 2/61

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5/4/62

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5/14/62

RELIABILITY
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5/14/62

(DATE)

AF 04(748)-289

CONTRACT NO.

5-78105-8640-68956

CHARGE NUMBER

VOL. 2
SEC. 2

NO. T2-2548
PAGE 1 OF 25

1.0.0

MANUFACTURING & INSPECTION RECORD - TEST LOG

1.0.1

The M&IR Test Log shows the test results recorded during EDIT II-1. The compatibility of the P/G and P/G Test Set was satisfactorily demonstrated on 30 March 1962, with representatives of BSD/STL attending the demonstration. Boeing Quality Control verification was not required for the Test Log.

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U3 4268 2000

BOEING

VOL. 2

NO. T2-2548

SEC. 2

PAGE 2

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[illegible]

[illegible]

[illegible]

[illegible]

[illegible]

57

[illegible]

96

TEL. NUMBER		UNIT SERIAL NUMBER		TEST PLANNING		MANUFACTURING AND INSPECTION RECORD				PAGE		
EDIT II-1		P/G-0000001		BY DATE		TEST LOG				OF		
D2-13308 Vol. 2		P/G-Test Set		WORK ORDER NO.		AUTHORITY				PAGE SER. NO.		
WS 133A		002		78105		EWA 08-956				DATE		
TEST ITEM		SER. NO.		WORK ORDER NO.		PLANNING BUDGET				VERIFICATION		
CONDITION NO.		WORK ITEM NO.				REF E. R.				OPERATION COMPLETION		
TIME						SER. NO.				SHOP INSP. ENGR.		
1130				3.2.5.0A Timer Sequential Drawer Test								
				page 6 (Completed) OK								
				Note: Formerly Launch Drawer								
				No. 1 Test.								
				Note: Cards -120, -121, -126, -127,								
				-128 and -137 resulted in								
				G.O's. Trouble was of								
				voltage tests which have								
				been recalibrated. page 10								
1145				3.2.6.0A Programmer Launch Sequence								
				Drawer Test page 6 (Completed)								
				OK.								
				Note: Formerly Launch								
				Drawer No. 2 Test.								
				Note: Card -155 was re-								
				programmed at result								
				in a G.O.								
PAGE		JOB NUMBER		PART NUMBER		NOMENCLATURE		LEAD INSP.		INSP. REG.		
OF		61347		25-22036-70		Programmer		STAMP		STAMP		
		U3 4400 5605 ORIG. 5/60		25-20278-2		P/G Test Set						

15

MANUFACTURING AND INSPECTION RECORD										PAGE 1 OF 2	
TEST PLAN		UNIT SERIAL NUMBER		TEST LOG		PLANNING BUDGET VERIFICATION		DATE			
BY	DATE	P/G 0000001	P/G Test Set	78105	EWA 08-956						
TEST ITEM		WORK ORDER NO.		AUTHORITY		REF E. R. SER. NO.		OPERATION COMPLETION			
TIME	CONDITION NO.	WORK ITEM SER. NO.					SHOP	INSP.	ENGR.		
1200			3.2.7.0A Programmer Calibrator Test								DM
			Drawer Test (page 7) (Completed)								
			OK								
			Note: Formerly Sequence Drawer Test								
			Note: Cards resulting in No-G's were changed to G's by re-calibration of the P/G Test Set (page 10)								
1230			3.2.8.0A Monitor Launcher Missile								DM
			Status Drawer Test. (page 7) (Completed) OK								
			Note: Formerly Monitor Drawer Test								
			Note: Cards resulting in No-G's were changed to G's by re-calibration (page 10)								

PAGE 1 OF 2		JOB NUMBER		PART NUMBER		NOMENCLATURE		LEAD INSP. STAMP		INSPI. REG. STAMP		CUS.	
61347		25-22036-70		25-29278-2		Programmer (P/G)							
U3 4400 5605 ORIG. 6/60						P/G Test Set							

10

MODEL NO.		TEST ITEM		UNIT SERIAL NUMBER		TEST PLAN		MANUFACTURING AND INSPECTION RECORD		PAGE	
WS133A		EDIT II-1		P/G 00000001		BY DATE		TEST LOG		PAGE OF	
D2-13308 Vol: 2				P/G Test Set				AUTHORITY		DATE	
SER. NO.		WORK ORDER NO.		78105		EWA 08-956		PLANNING BUDGET		3-20-6	
CONDITION NO.		WORK ITEM NO.		REF. E. R. SER. NO.		OPERATION COMPLETION		INSP. DATE		ENGR. DATE	
TIME											

TIME	CONDITION NO.	WORK ITEM NO.	DESCRIPTION	STATUS	DATE	ENGR.
1300			3.2.3.0C (Completed)	OK		DM
1330			3.2.4.0C (Completed)	OK		DM
1345			3.2.5.0C (Completed)	OK		DM
1400			3.2.6.0C (Completed)	OK		DM
1415			3.2.7.0C (Completed)	OK		DM
1430			3.2.8.0C (Completed)	OK		DM
1500			3.2.3.0D (Completed)	OK		DM
1515			3.2.4.0D (Completed)	OK		DM
1530			3.2.5.0D (Completed)	OK		DM

PAGE 61347

JOB NUMBER		PART NUMBER		NOMENCLATURE		LEAD INSP.		INSP. REG.		CUS.	
61347		25-22036-70		Programmer Group		STAMP DATE		STAMP DATE			
U3 4400 5505 ORIG. 6/60		25-29278-2		P/G Test Set							

TEST NUMBER	UNIT SERIAL NUMBER	TEST PLANNING BY DATE	MANUFACTURING AND INSPECTION RECORD	PAGE OF	1
EDIT II-1	P/G-0000001				
D2-13308 Vol. 2	P/G Test Set 002		TEST LOG	PAGE SER. NO.	2

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PAGE	JOB NUMBER	PART NUMBER	NOMENCLATURE	LEAD INSP.	INSP. REG.
OF	61347	25-22036-70	Programmer Group	STAMP	DATE
		25-29278-2	PIC (PIC)		
			PIC Test Set		

89-

TEST NUMBER		UNIT SERIAL NUMBER		TEST PLANNING		MANUFACTURING AND INSPECTION RECORD		PAGE	
EDIT II-1		P/G 0000001		BY DATE		TEST 100		OF	
D2-13308 U012		P/G Test Set				TEST 100		2	
MODEL NO.		TEST ITEM		WORK ORDER NO.		AUTHORITY		DATE	
WS 133A						EWA 08-956		3/21/11	
CONDITION NO.		WORK ITEM NO.		REF E. R. SER. NO.		OPERATION COMPLETION		ENGR.	
TIME									
0800		3.2.3.0 F	(Completed) OK						DM
0815		3.2.4.0 F	(Completed) OK						DM
0830		3.2.5.0 F	(Completed) OK						DM
0900		3.2.6.0 F	(Completed) OK						DM
0915		3.2.7.0 F	(Completed) OK						DM
1000		3.2.8.0 F	(Completed) OK						DM
1100	Vol. 2 Sec. 2	3.2.3.0 F	(Completed) OK						DM
1115	T2-2546 Page 19	3.2.4.0 F	(Completed) OK						DM
1130		3.2.5.0 F	(Completed) OK						DM

JOB NUMBER		PART NUMBER		NOMENCLATURE		LEAD INSP. REG.	
61347		25-22036-70		Programmer (P/G)		INSP. REG.	
J3 4400 5603 ORIG. 6/60		25-29278-2		P/G Test Set		STAMP DATE	
PAGE	OF						

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9-

TEST NUMBER		UNIT SERIAL NUMBER	TEST PLANNING	MANUFACTURING AND INSPECTION RECORD		PAGE
EDIT II-1		P/G 0000001	BT	TEST LOG		OF
D2-13308 Vol. 2		P/G-Test Set 002	DATE	AUTHORITY		DATE
MODEL NO. WS 133A		WORK ORDER NO.	78105	EWA 08-956		3/22/
TIME	CONDITION NO.	WORK ITEM NO.	TEST ITEM	REF. E. R. SER. NO.	OPERATION COMPLETION	ENGR.
					SHOP	INSP.
1200			3.2.5.0 H (Completed)			DM
1215			3.2.6.0 H (Completed)			DM
1230			3.2.7.0 H (Completed)			DM
1300			3.2.8.0 H (Completed)			DM
1330			3.2.3.0 I (Completed)			DM
1345			3.2.4.0 I (Completed)			DM
1400			3.2.5.0 I (Completed)			DM
1415			3.2.6.0 I (Completed)			DM
1430			3.2.7.0 I (Completed)			DM

PAGE	JOB NUMBER	PART NUMBER	NOMENCLATURE	LEAD INSP.	INSP. REG.
OF				STAMP	DATE
	61347	25-22036-70	Programmer		
		25-29278-2	P/G Test Set		

U3 4400 5605 ORIG. 6/50

[illegible]

[illegible]

T. NUMBER EDIT II-1	UNIT SERIAL NUMBER P/G 0000001	TEST PLANNING		MANUFACTURING AND INSPECTION RECORD	PAGE OF	2
		BY	DATE			
D2-13308 Vol.2	P/G TEST SET 002			TEST LOG	PAGE SER. NO.	2

[illegible]

PAGE	JOB NUMBER 61347	PART NUMBER 25-22036-70 25-29278-2	NOMENCLATURE PROGRAMMER GROUP (P/G) P/G TEST SET	LEAD INSP. STAMP DATE	INSP. REG. STAMP DATE	CUS.
OF						

THE BOEING COMPANY

NUMBER T2-2548 MODEL NO. VS-133A
 TITLE Engineering Development Integration Test No. Two
(EDIT II-1) - Photographs

2-5142

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SECTION TITLE PAGE U3 4238 0000 REV. 2/61

AF 04(647)-289
 CONTRACT NO.

5-78105-8640-68956
 CHARGE NUMBER

VOL. 2
 SEC. 3

NO. T2-2548
 PAGE 1 OF 4

1.0.0

PHOTOGRAPHS

1.0.1

The photographs which follow show the P/G and P/G Test Set.

Photograph two shows the Test Set connected to the P/G. The

Test Set is in two parts, the power supply for the Test Set

being a separate unit. The power supply, shown in photograph

two, to the left of the P/G and P/G Test Set, is also used in

the P/G Test Set - self-test.

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U3 4288 2000

BOEING

VOL

2

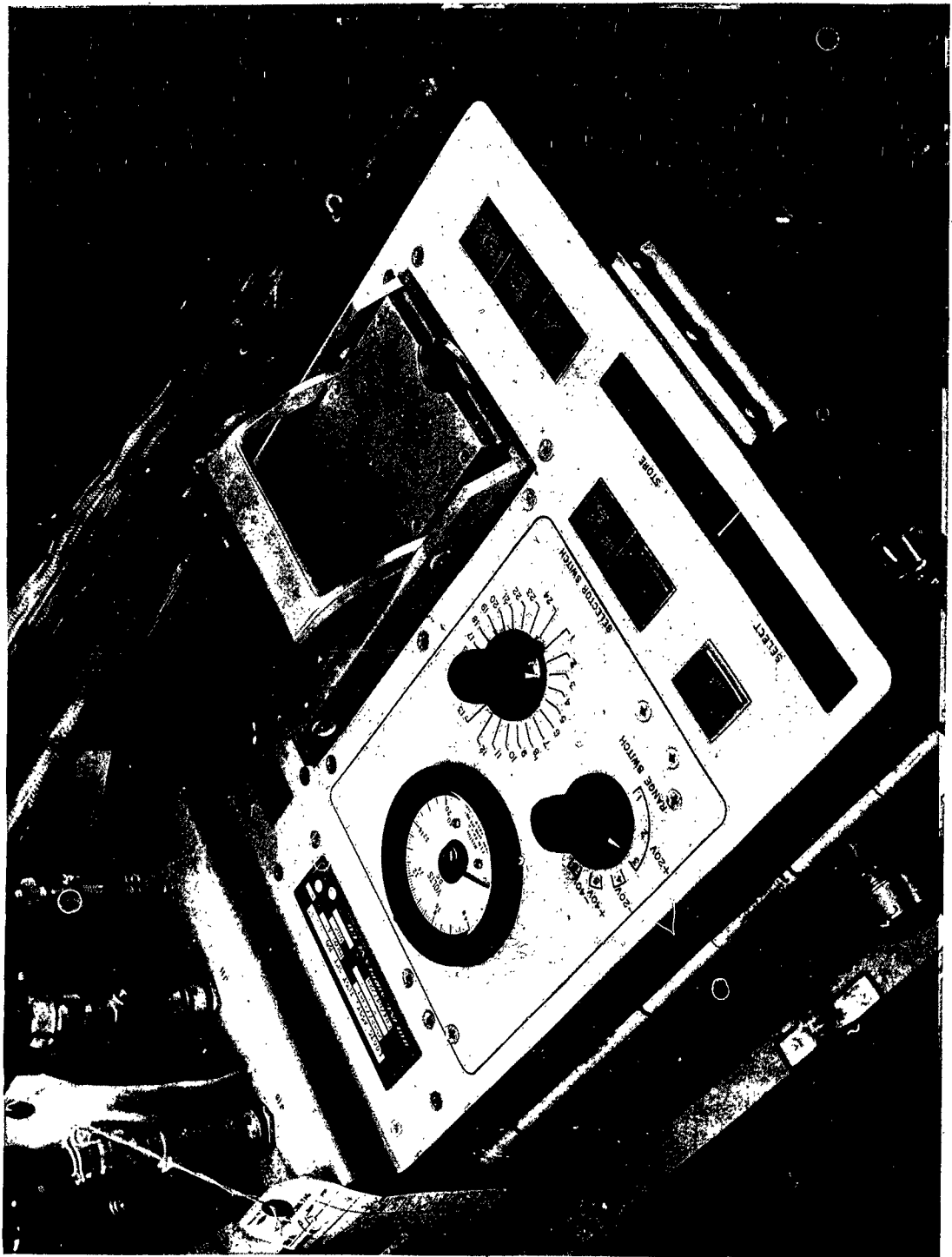
NO. T2-2548

SEC.

3

PAGE 2

AN ENGINEER DESIGN EDIT INTERGRATION 2A106465
 EDIT TEST EQUIPMENT - 4-3-62



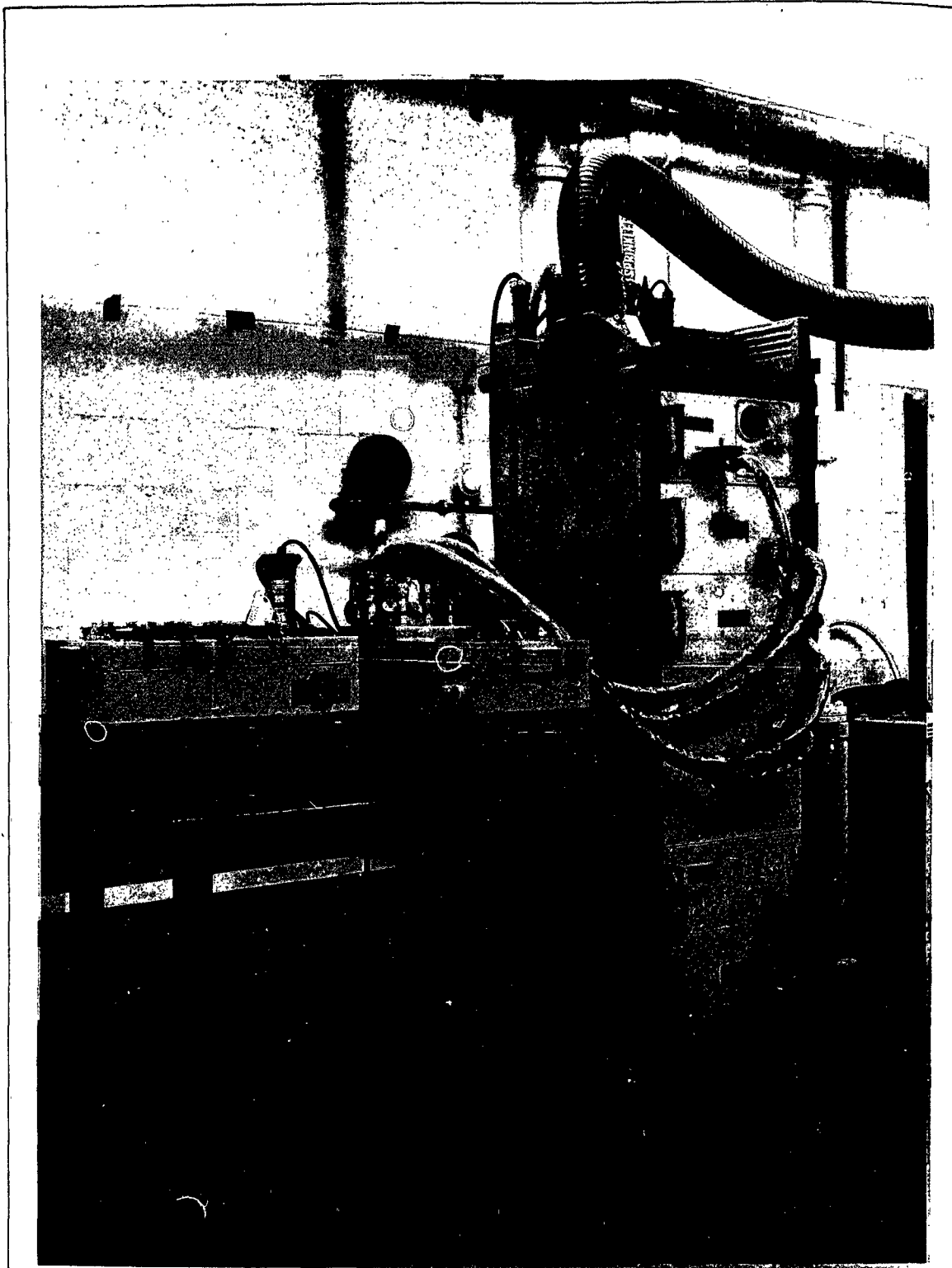
PROGRAMMER GROUP TEST SET (PORTABLE) FIG. A 3092

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U3 4288 2000

BOEING	VOL. 2	NO T2-25-48
	SEC. 3	PAGE 3

MINUTEMAN
 TEST - ED-1, TEST EQUIPMENT - 4-3-62
 ZERO DESIGN EDIT INTEGRATION 2A100463



PROGRAMMER GROUP TEST SET & PROGRAMMER GROUP

67

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U3 4288 2000

BOEING

VOL. 2

NO T2-2548

SEC. 3

PAGE 4



THE BOEING COMPANY

NUMBER T2-2548 MODEL NO. WS133A

TITLE Engineering Development Integration

Test No. Two-B (EDIT II-1B) Report

2-5142

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SECTION: TITLE PAGE U3 4288 0000 REV. 2/61

AF 04(647)-289
CONTRACT NO.

5-78105-8640-68956
CHARGE NUMBER

2/22/3

VOL. 2 NO. T2-2548
SEC. 4 PAGE 1 OF 43

1.0

PURPOSE

This section presents the test report of the Engineering Development Integration Test number two on Block Change -1 equipment of Operational Configuration -- EDIT II-1B. The test was conducted in accordance with the Test Procedures Document D2-13308, Volume 2, and the EDIT Program Plan D2-13307.

1.1

EQUIPMENT IN TEST

Programmer Group, Fig. A 1201, Ser. No. 0000034

Programmer Group Test Set, Fig. A 3092, Ser. No. 0003

Dummy Decoder Relay Assembly, Fig. A 3113, Ser. No. 0002

Power Supply, Fig. A 4523, Ser. No. 0000001

2.0

SUMMARY

2.1

The Self-Test of the Programmer Group (P/G) Test Set was completed in accordance with paragraph 3.1.0. The tolerances were increased for the voltage check of paragraph 3.1.3.2 (3) at Selector Switch 15 and 18 to allow for diode voltage drops.

Compatibility tests between the P/G and the P/G Test Set had continuous erratic NO-GO's on different cards. Majority of the times the first check would be a NO-GO and additional test gave GO's by opening and closing the Card Reader. All equipment was checked for configuration and functional tests. The Programmer Group had to be functional tested in the -89 configuration, and the P/G Test Set had calibration certification. Erratic NO-GO's

continued after these tests were completed. These conditions were more prevalent on the End-to-End tests than on individual drawer tests. Additional checks were made on the P/G Test Set and it was found that the trouble was in the Card Reader Assembly. A new assembly that passed functional test was installed in the P/G Test Set. The End-to-End test was performed and all was GO. The complete EDIT II-1 test was performed with all tests GO. The following day we ran through the End-to-End test (60 cards) eight times to obtain performance data on the new Card Reader Assembly. There were three NO-GO's during this test.

No formal demonstration was held for this test by direction of BSD/STL representative. The EDIT II-1 test in March 1962 demonstrated to the customer and BSD/STL that the equipment is compatible.

3.0 REPORT

3.0.1 The EDIT II-1B test was performed according to the Test Procedure Document D2-13308, Volume 2. These procedures and the recorded results are given in paragraph 3.1.0.0 through 3.3.8.3 which follow.

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.0.0 <u>P/G TEST SET SELF-TEST PROCEDURES</u> 3.1.1.0 Test Set-Up 3.1.1.1 The P/G Test Set must be isolated from all electronic equipment with the exception of the Test Set Power case. 3.1.1.2 Connect the Fig. A 4523 Distribution Box, and P/G Test Set, as shown in Figure 3.1.1.2-4.		OK	

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 U3 4288 2000 (WAS MAC 41310)

ENDING VOL 2 NO T2-2548
 SEC 4 PAGE 4

Fault Locator

Distribution Box

Part of Fig. A 3092

Part of Fig. A 3092

J1	P1	W1	P32	A2J1
J2	P2	W2	P31	A1J1
J3	P3	W3	P27	J7
J4	P4	W4	P29	J9
J6	P6	W6	P34	A4J1
J7	P7	W7	P33	A3J1
J8	P8	W8	P28	J8
J9	P9	W9	P30	J10
J10	P10	W10	P23	J3
J11	P11	W11	P37	A7J1
J12	P12	W12	P36	A6J1
J13	P13	W13	P21	J1
J14	P14	W14	P35	P2
J15	P15	W15	P24	J4
J18	P18	W18	P22	J2
J19	P19	W19	P25	J5

Power Supply

P5	J5	W5	P26	J4
	120V AC	W16	P20	J5

Fig. A 4623

Self-Test of P/G Test Set Connection

Figure 3.1.1.2-4

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2/22/63

US 4255 2000

BOEING

VOL

2

NO T2-2548

SEC.

4

PAGE

5

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.1.3 Connect power cable to 120 volt, 60 cps outlet. 3.1.1.4 Press the TEST SET POWER ON switch on the Test Set. NOTE: The Test Set must be ON for 30 seconds before running any card tests. 3.1.1.5 Press the P/G POWER ON switch.	TEST SET POWER ON lamp turns ON. The AFS ALARM and W/H ALARM lamps turn ON. The WARNING lamp starts flashing at 4 cps. These conditions exist until the 13th card. P/G POWER ON lamp ON.	OK OK OK OK	

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U3 4286 2000 (WAS BAC 4131D)

BOEING VOL 2 NO T2-2548
SEC. 4 PAGE 6

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.2.0 <u>CARD TESTS</u> 3.1.2.1 Remove program cards from the card container as required by the procedures and insert in UNTESTED CARDS slot of Test Set. 3.1.2.2 Insert selected punched cards, as called out by this procedure; in the Test Set CARD READER face up and beveled corner out. Pull CARD READER HANDLE fully back to engage electrical contacts.			

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3/22/63

US 4286 2000 (WAS BAC 4131D)

~~BOEING~~

VOL 2

NO T2-2548

SEC. 4

PAGE 7

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.2.3 Push START TEST button on the Test Set. <u>NOTE:</u> The START TEST switch must be held down until the START TEST lamp turns ON.	START TEST lamp ON.	OK	
3.1.2.4 GO or NO-GO lamp will illuminate and the TEST IN PROCESS lamp will go OFF. <u>NOTE:</u> Additional instructions, when needed, are given for each card test.	TEST IN PROCESS lamp OFF. GO or NO-GO lamp ON.	OK OK	
3.1.2.5 Record each test result in appropriate column allocated for each card or voltage test in this document.			

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 US 4286 2000 (WAS BAC 4131D)

ENDING

VOL	2	NO	T2-2548
SEC.	4	PAGE	8

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.2.6 Push card reader handle, remove card and place it in the TESTED CARD slot. 3.1.2.7 Repeat paragraphs 3.1.2.1 thru 3.1.2.6 for each succeeding test. 3.1.3.0 <u>SELF-TEST</u> 3.1.3.1 Select punched cards (25-30947-125 thru 25-30947-147) for self-test of the Test Set.			

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2/22/63

U3 4288 2000 (WAS BAC 4131D)

ENDING

VOL 2.

NO T2-2548

SEC. 4.

PAGE 9

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.3.2 Follow paragraphs 3.1.2.1 thru 3.1.2.7 starting with card 25-30947-125. (1) Card 25-30947-125. After the GO lamp is ON, set the Range Switch at 3, and rotate the Selector Switch successively through the positions indicated in Table 3.1.3.2-4. Read the voltmeter at each position of the Selector Switch. (2) Card 25-30947-126. Follow same procedure as number 1 above.	GO lamp ON. The voltmeter shall read 10 ± 0.5 volts at each setting of the Selector Switch. NO-GO lamp ON. The voltmeter shall read 10 ± 0.5 volts at each setting of the Selector Switch except at position 14 which read 0 ± 0.5 volts.	OK See Table 3.1.3.2-4 OK See Table 3.1.3.2-4	

REVISED

2/22/63

U3 4288 2000 (WAS SAC 41310)

BOEING

VOL

2

NO. T2-2548

SFC

4

PAGE

10

Table 3.1.3.2-4 Voltmeter Switching Requirements Self-Test Procedure							
Selector Switch Position	Card-125 Accept. Volts	Card-125 Volts Read	Card-126 Accept. Volts	Card-126 Volts Read	Card-127 Accept. Volts	Card-127 Volts Read	Card-128 Accept. Volts
1	$10 \pm 0.5V$	$10.0V$	$10 \pm 0.5V$	$10.0V$	$10 \pm 0.5V$	$10.0V$	
2							
3							
4							
5							
6							
7							
8							
9							
10							
11	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$
12							
13	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$
14							
15	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$
16							
17	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$
18	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$	$10.0V$	$10 \pm 0.5 V$

REVISED

3/22/63

DOING

VOL 2
SER 1

NO. T2-2548
PAGE 11

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.3.2 (Continued) (3) Card 25-30947-127. Follow same procedure as number (1) above.	GO lamp ON. The voltmeter shall read 10 ± 0.5 volts on selector switch positions 1 thru 13. Position 15 & 18 shall read 10 ± 0.5 V 1.1	OK See Table 3.1.3.2-4	
(4) Card 25-30947-128. Follow same procedure as number (1) above.	NO-GO lamp ON. The voltmeter shall read 10 ± 0.5 volts at each setting of the Selector Switch. GO lamp ON	OK OK See Table 3.1.3.2-4	
(5) Card 25-30947-129. <u>NOTE:</u> This test requires 66 seconds for completion.		OK	
(6) Card 25-30947-130	GO lamp ON.	OK	

REVISED 2/22/63
U3 4288 2000 (WAS BAC 4131D)

ENGINE VOL 2 NO T2-25AA
SEC. 4 PAGE 12

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.3.2 (Continued)			
(7) Card 25-30947-131	NO-GO lamp ON.	OK	
(8) Card 25-30947-132	GO lamp ON.	OK	
(9) Card 25-30947-133	GO lamp ON.	OK	
(10) Card 25-30947-134	NO-GO lamp ON.	OK	
(11) Card 25-30947-135	GO lamp ON. The stepping switch will scan 4 times during test with an audible pause between scans.	OK	
Repeat test but this time push the P/G POWER OFF switch during first scan of stepping switch.	NO-GO lamp ON.	OK	

REVISED 2/22/63
 U3 4266 2000 (WAS BAC 41310)

BEING VOL 2 NO T2-2548
 SEC. 4 PAGE 13

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.3.2 (Continued)			
(12) Card 25-30947-136	GO lamp ON.	OK	
(13) Card 25-30947-137.	GO lamp ON.	OK	
Repeat test, but this time push the P/G POWER OFF switch during test.	NO-GO lamp ON. The W/H ALARM, AFS ALARM and WARNING lamps will turn OFF when the P/G POWER OFF switch is depressed. The lamps will turn back ON when switch is released.	OK	
(14) Card 25-30947-138.	GO lamp ON. The AFS ALARM lamp turns OFF during test.	OK	

REVISED

9/22/63

U3 4286 2000 (WAS BAC 41310)

~~NOFIND~~

VOL 2
SEC. 4

NO T2-2548
PAGE 14

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.1.3.2 (Continued) (15) Card 25-30947-139. Initiate test and observe the START TEST, AFS SAFETY, W/H ALARM, and WARNING lamps.	The following events take place: A. Shortly after pressing the START TEST switch: (1) The AFS ALARM lamp turns OFF momentarily. (2) The START TEST lamp turns ON. B. After the START TEST lamps turn ON: (1) The W/H ALARM turns OFF. (2) The WARNING lamp starts flashing at 25% duty cycle.	OK OK OK OK	

REVISED

2/22/63

US 4200 2000 (WAS DAC 4131D)

RECEIVED

VOL

2

NO. T2-2548

SEC

4

PAGE 15

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
(18) Card 25-30947-142	GO lamp ON.	OK	
(19) Card 25-30947-143	GO lamp ON.	OK	
(20) Card 25-30947-144	GO lamp ON. The W/H ALARM, AFS ALARM and WARNING lamps will turn OFF when the card reader handle is operated UP. The START TEST lamp turns ON. When the step- ping switch starts its scan, the lamps will turn back ON. AFS alarm OFF.	OK OK OK OK OK OK OK OK OK OK	
(21) Card 25-30947-145	GO lamp ON.		
(22) Card 25-30947-146			
(23) Card 25-30947-147	GO lamp ON.	OK	

61

REVISED 2/22/63
US 4266 2000 (17AS BAC 4131D)

REINO VOL 2 NO T2-2548
SEC 1 PAGE 17



PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.0.0 <u>COMPATIBILITY TEST OF P/G WITH THE</u> <u>P/G TEST SET.</u> 3.2.1.0 Test Set-Up. 3.2.1.1 The P/G must be isolated from all electronic equipment with the exception of its SOURCE POWER SUPPLY and associated P/G Test Set equipment. 3.2.1.2 Apply cooling air to P/G (Ref. 2.1.2). 3.2.1.3 Make the cable connections shown in Figure 3.2.1.3-4.			

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2/22/63

U3 4286 2000 (WAS SAC 41310)

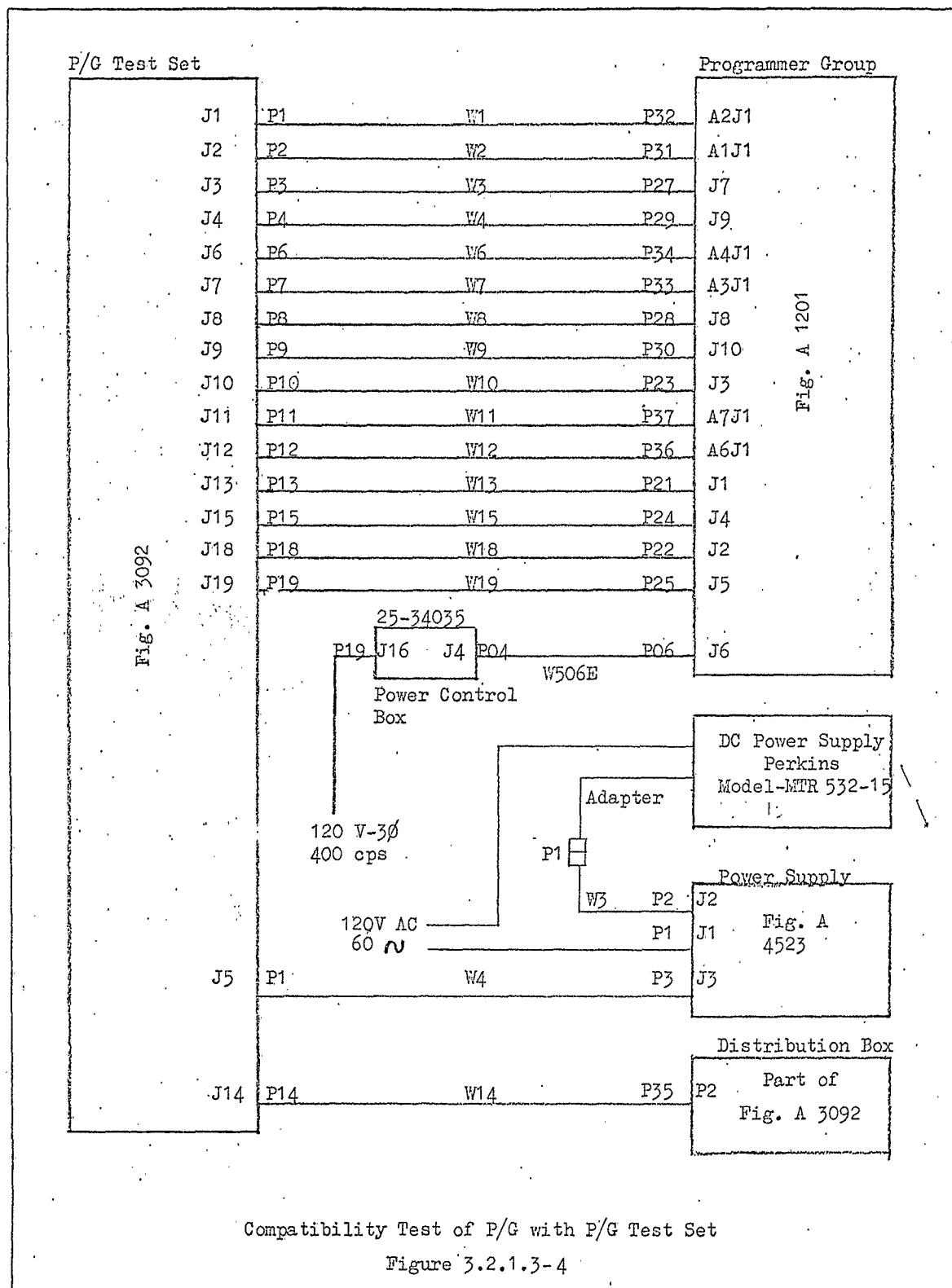
REVISIONS

VOL 2

NO. T2-2548

SEC 4

PAGE 18



U3 4288 2000 REV. 8/62

2-5142-2

REV SYM 3/22/63

BOEING NO. T2-2548, Vol. 2
SECT. 4 PAGE 19

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.1.4 The Dummy Decoder is inserted into Launch Drawer No. 1 of the P/G. 3.2.1.5 Press the TEST SET POWER ON switch. <u>NOTE:</u> The MONITOR POWER ON lamp on the P/G will have been ON continuously during preceding tests. It will remain On until the main LF circuit breaker is tripped OFF to replace a faulty P/G drawer.	TEST SET POWER ON lamp ON.	OK	

REVISED 2/22/63
 U3 4260 2000 (WAS BAC 4131D)

BOEING VOL 2 NO T2-2548
 SEC. 4 PAGE 20

64

REVISED 3/22/63
 U3 4268 2000 (WAS BAC 4131D)

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.2.0 <u>VOLTAGE LEVEL CHECK</u> 3.2.2.1 P/G Monitor Power ON Only 3.2.2.2 Measure the voltages at the Range and Selector positions on the Test Set as specified in Table 3.2.2.2-4. Record the results of each test on specified Table.	See Table 3.2.2.2-4.		
3.2.2.3 Press the P/G POWER ON switch.	P/G POWER ON lamps ON.	OK	
3.2.2.4 Measure the voltages at the Range and Selector positions on the Test Set as specified in Table 3.2.2.4-4. Record the results of each test on specified table.	See Table 3.2.2.4-4.		

RANGE												
+ 20 Volts DC						-20 V DC		+ 40 VDC			- 40 VDC	
1	1	2	2	3	3	4	4	5	5	6	6	6
Accept Volts	Volts Read	Accept Volts	Volts Read	Accept Volts	Volts Read	Accept Volts	Volts Read	Accept Volts	Volts Read	Accept Volts	Volts Read	Volts Read
1	0 to 2	0	0	0 to 2	0	0 to 2	0	0 to 5	0.8	0 to 5	0.5	
2	0 to 0.5	0.25	0	0 to 2	0	0 to 0.5	0	28 ± 1	28			
3	10 ± 0.5	10.0	0	0 to 2	0	10 ± 0.5	10.1	0 to 0.5	0			
4	0 to 0.5	0.25	0	0 to 2	0	0 to 0.5	0	28 ± 1	28			
5	10 ± 0.5	10.0	0	0 to 2	0	10 ± 0.5	10.0	0 to 0.5	0			
6	0 to 0.5	0	0	0 to 2	0	0 to 0.5	0	0 to 0.5	0			
7	10 ± 0.5	10.1	0	0 to 2	0	10 ± 0.5	10.3	28 ± 1	28.5			
8	0 to 0.5	0	0	0 to 2	0	0 to 0.5	0	0 to 0.5	0			
9	10 ± 0.5	10.0	0	0 to 2	0	10 ± 0.5	10.0	28 ± 1	27.5			
10	10 ± 0.5	10.0	0	0 to 2	0	0 to 0.5	0	0 to 0.5	0			
11	0 to 0.5	0	0	0 to 2	0	10 ± 0.5	10.2	28 ± 1	28.1			
12	11.5 ± 0.5	11.7	0	0 to 2	0	17.5 ± 2.5	17.5	28 ± 1	28.0			
13	17 ± 2	17.5	0	0 to 2	0	17.5 ± 2.5	17.7	36 ± 3	35.0			
14	17 ± 2	17.5	0	0 to 2	0			36 ± 3	35.1			
15	17 ± 2	17.5	0	0 to 2	0							
16	17 ± 2	17.5	0	0 to 2	0							
17												
18												

Table 3.2.2.2-4 P/G Monitor Power ON Voltage Check

REVISED

U3 4266 2000

BOEING

VOL 2
SEC. 4

NO T2-2548
PAGE 22

RANGE											
+ 20 Volts DC						-20 VDC		+ 40 VDC		- 40 VDC	
1	1	2	2	3	3	4	4	5	5	6	6
Accept Volts	Volts Read	Accept Volts	Volts Read	Accept Volts	Volts Read	Accept Volts	Volts Read	Accept Volts	Volts Read	Accept Volts	Volts Read
10 ± 0.5	10.0	10.5±0.5	10.4	10.5±0.5	10.5	10.5±0.5	10.6	28 ± 1	28.0	30 ± 1	30.0
↑	Δ	↑	Δ	↑	Δ	↑	Δ	↑	Δ		

REVISÉD

2/22/63

U3 4288 2000

ENDING

1 VOL 2.

1 NO T2-2548

SEC. 4

PAGE - 23

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.3.0 <u>PHASE SUPPLY VOLTAGE TEST</u> 3.2.3.1 Select punched cards (25-31059-106 thru 25-31059-110) to check the phase supply voltage in each of the five drawers. 3.2.3.2 Follow paragraphs 3.1.2.1 thru 3.1.2.7 of this volume starting with card 25-31059-106. <u>NOTE:</u> For more detail on function of these cards, see D2-12636, Volume I. (1) 25-31059-106 <u>NOTE:</u> This test shall be performed prior to any other test.	GO lamp ON	OK	

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
(2) 25-31059-107		OK	
(3) 25-31059-108		OK	
(4) 25-31059-109		OK	
(5) 25-31059-110		OK	
3.2.4.0			
<u>END TO END TEST</u>			
3.2.4.1			
Select punched cards (25-26642-165 thru 25-26642-232) for End-to-End Test.			

REVISED 2/22/63
 US 4288 2000 (WAS BAC 4131D)

<u>DOING</u>	VOL 2	NO T2-2548
	SEC 4	PAGE 25

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.4.2 Follow paragraphs 3.1.2.1 thru 3.1.2.7 of this volume and additional instructions stated in the procedure for each card, starting with card 25-26642-165. <u>NOTE:</u> For more detail on function of these cards, see D2-12636, Volume II. (1) 25-26642-165 (2) 25-26642-166 (3) -168 (4) -169 (5) -170 (6) -171 (7) -172 (8) -173 (9) -174 (10) 25-26642-175	GO lamp ON ← → GO lamp ON	OK OK OK OK OK OK OK OK OK OK	

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.4.2 (Continued) (11) 25-26642-176 (12) -177 (13) -178 (14) -180 (15) -181 (16) -182 (17) -183 (18) -184 (19) -185 (20) -187 (21) -188 (22) -189 (23) -190 (24) -191 (25) -193 (26) 25-26642-194	GO Lamp ON.	OK	

REVISED 2/22/63
U3 4288 2000 (WAS BAC 4131D)

ENGINE VOL. 2 NO T2-2548
SEC. 4 PAGE 27

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.4.2 (Continued) (27) 25-26642-195 (28) -196 (29) -197 (30) -198 (31) -199 (32) -200 (33) -201 (34) -204 (35) -205 (36) -206 (37) -207 (38) -208 (39) -209 (40) -210 (41) -211 (42) -214 (43) 25-26642-215	GO Lamp ON.	OK	

REVISED

2/22/63

U3 4288 2000 (WAS BAC 4131D)

BOEING

VOL 2

NO T2-2548

SEC 4

PAGE 28

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.4.3 Card 25-26642-216			
3.2.4.4 Select punched card 25-26642-216			
3.2.4.5 Follow paragraph 3.4.2.2			
3.2.4.6 Wait 40 ± 2 minutes and then push START TEST button.	NO-GO lamp ON.	OK	
3.2.4.7 Open and close the card reader and wait another 10 ± 2 minutes; then push TEST START button.	GO lamp ON.	OK	

REVISED

9/22/63

U3 4288 2000 (WAS BAC 4131D)

DD FORM

VOL 2

NO T2-2518

SEC 4

PAGE 29

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.4.13 Card 25-26642-218. 3.2.4.14 Select punched card 25-26642-218. 3.2.4.15 Follow paragraph 3.1.2.2. 3.2.4.16 Depress the P/G POWER OFF switch of the Test Set to initiate shut down. <u>KEEP DEPRESSED FOR DURATION OF TEST.</u>			

REVISED

2/22/63

U3 4286 2000 (WAS BAC 4131D)

ENDING

VOL 2

NO T2-2548

SEC. 4

PAGE 31

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.4.17 Follow paragraphs 3.1.2.3 thru 3.1.2.6. <u>NOTE:</u> To start up after the foregoing test, depress P/G POWER ON switch.	GO lamp ON.	OK	
3.2.4.18 Select punched cards (25-26642-219 thru 25-26642-232).			
3.2.4.19 Follow paragraphs 3.1.2.1 thru 3.1.2.7 of this volume starting with card 25-26642-219. (1) 25-26642-219 (2) -220 (3) -221 (4) -222 (5) 25-26642-223	GO lamp ON. ↕ GO lamp ON.	OK ↕ OK	

REVISED

2/22/63

U.S. 4258 2000 (WAS BAC 4131D)

ENDING

VOL

2

NO T2-2548

SEC.

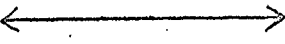
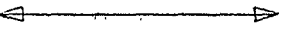
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PAGE 32

6.1

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.5.2 Follow paragraphs 3.1.2.1 thru 3.1.2.7 of this volume starting with card 25-26643-120. <u>NOTE:</u> For more detail on function of these cards, see D2-12636, Vol. III. (1) 25-26643-120 (2) -121 (3) -123 (4) -124 (5) -139 (6) -126 (7) -127 (8) -128 (9) -129 (10) -130 (11) 25-26643-131	GO lamp ON. GO lamp ON.	OK OK	

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PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.5.2 (Continued) (12) 25-26643-132 (13) -133 (14) -134 (15) -135 (16) -136 (17) -137 (18) 25-26643-138 3.2.6.0 <u>PROGRAMMER LAUNCH SEQUENCE DRAWER TEST</u> 3.2.6.1 Select punched cards (25-26644-128 thru 25-26644-164.) 3.2.6.2 Follow paragraphs 3.1.2.1 thru 3.1.2.7 of this volume starting with 25-26644-128.	GO lamp ON.  GO lamp ON.		

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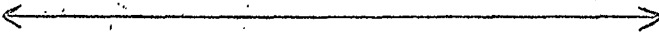
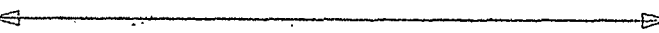
9/22/63

U3 4208 2000 (WAS BAC 41310)

DOING

VOL 2
SEC. 4

NO T2-2548
PAGE 35

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.6.2 (Continued) <u>NOTE:</u> For more detail on function of these cards, see D2-12636, Vol. IV. (1) 25-26644-128 (2) -129 (3) -130 (4) -131 (5) -132 (6) -133 (7) -134 (8) -135 (9) -136 (10) -137 (11) -139 (12) -140 (13) -142 (14) 25-26644-143	GO lamp ON. 		

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U3 4288 2000 (WAS BAC 4131D)

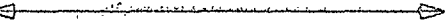
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VOL 2

NO T2-2548

SEC. 4

PAGE 36

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.6.2 (Continued) (15) 25-26644-145 (16) -156 (17) -157 (18) -158 (19) -159 (20) -160 (21) -161 (22) -162 (23) -163 (24) 25-26644-164	GO lamp ON. 	OK 	
3.2.7.0 <u>PROGRAMMER CALIBRATOR TEST DRAWER TEST</u> 3.2.7.1 Select punched cards (25-26645-128 thru 25-26645-157).			

REVISED

2/22/63

J3 4286 2000 (WAS GAC 4181D)

ENGINE

VOL 2

NO T2-2548

SEC. 4

PAGE 37

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.7.2 Follow paragraphs 3.1.2.1 thru 3.1.2.7 of this volume, starting with card 25-26645-128. <u>NOTE:</u> For more detail on function of these cards, see D2-12636, Vol. V. (1) 25-26645-128 (2) -129 (3) -130 (4) -131 (5) -132 (6) -133 (7) -134 (8) -135 (9) -136 (10) -137 (11) 25-26645-138	GO lamp ON GO lamp ON	OK	

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2/22/63

U3 4280 2000 (WAS BAC 4131D)

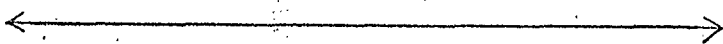
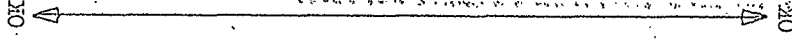
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VOL 2

NO T2-2548

SEC. 4

PAGE 38

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.7.2 (Continued) (12) 25-26645-139 (13) -140 (14) -141 (15) -142 (16) -143 (17) -144 (18) -145 (19) -146 (20) -147 (21) -148 (22) -149 (23) -150 (24) -151 (25) -152 (26) 25-26645-153	GO lamp ON.  GO lamp ON.		

REVISED 2/22/63
 U3 4266 2000 (WAS DAC 4131D)

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.7.2 (Continued) (27) 25-26645-154 (28) -155 (29) -156 (30) 25-26645-157 3.2.8.0 <u>MONITOR LAUNCHER MISSILE STATUS DRAVER TEST</u>	GO lamp ON. ↕ GO lamp ON.	OK ↕ OK	
3.2.8.1 Select punched cards (25-26646-137 thru 25-26646-172.) 3.2.8.2 Follow paragraphs 3.1.2.1 thru 3.1.2.7 of this volume, starting with card 25-26646-137.			

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2/22/63

U3 4286 2000 (WAS SAC 4131D)

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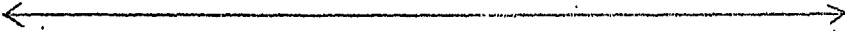
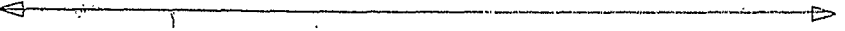
VOL 2

NO T2-2548

SEC. 4

PAGE

40

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.8.2 (Continued) (1) 25-26646-137 (2) -139 (3) -140 (4) -141 (5) -142 (6) -143 (7) -144 (8) -145 (9) -146 (10) -148 (11) -150 (12) -153 (13) -154 (14) -155 (15) -156 (16) -157 (17) 25-26646-158	GO lamp ON. 	OK 	

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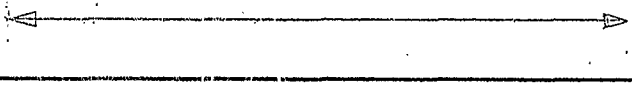
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VOL 2

NO T2-2548

SEC. 4

PAGE 4

PROCEDURE	ACCEPTABLE DATA	RECORD DATA	COMMENTS
3.2.8.2 (Continued) (18) 25-26646-159 (19) -160 (20) -162 (21) -163 (22) -164 (23) -165 (24) -166 (25) -167 (26) -168 (27) -169 (28) -170 (29) -171 (30) 25-26646-172 3.3.8.3 Test complete.	GO lamp ON.  GO lamp ON.	OK  OK	

REVISED 2/22/63
 U3 4288 2000 (WAS DAC 4131D)

~~BOEING~~ VOL 2 NO T2-2548
 SEC 4 PAGE 42

4.0

CONCLUSION

4.1

The EDIT II-1B test demonstrated that the operational configuration of the Programmer Group, P/G Test Set, Power Supply and Dummy Decoder Relay Assembly are physically, functionally, and electrically compatible. The problem discovered with the Card Reader Assembly in the P/G Test Set is being worked by the design group and packaging group. There are three areas of investigation: (1) Determine performance of the Test Sets delivered to operational sites. (2) Investigate materials and processes used on the Card Reader Assembly. (3) Evaluate the possibility of redesigning the Card Reader Assembly.

THE BOEING COMPANY

NUMBER T2-2548 MODEL NO. WS133A

TITLE Engineering Development Integration

Test No. Two B (EDIT II-1B) Test Log

2-5142

SECTION TITLE PAGE U3 4288 0000 REV. 2/61

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RELIABILITY
APPROVAL

(DATE)

AF 04(647)-289
CONTRACT NO.

5-78105-8640-68956
CHARGE NUMBER

2/22/63

VOL. 2	NO. T2-2548
SEC. 5	PAGE 1 OF 34

1.0

MANUFACTURING & INSPECTION RECORD - TEST LOG

The following M&IR Test Log shows the test results recorded during EDIT II-1B. This EDIT Test was completed on January 17, 1963. Boeing Quality Control verification was not required for the Test Log.

REV SYM

2/22/3

BOEING

NO. T2-2548, Vol. 2

SECT. 5

PAGE 2

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TEST NUMBER		UNIT SERIAL NUMBER		TEST PLANNING		WORK ORDER NO.		TEST LOG		PLANNING BUDGET		DATE	
EDIT II-13		D2-13308 Vol. 2		BY DATE		78105		EWA 8956		PAGE SER. NO.		12/11/5	
MODEL NO.	TEST ITEM	CONDITION NO.	WORK ITEM NO.	REF E. R. SER. NO.	SHOP	INSP.	ENGR.	OPERATION COMPLETION	DATE	TEST LOG	PLANNING BUDGET	DATE	PAGE OF
1415	Started Time Sequential Drawer Test												
	25-26643-120 thru -138												
	All GO's												
	Programmer Launch Sequence Drawer Test												
	One NO-GO; three repeats were GO												
	Programmer Calibrator Test Drawer Test												
	Two NO-GO's, three repeats on each card were GO												
	T2-2548, Vol. 2												
	Sec. 5 Page 5												

PAGE 3 OF 3

U3 4400 5535 ORIG. 6/60

JOB NUMBER

61347

PART NUMBER

COMPATIBILITY TEST

HOMECLATURE

COMPATIBILITY TEST

TEST NUMBER		UNIT SERIAL NUMBER		TEST PLANNING BY DATE		MANUFACTURING AND INSPECTION RECORD						PAGE OF
						TEST 106						
MODEL NO.		WORK ORDER NO.		AUTHORITY		PLANNING BUDGET VERIFICATION		PAGE SER. NO.		DATE		
TEST ITEM		SER. NO.		78105		EWA-8956				12/12/60		
CONDITION NO.	WORK ITEM NO.											
I.T.E.												
2830				Started Programmer Calibrator Test Drawer								
				Test 25-26646-137 thru -172								
				Card 25-26646-137 gave NO-GO								
				Two repeats gave GO								
				D2-13308, Vol. 2, Sec. 2								
				TP 3.2.4								
				NO-GO's that should have been GO.								
				Cards 25-26642-166								
				-								
				-171								
				-189								
				-193								
				-218								
				TP 3.2.5								
				Cards 25-26643-120 thru -138								
1130				All GO								

U3 4400 5505 ORIG. 6/60

TEST NUMBER		UNIT SERIAL NUMBER		TEST PLANNING		MANUFACTURING AND INSPECTION RECORD		PAGE OF
				BY	DATE	TEST LOG		3
MODEL NO.	TEST ITEM	WORK ORDER NO.	AUTHORITY	PLANNING BUDGET VERIFICATION	REF E.R. SER. NO.	SHOP INSP.	ENGR.	
NAME	CONDITION NO.	WORK T.I. NO.						DATE
D2-13308 Vol. 2	V/S133A		78105	EWA 8956				12/12/6
1300			TP 3.2.6	Cards 25-26644-128 thru -164				
		2/22/3	All - GO					NW
1400			TP 3.2.7	Cards 25-26645-128 thru -157				
				Card 25-26645-134 Gave NO-GO				NW
1445			TP 3.2.8	Cards 25-26646-137 thru -172				
		F2-254B, Vol. 2 Sec. 5 Page 7	All - GO					NW
1515			Repeat TP 3.2.4	Cards 25-26642-169				
			-171	NO-GO				
			-173					
			-195					
			-232					NW

JOB NUMBER
G1347
U3 4400 5605 ORIG. 6/60

PAGE
5
OF

NOMENCLATURE
COMPATIBILITY TEST

LEAD INSP.
STAMP DATE

INSP. REG.
STAMP DATE

CUS.

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91-

TEST NUMBER		UNIT SERIAL NUMBER		TEST PLANNING		MANUFACTURING AND INSPECTION RECORD				PAGE OF	
EDIT II-1B		—		BY DATE		TEST LOG				PAGE SER. NO.	
D2-13308 Vol. 2		—		—		TEST LOG				DATE	
TEST ITEM		WORK ORDER NO.		AUTHORITY		PLANNING BUDGET VERIFICATION				DATE	
W5133A		78105		EWA 8956		TEST LOG				12/13	
MODEL NO.		SER. NO.		TEST ITEM		CONDITION NO.		WORK ITEM NO.		TIME	
W5133A		78105		TP 3.2.4		NO-GO cards 25-26642-219		TP 3.2.5 All Cards GO		TP 3.2.6 All Cards GO	
W5133A		78105		TP 3.2.7 All Cards GO		TP 3.2.8 All Cards GO		Repeat TP's 3.2.4 thru 3.2.8		NO-GO's TP 3.2.4 25-26642-177	
W5133A		78105		TP 3.2.9 All Cards GO		TP 3.2.10 All Cards GO		TP 3.2.11 All Cards GO		TP 3.2.12 All Cards GO	
W5133A		78105		TP 3.2.13 All Cards GO		TP 3.2.14 All Cards GO		TP 3.2.15 All Cards GO		TP 3.2.16 All Cards GO	
W5133A		78105		TP 3.2.17 All Cards GO		TP 3.2.18 All Cards GO		TP 3.2.19 All Cards GO		TP 3.2.20 All Cards GO	
W5133A		78105		TP 3.2.21 All Cards GO		TP 3.2.22 All Cards GO		TP 3.2.23 All Cards GO		TP 3.2.24 All Cards GO	
W5133A		78105		TP 3.2.25 All Cards GO		TP 3.2.26 All Cards GO		TP 3.2.27 All Cards GO		TP 3.2.28 All Cards GO	
W5133A		78105		TP 3.2.29 All Cards GO		TP 3.2.30 All Cards GO		TP 3.2.31 All Cards GO		TP 3.2.32 All Cards GO	
W5133A		78105		TP 3.2.33 All Cards GO		TP 3.2.34 All Cards GO		TP 3.2.35 All Cards GO		TP 3.2.36 All Cards GO	
W5133A		78105		TP 3.2.37 All Cards GO		TP 3.2.38 All Cards GO		TP 3.2.39 All Cards GO		TP 3.2.40 All Cards GO	
W5133A		78105		TP 3.2.41 All Cards GO		TP 3.2.42 All Cards GO		TP 3.2.43 All Cards GO		TP 3.2.44 All Cards GO	
W5133A		78105		TP 3.2.45 All Cards GO		TP 3.2.46 All Cards GO		TP 3.2.47 All Cards GO		TP 3.2.48 All Cards GO	
W5133A		78105		TP 3.2.49 All Cards GO		TP 3.2.50 All Cards GO		TP 3.2.51 All Cards GO		TP 3.2.52 All Cards GO	
W5133A		78105		TP 3.2.53 All Cards GO		TP 3.2.54 All Cards GO		TP 3.2.55 All Cards GO		TP 3.2.56 All Cards GO	
W5133A		78105		TP 3.2.57 All Cards GO		TP 3.2.58 All Cards GO		TP 3.2.59 All Cards GO		TP 3.2.60 All Cards GO	
W5133A		78105		TP 3.2.61 All Cards GO		TP 3.2.62 All Cards GO		TP 3.2.63 All Cards GO		TP 3.2.64 All Cards GO	
W5133A		78105		TP 3.2.65 All Cards GO		TP 3.2.66 All Cards GO		TP 3.2.67 All Cards GO		TP 3.2.68 All Cards GO	
W5133A		78105		TP 3.2.69 All Cards GO		TP 3.2.70 All Cards GO		TP 3.2.71 All Cards GO		TP 3.2.72 All Cards GO	
W5133A		78105		TP 3.2.73 All Cards GO		TP 3.2.74 All Cards GO		TP 3.2.75 All Cards GO		TP 3.2.76 All Cards GO	
W5133A		78105		TP 3.2.77 All Cards GO		TP 3.2.78 All Cards GO		TP 3.2.79 All Cards GO		TP 3.2.80 All Cards GO	
W5133A		78105		TP 3.2.81 All Cards GO		TP 3.2.82 All Cards GO		TP 3.2.83 All Cards GO		TP 3.2.84 All Cards GO	
W5133A		78105		TP 3.2.85 All Cards GO		TP 3.2.86 All Cards GO		TP 3.2.87 All Cards GO		TP 3.2.88 All Cards GO	
W5133A		78105		TP 3.2.89 All Cards GO		TP 3.2.90 All Cards GO		TP 3.2.91 All Cards GO		TP 3.2.92 All Cards GO	
W5133A		78105		TP 3.2.93 All Cards GO		TP 3.2.94 All Cards GO		TP 3.2.95 All Cards GO		TP 3.2.96 All Cards GO	
W5133A		78105		TP 3.2.97 All Cards GO		TP 3.2.98 All Cards GO		TP 3.2.99 All Cards GO		TP 3.2.100 All Cards GO	

JOB NUMBER		PART NUMBER		NOMENCLATURE		LEAD INSP.		INSP. REC.		CUS	
61347		—		COMPATIBILITY TEST		STAMP DATE		STAMP DATE		STAMP DATE	

U3 4403 5505 ORIG. 6/60

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TEST NUMBER		UNIT SERIAL NUMBER	TEST PLANNING	MANUFACTURING AND INSPECTION RECORD		PAGE	
EDIT II-1B			BY	DATE	TEST LOG	OF	
D2-13308 Vol. 2							
MODEL NO.	TEST ITEM	TEST SER. NO.	WORK ORDER NO.	AUTHORITY	PLANNING BUDGET	DATE	
WS133A						12/13	
TIME	CONDITION NO.	WORK ITEM NO.		REF. E. R. SER. NO.	OPERATION COMPLETION		
					SHOP INSP. ENGR		
			TP 3.2.5	NO-GO Card 25-26643-123		RM	
			TP 3.2.6	All Cards -- GO		RM	
1420			Ray Miller from P/G Test Set design removed bottom cover from Test Set and took out Phase Supply Evaluator cards A-7, A-8, & A-9. This was done to correct NO-GO signals on GO cards. Results -- NO-GO on cards 25-26642-168			RM	
				-171			
				-177			
1535			Shut-down all equipment and replaced Fig. A 4523 with ACO 711 Power Supply, Serial No. 0001, Part No. 25-26826-6			RM	
			Results -- NO-GO cards on 25-26642-174				
PAGE 9		JOB NUMBER 61347		PART NUMBER		NOMENCLATURE	
OF						COMPATIBILITY TEST	
		U3 4400 5605 ORIG. 6/60					

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TEST NO. & REP.		UNIT SERIAL NUMBER	TEST PLANNING BY DATE	MANUFACTURING AND INSPECTION RECORD		PAGE OF
				TEST LOG	PLANNING BUDGET VERIFICATION	DATE
MODEL NO.		WORK ORDER NO.	AUTH. CITY	EWA8956	SER. NO.	12/14
CONSTRUCTION NO.	SER. NO.	ITEM NO.	REF. E. R. SER. NO.	SHOP INSP.	CNCR	
WS133A	02-13308 V/L 2	78105				
1420	Arrangements were made with Mfg. to borrow one of their BTS 155A Test set. Continued testing and NO-GO's appeared at random.					NW
1500	Test Set, Fig. A 3092 serial No. 0004, was brought into the EDIT Lab and connected in place of serial No. 0003. End to End Test cards were run - 25-26642-165 thru - 232 All Cards -- GO					NW
1620	Second run with same cards - 165 to - 195 All cards -- GO					
T2-2548, Vol. 2 Sec. 5 Page 14						
JOB NUMBER G1347		PART NUMBER		NOMENCLATURE COMPATIBILITY TEST		LEAD INSP. STAMP DATE
PAGE OF 11						INSP. REG. CL

UNIT SERIAL NUMBER		TEST PLANNING	TEST LOG	PLANNING BUDGET VERIFICATION	PAGE SER. NO.	PAGE OF
BY	DATE	TEST LOG	PLANNING BUDGET VERIFICATION	PAGE SER. NO.	PAGE OF	
MODEL NO. 1415	WORK ORDER NO. 78105	TEST ITEM SER. NO. 1415	DISCONNECTED P/G TEST SET SER. NO. 0004	REF E. R. SER. NO.	OPERATION COMPLETION	DATE 12/17/71
1415	1415	1415	and returned to EDL/ET	SHOP	INSP.	ENGR
1415	1415	1415	Connected Test Set, ser. No. 0003, and continued test compatibility.	SHOP	INSP.	ENGR
1415	1415	1415	Review of M & IR paper on Fig. A 3092, ser. No. 0003, revealed it had not had calibration certified since August '62. This certification is required every 60 days so this had to be done	SHOP	INSP.	ENGR
1415	1415	1415	Calibration certification begun on Fig. A 3092.	SHOP	INSP.	ENGR
1415	1415	1415	BTS 155A received in EDIT LAB	SHOP	INSP.	ENGR
1415	1415	1415	No Cables	SHOP	INSP.	ENGR
1415	1415	1415	Arrangements continued to pick-up cables during Second shift	SHOP	INSP.	ENGR
1415	1415	1415		SHOP	INSP.	ENGR

JOB NUMBER 61347
 US 4400 5605 ORIG. 6/60

MANUFACTURING AND INSPECTION RECORD						PAGE OF
TEST PLANNING		WORK ORDER NO.		PAGE SER. NO.		DATE
BY	DATE			PLANNING BUDGET	VERIFICATION	
		78105		EWA 8956		12/1/44
UNIT SERIAL NUMBER	TEST ITEM	CONDITION NO.	WORK ITEM NO.	REF E. R. SER. NO.	OPERATION COMPLETION	
D2-13303 Vol. 2	1/1-1/1-1/1				SHOP	INSP.
						ENGR.
1000	Calibration completed on P/O Test Set					
1200	0003 -					
	(1) The D calibration Inter-pulse Level was below the $-1.75 \pm .05$ volt DC at jack INPUT					
	(2) Calibration of Pulse Generators Card A15 - there was 113 microsecond pulse at jack M1 OUTPUT instead of 110 ± 2 μ sec.					
	(3) Calibration of Inhibit Amplifiers Card A16 - voltage at jack #1 OUTPUT was 20 volt DC instead of $17 \pm .5$ volt DC	T2-1540 Sec. 5	Vol. 2 Page 16			
	Card A16 - voltage at jack #2 OUTPUT was 20 volt DC instead of $17 \pm .5$ volt DC					

U3 4400 5605 ORIG. 6/60
 JOB NUMBER
G1347
 PART NUMBER
 NOMENCLATURE
COMPATIBILITY TEST
 LEAD INSP. STAMP DATE
 INSP. REG. CL
 PAGE 1 OF

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TEST NUMBER			UNIT SERIAL NUMBER			TEST PLANNING			MANUFACTURING AND INSPECTION RECORD						PAGE OF
EDIT II-1B						BY DATE			TEST LOG						
D2-13308 Vol 2									AUTHORITY EWA 8956						DATE 12/1
MODEL NO. WS133A			WORK ORDER NO. 78105			PLANNING BUDGET VERIFICATION			PAGE SER. NO.						
TIME	CONDITION NO.	WORK ITEM NO.	REF. E. R. SER. NO.	OPERATION	COMPLETION	SHT.	EN								
0220								Prepare BTS 155A to be shipped back to factory - also cabling and other equip.							N/A
0900		2/2/3						Connect Fig A's 1201, 3113, 3092 & 4523 as shown on Fig 3.3.1.3-1 of document D2-13308, Volume 2, Section 2 for EDIT II-1B program							N/A
9000								Run self-check on P/G Test Set OK							N/A
			T2-2548, Sec. 5					Voltage checks - OK							N/A
1300								Trouble on End to End Card Test							N/A
								Run Station - MGE Mexican Group Connected Test Adapter - P/G test set - AC 72.1 & source							N/A
JOB NUMBER 61347			PART NUMBER —			NOMENCLATURE COMPATIBILITY TEST			LEAD INSP. STAMP			INSP. REG. STAMP			
PAGE 4 OF															

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PAGE OF		TEST LOG		PAGE SER. NO.		PLANNING BUDGET VERIFICATION		DATE	
TEST PLANNING BY		DATE		AUTHORITY		EWA 8956		1-9-	
WORK ORDER NO.		78105		TEST LOG		EWA 8956		1-9-	
ITEM SER. NO.		170		ALL-GO		ALL-GO		ALL-GO	
170		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
171		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
172		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
173		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
174		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
175		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
176		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
177		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
178		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
179		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
180		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
181		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
182		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
183		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
184		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
185		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
186		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
187		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
188		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
189		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
190		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
191		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
192		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
193		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
194		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
195		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
196		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
197		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
198		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
199		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
200		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
201		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
202		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
203		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
204		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
205		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
206		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
207		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
208		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
209		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
210		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
211		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
212		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
213		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
214		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
215		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
216		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
217		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
218		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
219		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
220		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
221		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
222		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
223		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
224		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
225		ALL-GO		ALL-GO		ALL-GO		ALL-GO	
226		ALL-GO		ALL-GO					

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TEST PLANNING						MANUFACTURING AND INSPECTION RECORD					
UNIT SERIAL NUMBER		TEST PLANNING BY DATE		TEST LOG		PAGE SER. NO.		DATE		PAGES OF	
				EWA 8956				1-1			
WORK ORDER NO.		78105		AUTHORITY		PLANNING BUDGET VERIFICATION		REF E. R. SER. NO.		OPERATION COMPLETED	
MODEL NO.	TEST ITEM	SER. NO.	CONDITION	NO.	TIME						
D2-13308 Vol. 2	WS133A				08:00	1-	Continued End to End testing with the following NO-GO's	-168	-189	-204	-220
								-173	-190	-205	-222
								-180	-193	-206	-225
								-181	-194	-214	-226
								-185	-195	-215	-228
								-188	-201	-219	-232
						2-	Times Sequential Test - All GO				
						3-	P/C Launch Sequence Test - NO-GO's				
								-132,	-133,	-136,-140,-145,-157	
						4-	Programmer Calibration Test - All GO				
						5-	Monitor Launcher Missile Status Test -				
								NO GO's --:-141,-155,-156,-160			

PAGE	JOB NUMBER	PART NUMBER	NOMENCLATURE	LEAD INSP.	INSP. REG.
OF	61347		COMPATIBILITY TEST	STAMP DATE	STAMP DATE
	U3 4400 5605 ORIG. 6/60				

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MANUFACTURING AND INSPECTION RECORD										PAGE OF	
TEST LOG				PLANNING BUDGET VERIFICATION		PAGE SER. NO.		DATE		1-17-	
UNIT SERIAL NUMBER		TEST PLANNING		WORK ORDER NO.		AUTHORITY		REF E. R. SER. NO.		OPERATION COMPLETION	
BY		DATE						SHOP		INSP.	
MODEL NO.		TEST ITEM SER. NO.		CONDITION NO.		WORK ITEM NO.		TIME		ENG.	
78105		EWA 8956									
Performance End to End test to obtain performance data on Card Reader Assy. (60 cards) 1st cycle 2nd 3rd 4th All cards -174 All cards All cards GO -181 GO GO NO-GO											
5th 6th 7th 8th -199 All cards All cards All cards NO-GO GO GO GO											
Final shut-down of equipment in EDIT II-IB test. Elapsed time Meter reading - 0169.7 Total hours 141.3 hours											
T2-2548, Vol. 2 Sec. 5 Page 34											
COMPATIBILITY TEST											
JOB NUMBER 61347											
U3 4400 5605 ORIG. 6/60											

THE BOEING COMPANY

NUMBER T2-2548 MODEL NO. WS133A
TITLE Engineering Development Integration
Test No. Two-B (EDIT II-1B) Photographs

PREPARED BY

D. W. Gladish
D. W. Gladish

3/14/63

SUPERVISED BY

E. G. Helling
E. G. Helling

3/12/63

APPROVED BY

R. B. Edgar
R. B. Edgar

3/13/63

RELIABILITY
APPROVAL

(DATE)

AF 04(647)-289
CONTRACT NO.

5-78105-8640-68956

CHARGE NUMBER

2/22/63

VOL. 2 NO. T2-2548
SEC. 6 PAGE 1 OF 8

1.0

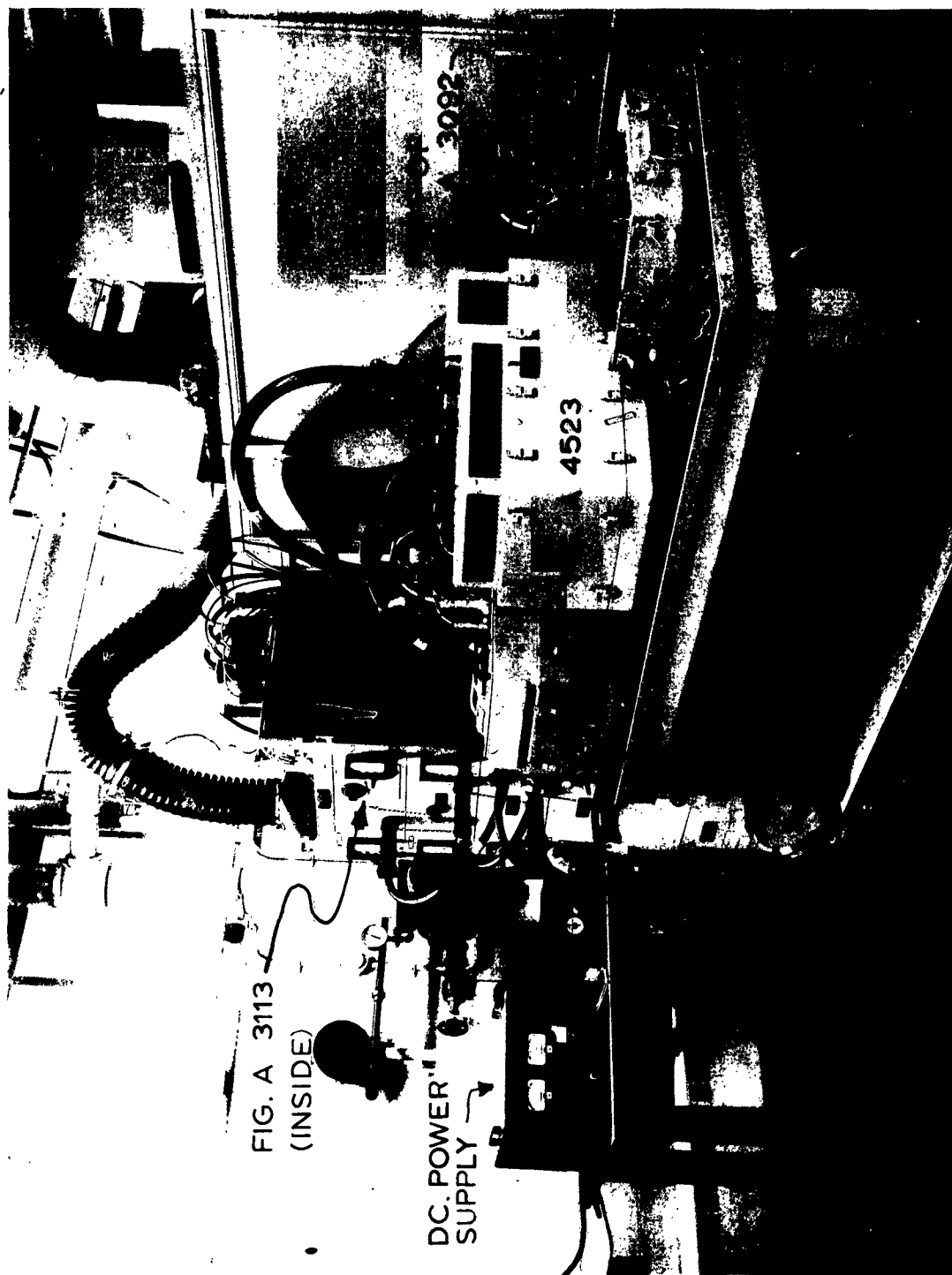
PHOTOGRAPHS

The following photographs show equipment used in the EDIT test.
The first photograph shows all the equipment connected as shown
in Figure 3.2.1.3-1.

14. PROGRAM GROUP CARD READER
AND ADAPTER - RMT TEST 1-17-65 2A131245

REVISED 2122163

U3 4288 2000



BOEING

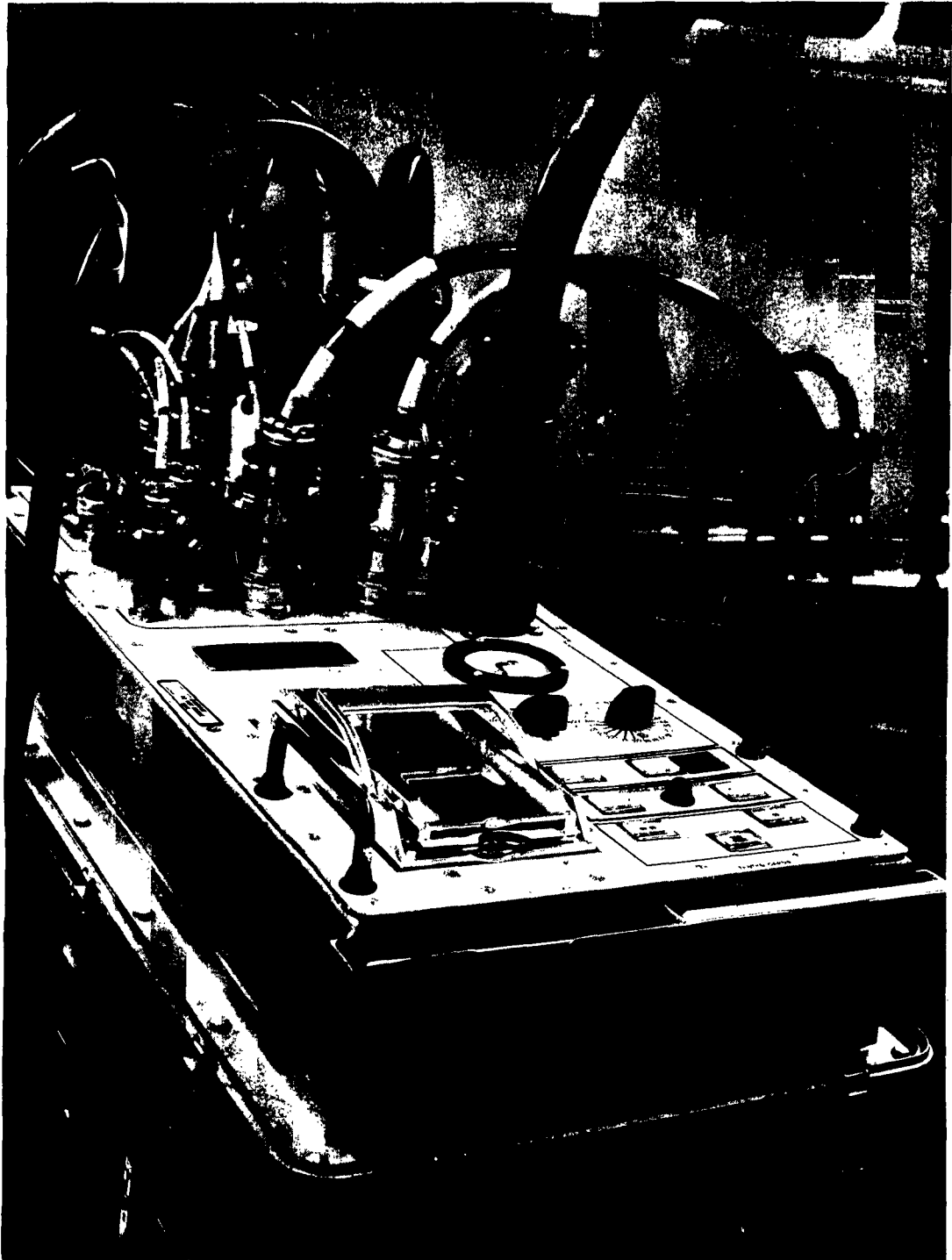
VOL. 2

NO T2-2548

SEC. 6

PAGE 3

2A131212
 PROGRAMMER GROUP CARD READER
 AND ADAPTER - EXT TEST 1-17-69



PROGRAMMER GROUP TEST SET
 FIG. A 3092

REVISED 2/22/63

U3 4288 2000

BOEING

VOL. 2

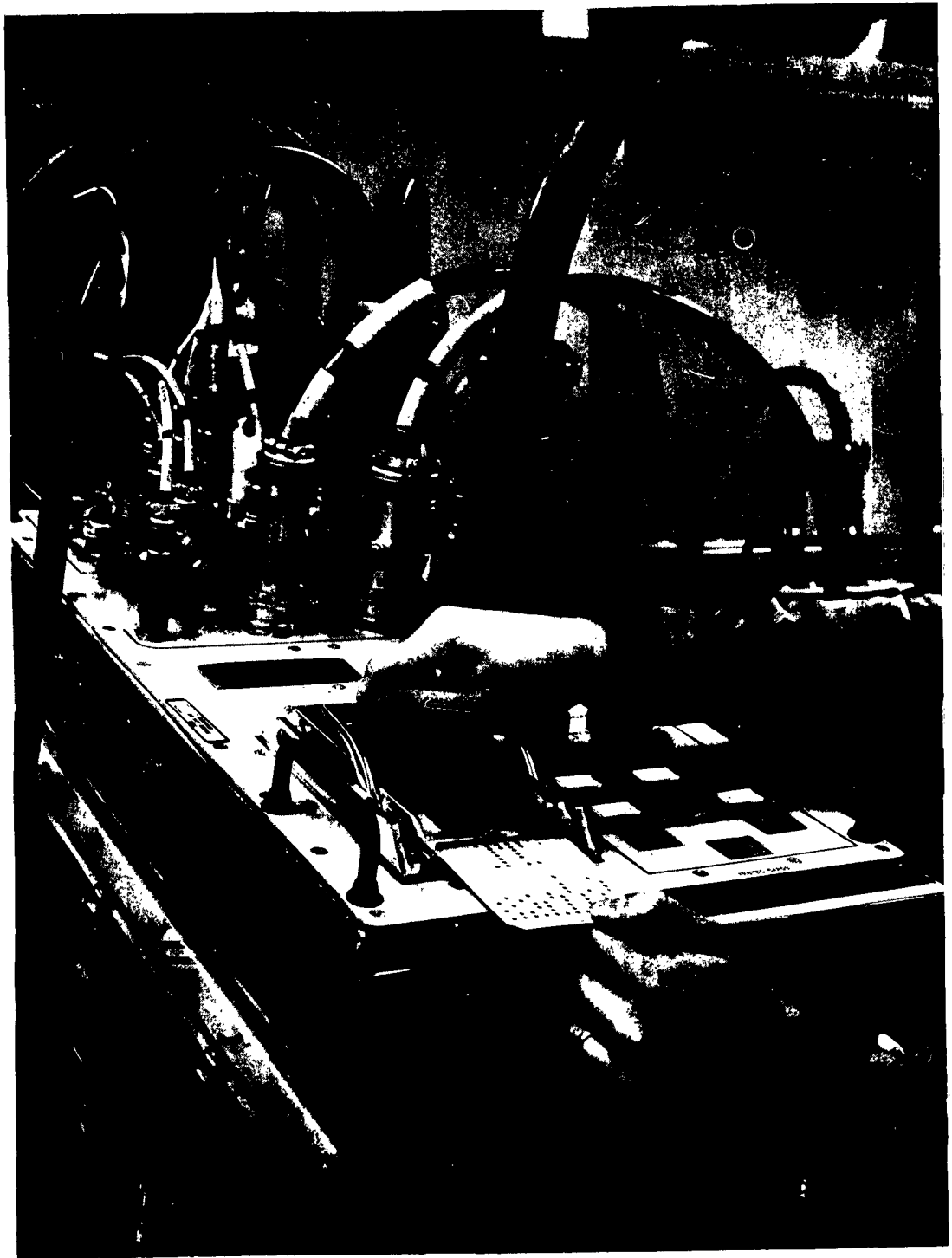
NO T2-2548

SEC. 6

PAGE 4



2A131244
M- PROGRAMMER GROUP CARD READER
AND ADAPTER - EXT. TEST 1-17-63



REMOVING CARD FROM CARD READER

FIG. A 3092

REVISED 212263

U3 4288 2000 ,

BOEING

VOL. 2

SEC. 6

NOT2-2548

PAGE 5

NO. PROGRAMMER GROUP CARD MAKER 2A131243
AND ADAPTER - BDTT TEST 1-17-63



DISTRIBUTION BOX—PART OF
FIG. A 3092

REVISED 2/22/63
U3 4288 2000

BOEING

VOL. 2
SEC. 6

NOT 2-2548
PAGE 6



16
10. PROGRAMMER GROUP CARD READER AND ADAPTER - BENT TEST 1-17-63 2A131240



POWER SUPPLY — FIG. A 4523

REVISED 2122163
U3 4288 2000

BOEING	VOL. 2	NOT 2-2548
	SEC. 6	PAGE 7

→

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164- PROGRAMMER GROUP CARD READER
AND ADAPTER - RENT TEST 1-17-63



DUMMY DECODER RELAY ASSEMBLY—
FIG. A 3113

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PAGE 8

