

AD-A039 950

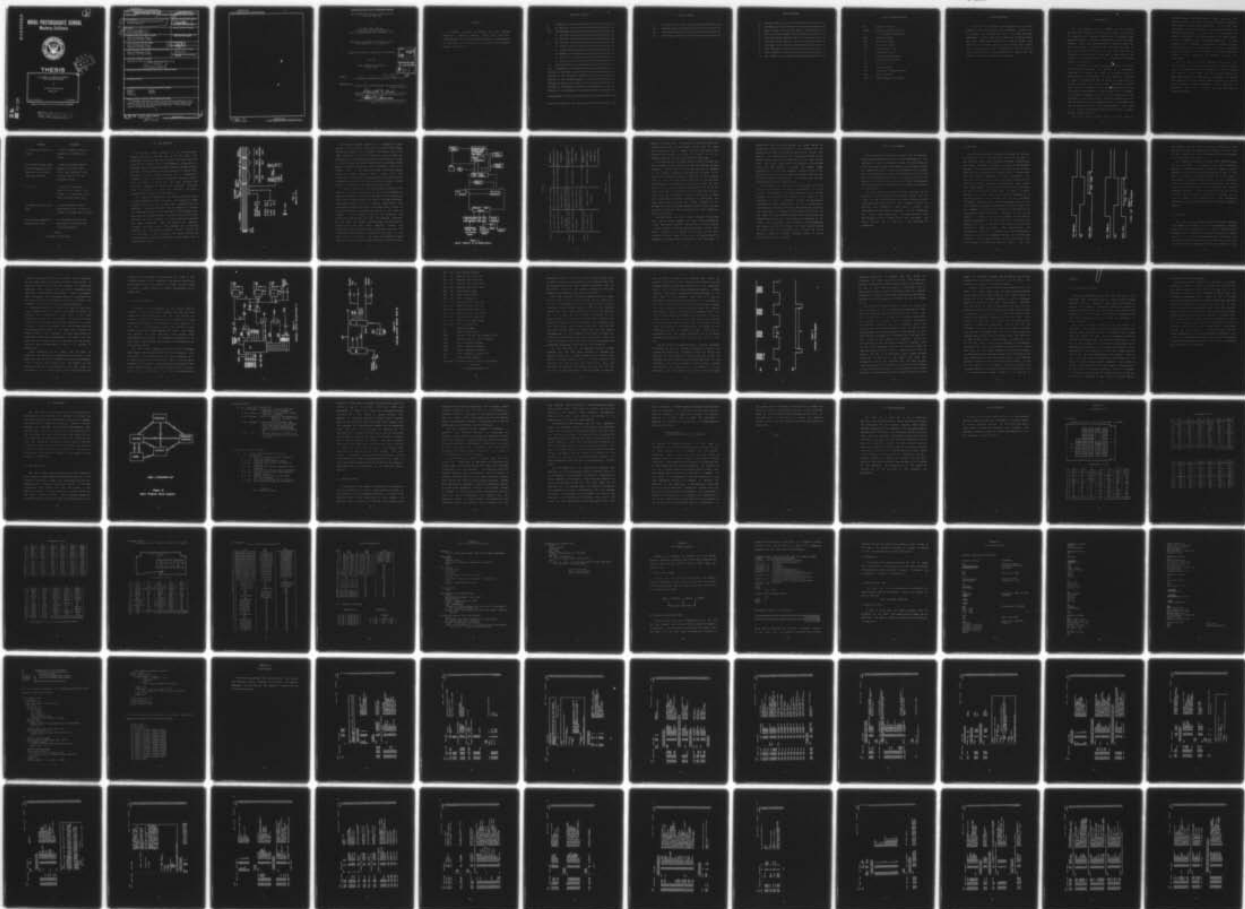
NAVAL POSTGRADUATE SCHOOL MONTEREY CALIF
AN INTERACTIVE COMPUTER INTERFACE WITH A DIGITAL RECEIVER.(U)
MAR 77 W 6 BORRIES

F/G 9/2

UNCLASSIFIED

NL

1 OF 2
AD
A039 950



AD A 039950

②
B.S.

NAVAL POSTGRADUATE SCHOOL

Monterey, California



THESIS

AN INTERACTIVE COMPUTER INTERFACE
WITH A DIGITAL RECEIVER

by

William Glenn Borries
March 1977

Thesis Advisor:

S. Jauregui

Approved for public release; distribution unlimited.

AD No.
DDC FILE COPY

COPY AVAILABLE TO DDC DOES NOT
PERMIT FULLY LEGIBLE PRODUCTION

UNCLASSIFIED

SECURITY CLASSIFICATION OF THIS PAGE (When Data Entered)

REPORT DOCUMENTATION PAGE		READ INSTRUCTIONS BEFORE COMPLETING FORM
1. REPORT NUMBER	2. GOVT ACCESSION NO.	3. RECIPIENT'S CATALOG NUMBER 9
4. TITLE (and Subtitle) An Interactive Computer Interface with a Digital Receiver		5. TYPE OF REPORT & PERIOD COVERED Master's Thesis March 1977
7. AUTHOR(s) William Glenn Borries		6. CONTRACT OR GRANT NUMBER(s)
9. PERFORMING ORGANIZATION NAME AND ADDRESS Naval Postgraduate School Monterey, California 93940		10. PROGRAM ELEMENT, PROJECT, TASK AREA & WORK UNIT NUMBERS
11. CONTROLLING OFFICE NAME AND ADDRESS Naval Postgraduate School Monterey, California 93940		12. REPORT DATE March 1977
14. MONITORING AGENCY NAME & ADDRESS (if different from Controlling Office) Naval Postgraduate School Monterey, California 93940		13. NUMBER OF PAGES 124
16. DISTRIBUTION STATEMENT (of this Report) Approved for Public Release: Distribution Unlimited 12 125p.		15. SECURITY CLASS. (of this report) UNCLASSIFIED
17. DISTRIBUTION STATEMENT (of the abstract entered in Block 20, if different from Report)		15a. DECLASSIFICATION/DOWNGRADING SCHEDULE
18. SUPPLEMENTARY NOTES		
19. KEY WORDS (Continue on reverse side if necessary and identify by block number) Register Digital Buffer Computer Interface		
20. ABSTRACT (Continue on reverse side if necessary and identify by block number) A computer interface to connect both the Applied Technology Airborne Computer (ATAC) and the KIM-1 Microprocessor to a Watkins Johnson digitally tuned receiver was designed and constructed. The existing ATAC computer program was modified.		

251450

Handwritten initials

UNCLASSIFIED

SECURITY CLASSIFICATION OF THIS PAGE(When Data Entered)

DD Form 1473
1 Jan 73
S/N 0102-014-6601

2 UNCLASSIFIED
SECURITY CLASSIFICATION OF THIS PAGE(When Data Entered)

Approved for public release; distribution unlimited

An Interactive Computer Interface with
a Digital Receiver

by

William Glenn Borries
Lieutenant, United States Navy
B.S., United States Naval Academy, 1970

Submitted in partial fulfillment of the
requirements for the degree of

MASTER OF SCIENCE IN ELECTRICAL ENGINEERING

from the

NAVAL POSTGRADUATE SCHOOL
March, 1977

ACCESSION for	
NTIS	White Section <input checked="" type="checkbox"/>
D.C.	Buff Section <input type="checkbox"/>
UNANNOUNCED	<input type="checkbox"/>
JUSTIFICATION.....	
BY.....	
DISTRIBUTION/AVAILABILITY CODES	
Dist.	AVAIL and/or SPECIAL
A	23

Author:

William Glenn Borries

012

Approved by:

[Signature]

Thesis Advisor

Donald E. Kirk

Chairman, Department of Electrical Engineering

Robert A. Johnson

Dean of Science and Engineering

ABSTRACT

A computer interface to connect both the Applied Technology Airborne Computer (ATAC) and the KIM-1 Microprocessor to a Watkins Johnson digitally tuned receiver was designed and constructed. The existing ATAC computer program was modified.

TABLE OF CONTENTS

I.	INTRODUCTION	10
II.	THE RECEIVER	15
III.	THE COMPUTERS	22
	A. THE ATAC	23
	B. THE KIM-1	25
IV.	THE INTERFACE	27
	A. CONTROL SECTION	29
	B. INPUT/OUTPUT REGISTERS	38
V.	THE PROGRAM	42
	A. MAIN SYSTEM	42
	B. RECEIVER CONTROL	45
VI.	RECOMMENDATIONS	50
VII.	CONCLUSION	51
	APPENDIX A: INTERFACE WIRING	52
	APPENDIX B: ATAC OPERATING INSTRUCTIONS	60
	APPENDIX C: ATAC PROGRAM ASSEMBLY	62
	APPENDIX D: SAMPLE ATAC OUTPUT	65
	APPENDIX E: CONVERSION PROGRAMS FOR THE ASSEMBLER	68
	APPENDIX F: ATAC PROGRAM	72
	BIBLIOGRAPHY	124
	INITIAL DISTRIBUTION LIST	125

LIST OF TABLES

I.	Problems and Solutions -----	14
II.	Receiver Modes and Periods -----	19
III.	Interface Command List -----	32
IV.	ATAC Program Commands -----	44

LIST OF FIGURES

1.	Computer Control -----	13
2.	The Receiver's Word -----	16
3.	Block Diagram of the Digital Section -----	18
4.	ATAC I/O Timing Diagram -----	24
5.	Interface Control Section (Part 1) -----	30
6.	Interface Control Section (Part 2) -----	31
7.	Interface Timing Diagram -----	35
8.	Input Register -----	40
9.	Output Register -----	41
10.	ATAC Program Block Diagram -----	43

LIST OF ABBREVIATIONS

A/D	Analog to Digital
ASCII	American Standard Code for Information Interchange
Baud	Bits per second
D/A	Digital to Analog
high	TTL logic 1 (+5v)
I/O	Input and/or Output
IC	Integrated Circuit
IF	Intermediate Frequency
ISB	Intermediate Sideband
low	TTL logic 0 (0v)
LSB	Lower Sideband
TTL	Transistor Transistor Logic
USB	Upper Sideband
BFO	Beat Frequency Oscillator

ACKNOWLEDGEMENTS

I would like to express my deep appreciation to Carole Hickey who wrote the initial ATAC programs. Without her Main System the programming that I did would have been unbearable. I would also like to thank the following people who have helped along the way: LT. Al May, Al Gilkes, Greg Ramos, LT. Bill Hickey, Bob Glaz, Dave Blonden, Dean Hayes, and Virginia Ward. Most importantly, I want to thank my wife, Cathy, for all the encouragement and advice she has given me during the writing of this thesis.

I. INTRODUCTION

For many decades man has dreamed of the day when machines could relieve him of much of his work. In this era of computers and advanced technology, this dream is now becoming a reality. Connecting computers to other machines, however, is not just a simple matter of running a wire from one to the other. In order for the computer to be able to use its "thinking" ability, it must have some way to translate its signals into a form that is recognized by the machine it is controlling. This is where the interface becomes all important.

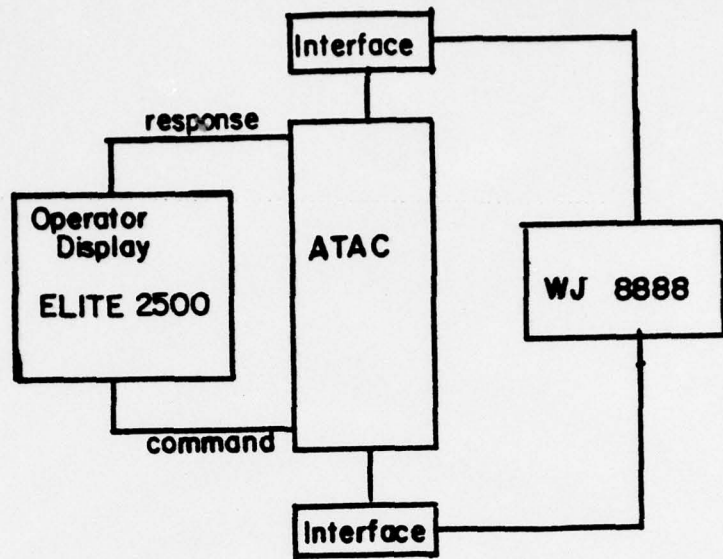
An interface is a piece of equipment placed in the data path between two devices. Its purpose is to rearrange, translate, or change the speed of this data to meet the needs of one or both devices. In other cases the interface is used to convert data from an analog to digital (A/D) or digital to analog (D/A) form, or both. Interfaces of either type range in complexity from a few integrated circuits to the use of microprocessors. Most, however, fall in between. This thesis discusses the design and construction of an interface in this middle class. Here, the computers are the Applied Technology Airborne Computer (ATAC) minicomputer and the MOS Technology Inc.'s KIM-1 microprocessor. Their goal is to program and process outputs from a digitally tunable Watkins Johnson WJ-8888.

The two computer systems arrive at their goal by

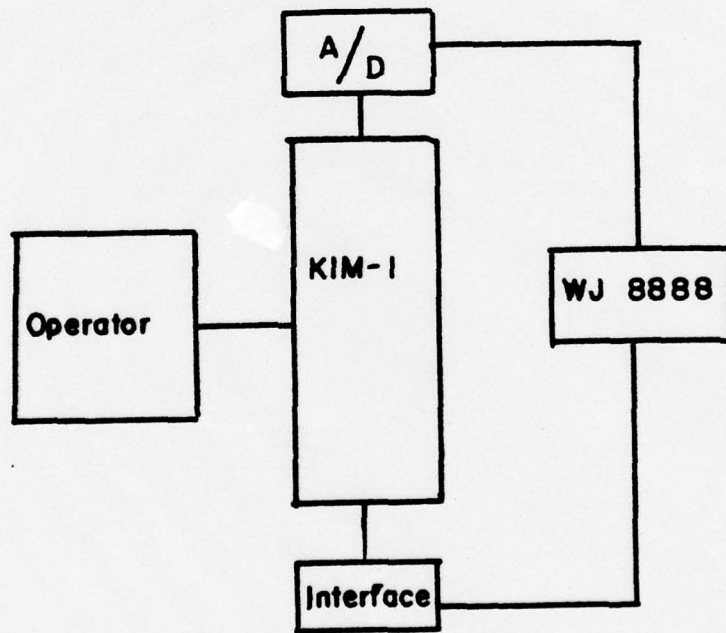
different means. The ATAC uses a closed loop with the operator (Figure 1a) while the KIM-1 excludes the operator while executing its program (Figure 1b). In the ATAC loop the operator actively controls all communication between the computer and receiver. In this way it is possible to display information from the receiver on the video display at any time except during a scan (see Chapter V). It also provides quick reference to the data to be sent, the data last sent, and latest received data. This was invaluable during debugging. From the terminal it is also possible to adjust available parameters as necessary to meet any requirement.

The KIM-1 does not directly exchange digital words with the receiver, but rather exchanges digital data for analog data. This does not provide a feedback loop that includes the operator. Once begun, the KIM-1 program selects and sends data words to the receiver and processes the analog data received until the program comes to an end or is halted by the operator. Direct information is not available to determine when or if a digital word has been sent or received correctly.

Problems encountered during the design and construction of the interface and their solutions are shown in Table I. In this instance signal level compatibility was not a problem because the I/O from the receiver, the interface, and the two computers were all TTL logic levels and, therefore, matched. It is believed that these problems are a typical list that may be encountered when interfacing.



(a)



(b)

Figure 1
Computer Control

PROBLEM

SOLUTION

- | | |
|--|---|
| 1. Noise on the ATAC I/O lines. | 1. Use of Schottky circuits reduced or eliminated the noise. |
| 2. Different clock rates of the computers and the receiver, and different data word lengths. | 2. ATAC; converted parallel outputs into serial form. KIM-1; used interrupt lines to slave the KIM-1 to the receiver's clock. |
| 3. Timing | 3. Identified receiver periods by the Monitor Clock output. This provided a pulse which signaled stable data. |
| 4. Inputting data to the ATAC. | 4. Open collector buffers were used to sink the required current for proper data transfer. |
| 5. Switching between the ATAC and the KIM-1. | 5. Multiplexers and buffers were used to switch between the two computers. |

Table I
Problems and Solutions

II. THE RECEIVER

The Watkins Johnson WJ-8888 (WJ) is an HF receiver designed for use in the 550 kHz to 30 MHz band. Its advantages include the ability to detect and output both the AM and FM IF signals while simultaneously maintaining a separate output of eight selectable detection modes. Options available to the operator include different IF bandwidths, variable RF gain, squelch control, and a tuneable BFO frequency. The WJ is digitally controlled and uses a 64-bit word as shown in Figure 2. This word contains the information necessary to transfer the frequency, detection mode, IF bandwidth, RF gain, BFO frequency, and signal strength both internally and externally.

All inputs and outputs from the receiver are controlled by the synchronous, remote I/O board. This board is a gated transfer point for all digital data exchanged with the receiver. A number of control lines are needed to provide the necessary demands on the receiver. Three balanced input pairs and four balanced output line pairs, plus a ground are provided for this purpose. All three inputs are required for remote operation. They are address (or enable), trigger, and data input. The address pair is the most important for it serves as the master "on-off" switch for the remainder of the I/O pairs. The outputs furnish the required clocks (command and monitor), output data, and a local/remote status.

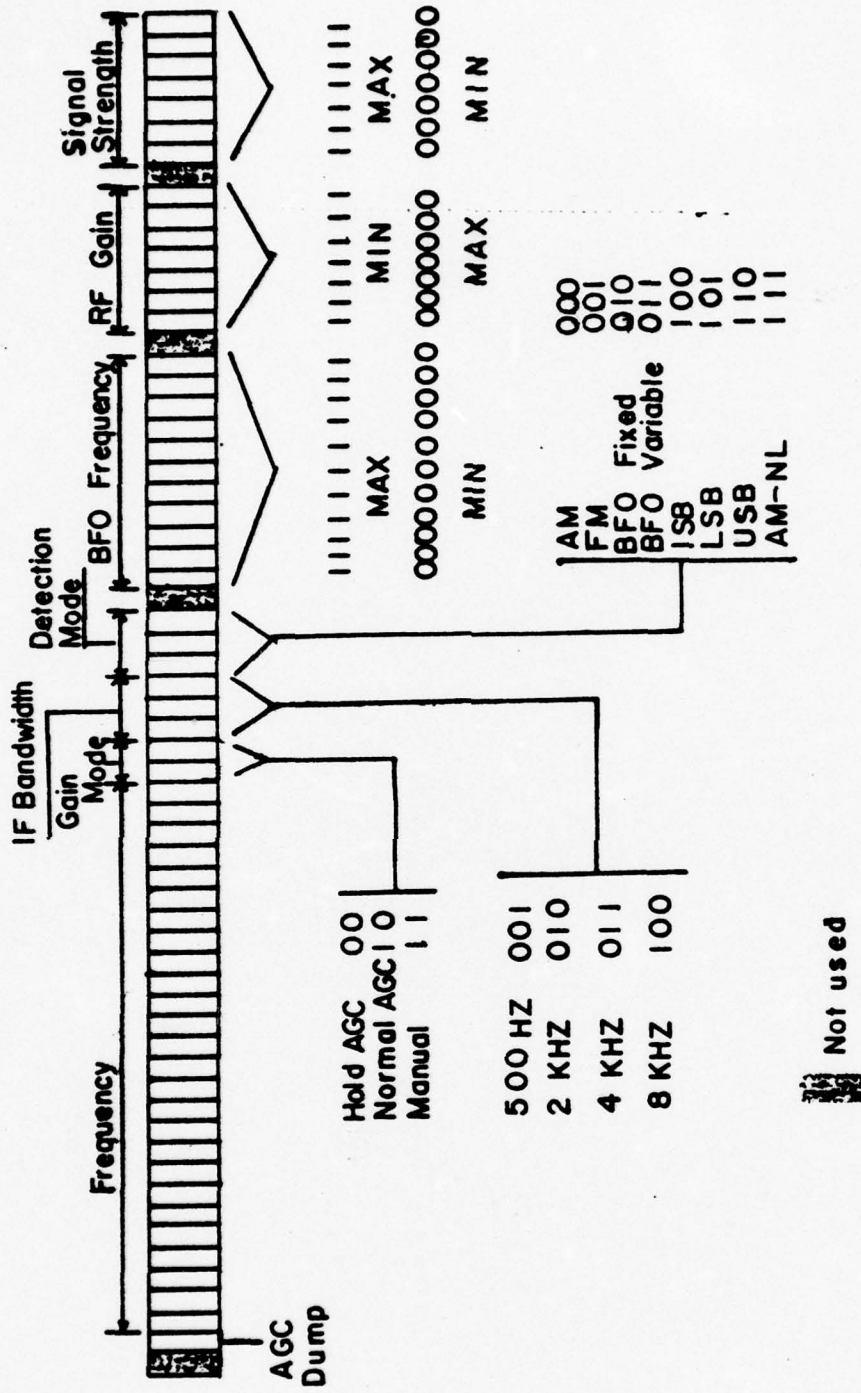


Figure 2
Receiver's Word

The Watkins Johnson operates on a sequential cycle divided into four equal periods and six identifiable modes. The periods regulate the different operations while the modes ascertain the origin of the data. Table II shows the interaction of the periods and modes of the receiver. Three of the six modes are memory read and write functions; these cannot be remotely controlled and, therefore, are of no concern here. Of the remaining three, two are the remote active and remote passive modes. These allow the introduction of externally generated data and prevent manual intervention during all but one of the four periods. Manual control is available in the remaining mode, local.

In order to manage the data word movement correctly, the receiver utilizes a common bus or data node arrangement as shown in Figure 3. This simplifies operation by forcing all data words to pass through this node in the same direction, regardless of their origin or desired destination. The multiplexer controls the input to the data node. Control of the multiplexer and, therefore, the origin of the data is managed by the internal modes of the receiver. The objective of period one is to load the receiver register. In the local and remote passive modes, the data word is shifted from the front panel register, through the multiplexer and data node, into the receiver register. The difference between these two modes is in the action of the data prior to shifting. The local mode updates the data word from the front panel storage registers during the early

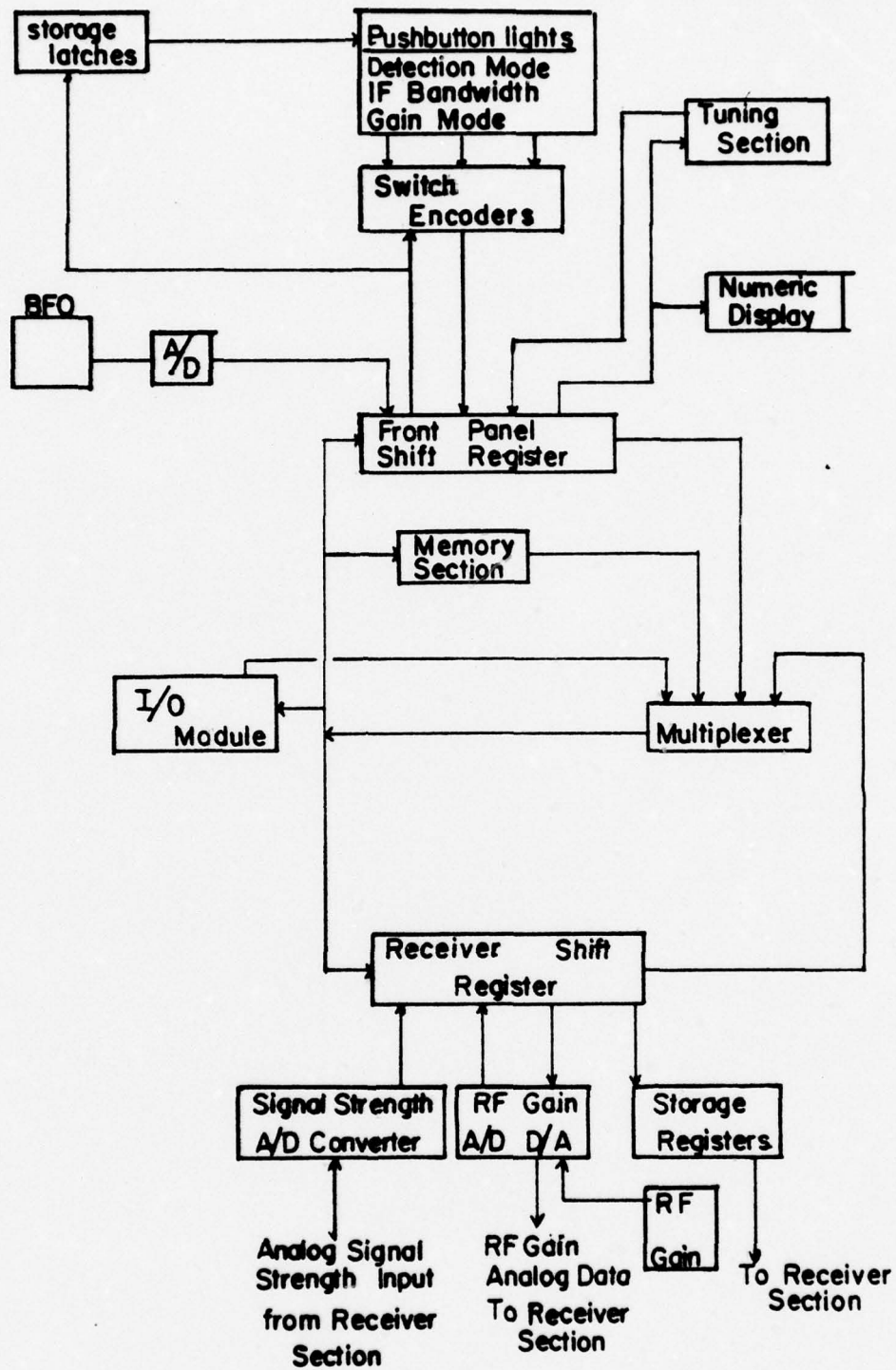


Figure 3
Block Diagram of the Digital Section

Period
1 2 3 4

Local	Load Front Panel Reg from Front Panel/word shifted to Receiver Register	Load Rcvr Storage Reg/ Signal Strength and RF gain updated /word shifted to FrontPanel Register	Display updated no words shifted	Change to Remote Active or Remote Passive possible
Remote Passive	Word shifted from FrontPanel Register to Receiver	Update only Sig Str /Load Kcvr Storage Reg/word shifted to FrontPanel Reg.	Same as Local	Change to Lcl or Remote Active possible
Remote Active	Word shifted from I/O module to Receiver Register	Same as Remote Passive	Same as Local	Change to Remote Passive automatic. Change to Local possible

Table II
Receiver Modes and Periods

portion of period one. This action is inhibited during the remote passive mode. In the remote active mode the data word originates from a remote device, is shifted by the command clock through the remote I/O board, on to the receiver register via the multiplexer and data node.

The first part of the second period is spent loading the data shifted during period one into the receiver storage registers. During this time the signal strength is updated in the receiver register regardless of the mode. The RF gain A/D-D/A converter functions according to the selected mode. In the local mode the RF gain bits in the data word are replaced by A/D conversion of the front panel RF gain control knob. The two remote modes reverse this action and load the RF gain D/A converter with this data from the word. After this is completed, the word is shifted in all modes out of the receiver register, through the multiplexer and data node, and into the front panel register. If the address line from the remote device is active high, the data word and the monitor clock are available on their respective output line pairs.

Periods three and four inhibit movement of the data word. Period three updates the front panel pushbutton lights and numeric display. Period four is the only period in which changes in receiver mode are allowed. During this period changes from a remote mode to local, or from local directly to remote passive can only be accomplished by depressing the appropriate pushbutton on the front panel. A

change from local and remote passive to remote active is automatically done by the remote I/O board whenever both the address and trigger line pairs are active high during this period. The remote active mode immediately reverts to the remote passive mode at the beginning of the next period four. The total cycle time of the receiver is 10.24 msec (2.56 msec per period). In order to change modes successfully, it may be necessary either to hold in the pushbutton or to hold the trigger and address lines high for up to 7.68 msec (three periods). This ensures that the mode change demand occurs in period four.

All outputs are available from connectors J1, and J6 through J10 located on the back of the receiver. J1 is the digital I/O connector. The other connectors are all analog outputs. J6 is a 455 KHz IF signal of at least 20 KHz bandwidth. AM and FM detector monitors are provided at connectors J7 and J9 respectively. J8 is a predetection, 455 KHz center frequency IF output whose bandwidth is set by the front panel. A balanced and unbalanced line audio and both upper and lower sideband outputs are available from the appropriate pins at J10. The balanced line operates at all times. The unbalanced line is operable unless headphones are plugged into the front panel. The lower sideband output is active when the receiver is in either ISB or LSB detection modes, and the upper sideband output is active during ISB, USB, and CW modes.

III. THE COMPUTERS

After studying the inputs and outputs from the receiver, three choices were available for further development of the interface. It could be designed to pass the clock pulses on to the interrupt lines of the computer and, therefore, match the computer's timing to that of the receiver. Or, a buffer could be constructed to input the data serially at the clock rate of the computer and output it at the clock rate of the receiver. The third choice, also a buffering arrangement, could exchange data in parallel to the computer and serially to the receiver.

The chief factor influencing the design decision was the availability and distribution of computer control and I/O lines. For the first computer, the primary objective was to investigate the feasibility of both remotely tuning the receiver and accepting a data word in return. The requirements for the second computer, the MOS Technology Inc. KIM-1, were less strict. Its objective was to tune the receiver digitally through use of the interface. Its input, however, was to come from a A/D converter for processing.

A. THE ATAC

The ATAC was originally designed to provide EW service to aircraft. Built to do real-time analysis of signals, it has very short cycle times, optional microcode programming, and double precision arithmetic as part of the standard package. All this, combined with its large instruction set, makes the ATAC a versatile and powerful tool. Although data could be transferred serially by proper programming, the ability of the ATAC to both input and output sixteen bits in parallel on the PIO (parallel input/output) lines proved more advantageous. Any one of the ATAC's sixteen registers can input or output from these lines. In order to properly transfer this data, the PIO bus must be augmented by an address provided by the sixteen bits of the "extended" Arithmetic Register (XAR). Another necessary output is one that informs the external device when the ATAC is ready for the transfer. On the ATAC this function is provided by the Input/Output Demand (IOD) line. Referring to the timing diagram in Figure 4, an input command is initiated by placing an address on the XAR lines and following this address with a low on the IOD. This signifies that the ATAC register is ready for data. After approximately one microsecond, the IOD is placed high and the address is removed. During this microsecond the data for the ATAC must be stable. For an output command, the XAR and the PIO lines first present the address and data for output. When they

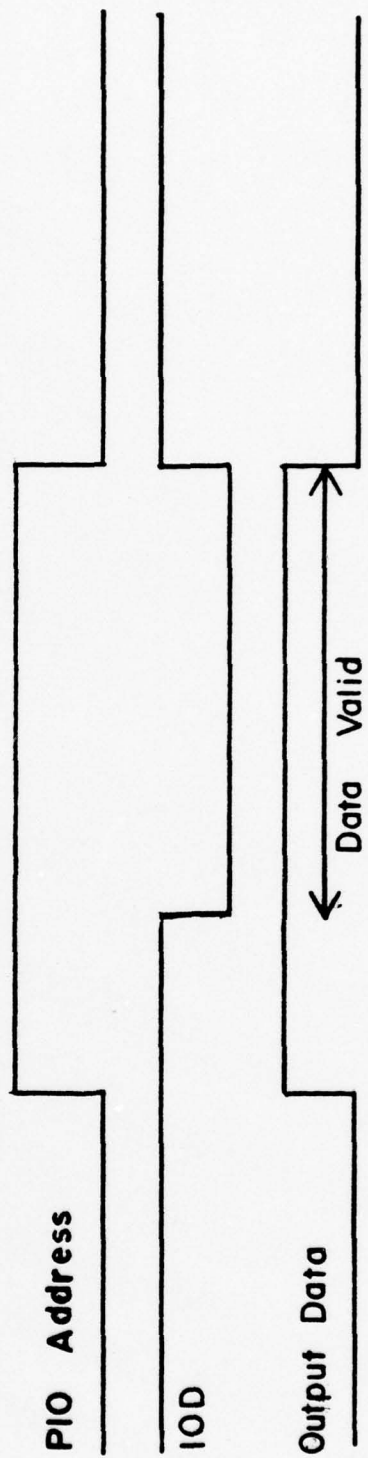
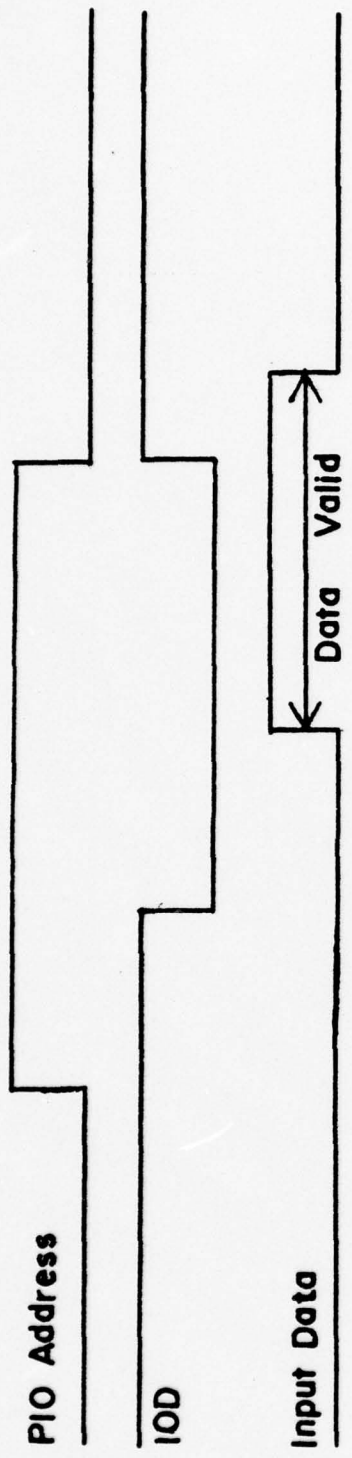


Figure 4
ATAC I/O Timing Diagram

are stable, the IOD line is set low. The data is then available for about a microsecond, as before. The IOD line is then placed high and the address and data are removed from their respective lines.

For operator interaction a serial ASCII, RS232 I/O port is also available. A Datamedia Elite 2500 television terminal is connected here to provide the operator with the necessary control and programming capability for use of the ATAC. By proper programming and use of the XAR lines, it was possible to translate each command for the interface. Using a demultiplexer on the interface board, four of the five available XAR addresses were separated into sixteen separate commands. One of the remaining lines and the IOD line were used as strobes to identify the receiver and to signify stable data (Chapter III). This arrangement provided both the adequate isolation and flexible operation desired.

B. THE KIM-1

The KIM-1 is at the other end of the computer spectrum with respect to the ATAC. It is a microprocessor designed around the MOS Technology Inc. series MCS6500 Central Processor Unit. Complete on a single printed circuit board, the KIM-1 is simple to operate and easy to program. While its cycle time is slower than that of the ATAC, it is still much faster than the receiver and more than adequate to meet

the requirements. Since the input data came from converted analog data supplied from the receiver's FM IF output (J9) and an external A/D converter, the design for this portion of the interface was simpler.¹

¹For a more detailed discussion of the KIM-1, its objectives, programs, and operating procedures, see Signal Acquisition and Sampling Using a Microprocessor, by LT. D. Rosenberger.

IV. THE INTERFACE

The interface was initially designed solely for the ATAC. A means of converting four ATAC words into one receiver word was needed first, in order to test the program, the computer, and the receiver together. The simplest and cheapest way to accomplish this conversion and still fully utilize the capabilities of the ATAC was to build a 32-bit register using eight parallel-in, serial-out, eight-bit shift registers. A control section was also necessary to properly handle this data. The ATAC XAR addresses were decoded by this control section to provide the load commands for the registers and to signal the receiver to input the word.

The next step in construction was also simple in theory. Since the computer uses the PIO lines for input as well as output, what was needed was a connection which would not interfere with the section already built. The ICs chosen to isolate the two sections are called Tri-State. These ICs have a "no output" state in addition to the normal high and low of TTL circuits. They could not, however, sink or supply enough current to drive the computer PIO bus. A solution was found by following these ICs with open collector buffers. Not only did they provide the necessary amplification, they did not degrade the isolation performance of the Tri-States. This second section also had

a 64-bit register built from the smaller shift registers. In this case, though, they were serial-in, parallel-out. In order to remove the word from the register in sixteen-bit sections, the outputs from the shift registers were connected to four-to-one multiplexers. These multiplexers were Tri-State. With the proper control it was possible to shift the word from the receiver into this register, and transfer it to the PIO bus in the correct sequence.

Increased complexity in the control section came with this implementation. A method was needed to prevent the computer from transferring a word until it had been completely shifted into the register. The period two clock output from the receiver was used as a reference to provide a pulse to inform the computer when shifting was complete. This pulse was positioned in the same time interval as period three of the receiver. The additional benefit of identifying period four was obtained. This meant that the output for the tri-state line to the receiver could be shorter and still meet the requirement to occur in a portion of period four.

After completion of the testing for the ATAC, an interface was designed and constructed for the KIM-1. This design was very simple to implement, since all the necessary timing circuits were already built and tested. The two computers were kept from interfering with each other by installation of a manual switch. This switch controls the

address of a multiplexer that separates the lines in the interface common to both computers. The control section was wire-wrapped rather than placed on a printed circuit board to provide greater flexibility, easier maintenance, and to reduce cost.

A. THE CONTROL SECTION

The heart of the interface is the control section (Figures 5 and 6). The main purpose of this section is to decode and route commands from the ATAC and provide the necessary circuits to interface with the receiver. It also contains the circuits for the operation of the receiver by the KIM-1. The receiver's outputs are driven by line drivers which provide complementary TTL levels. The inputs are applied to line receivers which accept these complementary TTL levels. The interface, therefore, had to use these same receivers and drivers to be compatible with the Watkins Johnson.

The SPST switch mounted on the front of the interface case selects the computer controlling the receiver. With the switch in the ATAC position, a high is placed on pin 1 of IC-JJ and pins 1 and 10 of IC-MM. IC-JJ is now set up to transfer the following: the address and data outputs to the line drivers on IC-LL, the trigger command to pin 2 of IC-Z, and a low in line CCK7. The CCK7 line completes the

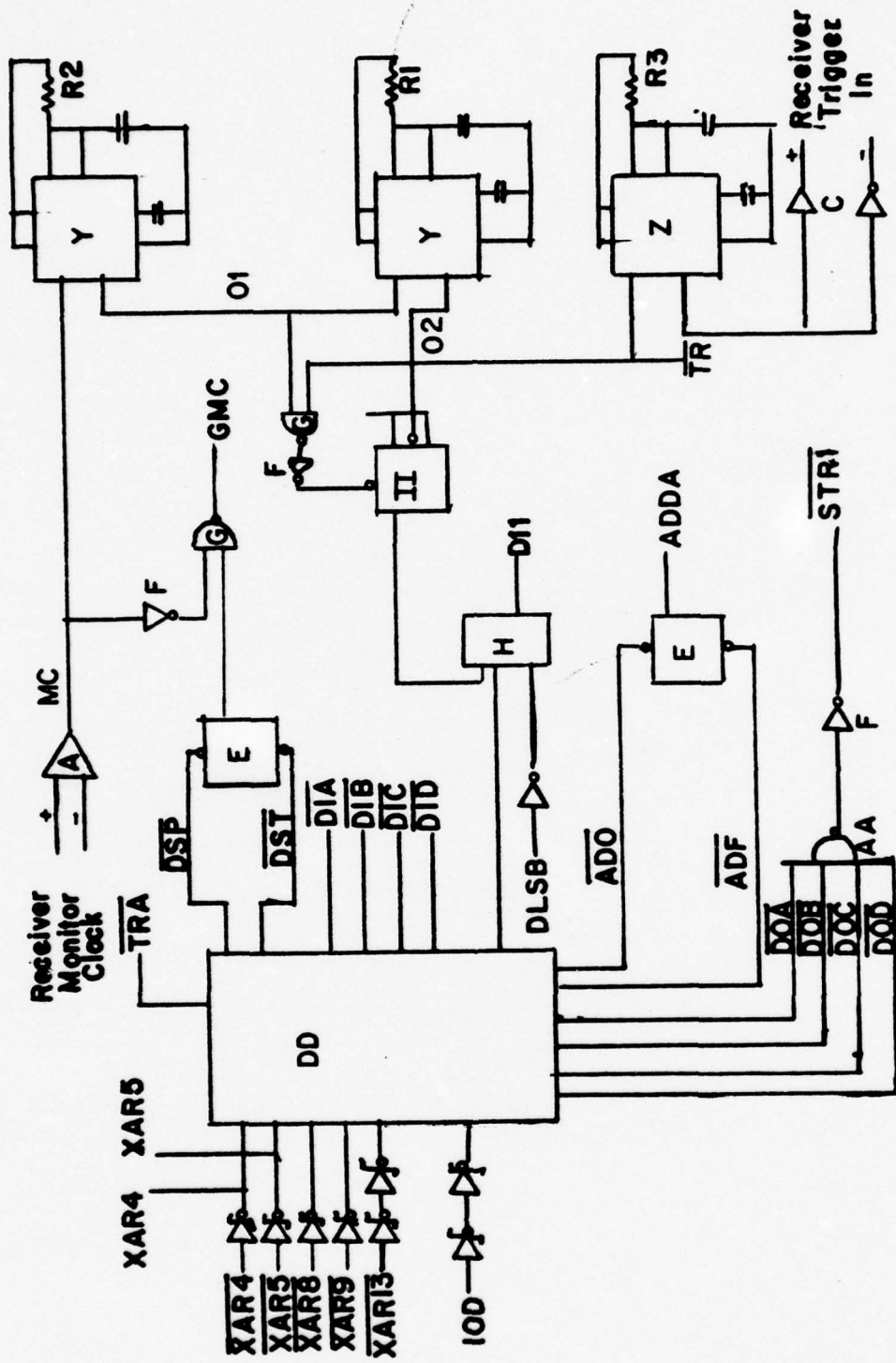


Figure 5
Interface Control Section (Part I)

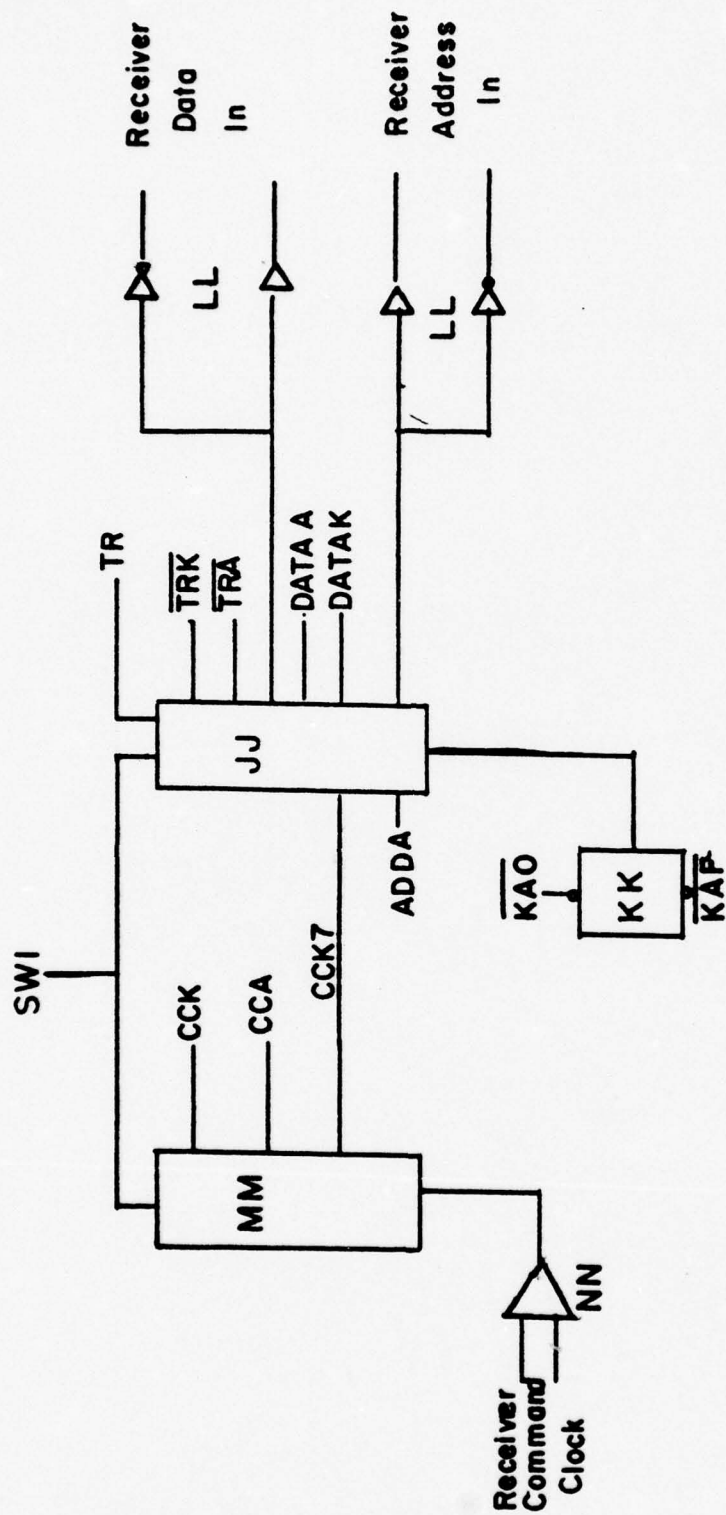


Figure 6
Interface Control Section (Part 2)

TRA	[0]	ATAC Trigger Command
DIA	[1]	Receiver input word one
DIB	[2]	Receiver input word two
DIC	[3]	Receiver input word three
DID	[4]	Receiver input word four
DSP	[5]	Stop Monitor Clock
DST	[6]	Start Monitor Clock
ADD	[7]	ATAC - Address on
DOA	[8]	Receiver output word one
DOB	[9]	Receiver output word two
DOB	[9]	Receiver output word two
DOC	[10]	Receiver output word three
DOO	[11]	Receiver output word four
RDY	[12]	Read D11 for ready signal
ADF	[15]	ATAC - Address off
TR		Receiver trigger
CCK		KIM-1 Command Clock
CCK7		Control Line for KIM-1 Command Clock
CCA		Command Clock for ATAC interface
ADDA		ATAC - Receiver Address
KADD		KIM-1 - Receiver Address
KAO		KIM-1 - Receiver Address On
KAF		KIM-1 - Receiver Address Off
TRK		KIM-1 Trigger Command

(Numbers in brackets refer to ATAC XAR commands)

Table III
Interface Command List

commands to IC-MM. This IC is a quad Tri-State buffer which is used to control the destination of the command clock. The switch opens buffer one which directs the command clock to the ATAC. The CCK7 line closes buffers two and four disabling the command clock input to the KIM-1.

The ATAC supplies the control section with six lines. Five of these are the XAP bits 4, 5, 8, 9, and 13. Using 4, 5, 8, and 9 as address lines to pins 20-23 of IC-DD, a four-to-sixteen demultiplexer, sixteen (2^4) unique commands (Table III) were made available. The sixth line, the IOD, and XAP 13 were used as strobes or enables for the demultiplexer. In this way XAP 13 was able to specify this receiver uniquely, and the IOD ensured that addresses and data were stable before passing a command. When both IOD and XAP 13 are low, IC-DD is operational and the output corresponding to the address on pins 20-23 is forced low. At any time that either or both the two strobe lines are high, all outputs of IC-DD are held high and no commands are generated, regardless of the activity on pins 20-23.

At the beginning of the Receiver Control program (Chapter IV), the ATAC sends commands to address the receiver (ADD) and to open the gate for the monitor clock (DST). ADD places a low on pin 2 of IC-E, setting the flip-flop and forcing the ADDA line high. This line activates the receiver's I/O through ICs -JJ, -A, -B, -C, and -LL, as described above. The DST command is passed to

pin 7 of IC-E. This sets this flip-flop and allows the monitor clock (MC) to shift data from the receiver into the storage register during every period two of the receiver's cycle. The MC line is also connected directly to a timing circuit. This circuit produces the pulse described in the early part of this chapter. The first of a pair of monostable multivibrators, IC-Y (Figure 5) is triggered by the first clock pulse of MC. IC-Y outputs a pulse, interval A of timing diagram (Figure 7), which triggers the second. The second's output, interval B, is connected to pin 1 of IC-II, a negative-edge triggered, J-K flip-flop. This IC is wired so that it is set on the output of the second multivibrator and reset by either the the output of the first multivibrator or the command TR. The output of this flip-flop, pin 15, is called the PLP. This line is multiplexed with the least significant bit of the output register and inverted by IC-D for use by the ATAC on line D11.

The RLP pulse is adjustable through variable resistors (trimmers) one and two. Trimmer one controls interval B and trimmer two interval A. In effect, trimmer two varies the position of the pulse and trimmer one its width. The placement and width are the key to proper operation of the interface. The pulse must remain in period three. Although some overlap into period four is allowable it is not desirable, and any overlap into period two could cause

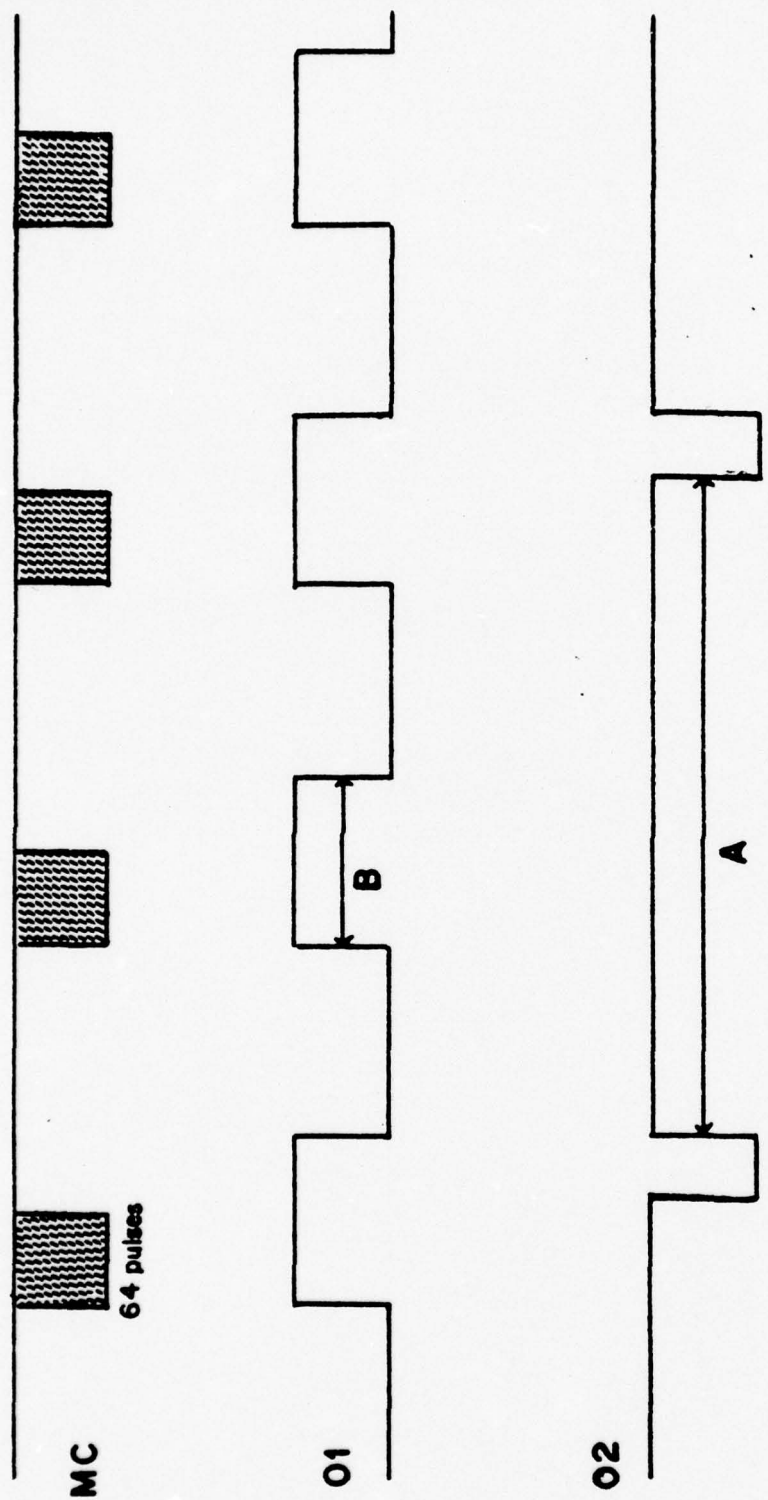


Figure 7
Interface Timing Diagram

incorrect operation. At present, the RLP pulse is programmed for every other period three. This allows the receiver to stabilize between samples taken by the computer. If more or less time is desired, the pulse can be set in every, every other, or every third period three by varying trimmer one. Greater time between pulses can be achieved by changing the .47 uF capacitor (hH-7,8) to one of larger value.

When the ATAC is ready to send a word, it loads the input register of the interface using commands DIA, DIB, DIC, DID, and then waits for a high on the D11 line. When RLP is low, D11 is high and the ATAC sends command TRA. This command is routed to a separate monostable multivibrator, IC-Z, by way of multiplexer IC-JJ. The timing circuit provides the trigger pulse in period four which changes the receiver's mode to remote active. It also sets RLP high to prevent any interaction with the ATAC until this cycle of the receiver is complete. During the following period one, the receiver sends the command clock to the input register via ICs -JJ and -MM, and inputs the data word through ICs -JJ and -LL. Meanwhile, the ATAC is waiting for RLP to go low again. When it does, the ATAC closes the MC gate with a DSP command and loads four sixteen-bit words with commands DDA, DDB, DDC, and DDD. Once the receiver word is stored in the ATAC, a DSIA command is sent to open the MC gate. When the operator has finished execution of the Receiver Control program and exits, the ATAC

sends the interface commands ADF and DSP to turn off the address line to the receiver and close the MC gate. The interface is now back in a stand-by status.

In order to set up the interface for operation with the KIM-1, the reset button must be pushed and the computer switch placed in the KIM-1 position. The reset button is unique to KIM-1 interface operation, and is necessary because of the use of the KIM-1's non-maskable interrupt. This interrupt is used to synchronize the KIM-1 with the receiver's command clock. Pressing the reset button places a momentary low on pin 5 of IC-KK, the flip-flop that controls the receiver's address line from the KIM-1. This resets the flip-flop and insures that the command clock output is disabled until required. ICs -JJ and -MM now transfer data from the KIM-1 and not the ATAC. The CCK7 line follows the address line from IC KK and gates the command clock off and on at the proper time. When the KIM-1 is ready to send a word to the receiver, it waits for a low on the RLP line. This line is connected to the maskable interrupt line. This low generates an interrupt and places the KIM-1 in the output program. This routine provides a trigger pulse for the trigger timing circuit and outputs the data synchronously with the command clock. The difference between the ATAC and KIM-1 actions of the interface is due to the position of the switch. The only function the interface serves is to provide reliable and compatible data to the appropriate device, whether it is receiver or

computer.

5. INPUT/OUTPUT REGISTERS

These two registers are used for the ATAC only. The registers were designated input or output by their related function with the receiver. They were constructed to provide the necessary, temporary storage while converting parallel and serial data back and forth. Both registers are connected to the PIO bus, with the major difference being the tri-state connections of the serial to parallel, or output register.

The input register (Figure 8) was the easier to implement. It consists of eight 8-bit shift registers with parallel input and serial output. The parallel input comes from the ATAC's PIO bus, which is buffered by schottky inverters to reduce noise. The lines are connected to the ICs in such a way as to load words into two adjacent shift registers simultaneously. This is possible because the shift registers will only latch data in when their respective load line is low. By proper connection of the DIA-DID lines to pin 1 of the ICs, and coordinating the commands with the data, the output register can be completely and correctly filled. The command clock from the receiver is connected to pin 15 of each of the eight registers. When it is present, it clocks the data through the register exiting through pin 16 of IC-VV. From here, it goes through the control section

at IC-MM and on to the receiver.

The output register (Figure 9) performs the reverse operation. However, in order to separate it into words that are short enough for the ATAC, the data has to be multiplexed before it can be connected to the PIO bus. The Tri-State multiplexers, ICs -I through -L and -U through -X, and the required buffers, ICs -FE through -GG, were used to prevent interaction with the PIO bus when not in use. The timing here is more critical than in the input register system. Before the ATAC begins a read cycle from the output register, the clock signal to the register is stopped (DSP). This prevents the ATAC from reading non-stationary data. All the Tri-State multiplexers are addressed by connecting XAR bits 4 and 5 to pins 2 and 4 respectively. The commands D0A-D0D are ANDed together (NANDed and inverted) and the output connected to all the multiplexers as strobes at pins 1 and 15. When the ATAC reads a word, the XAR bits select the word and the strobe produces it during the microsecond when the PIO bus is available.

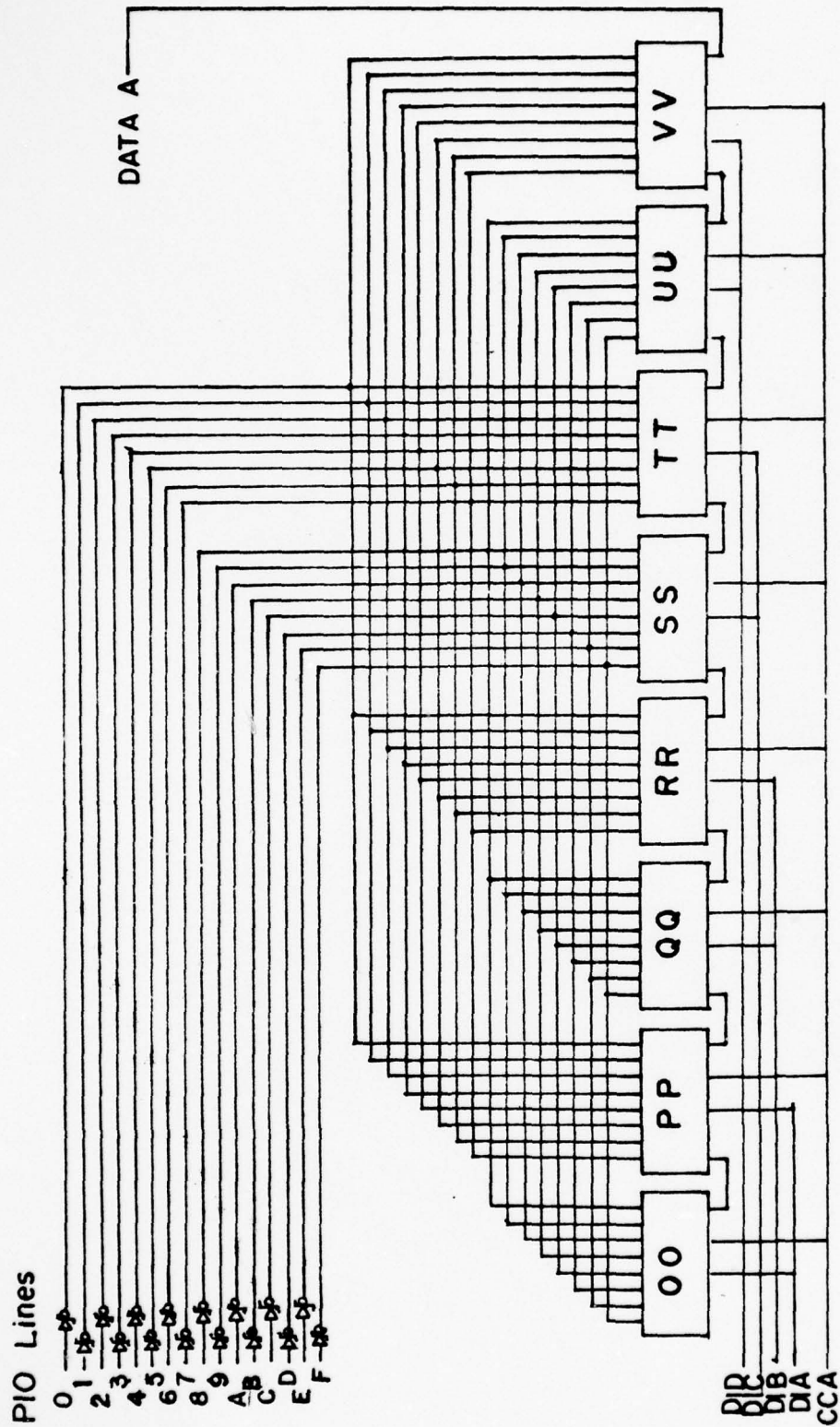


Figure 8
Input Register

PIO Lines

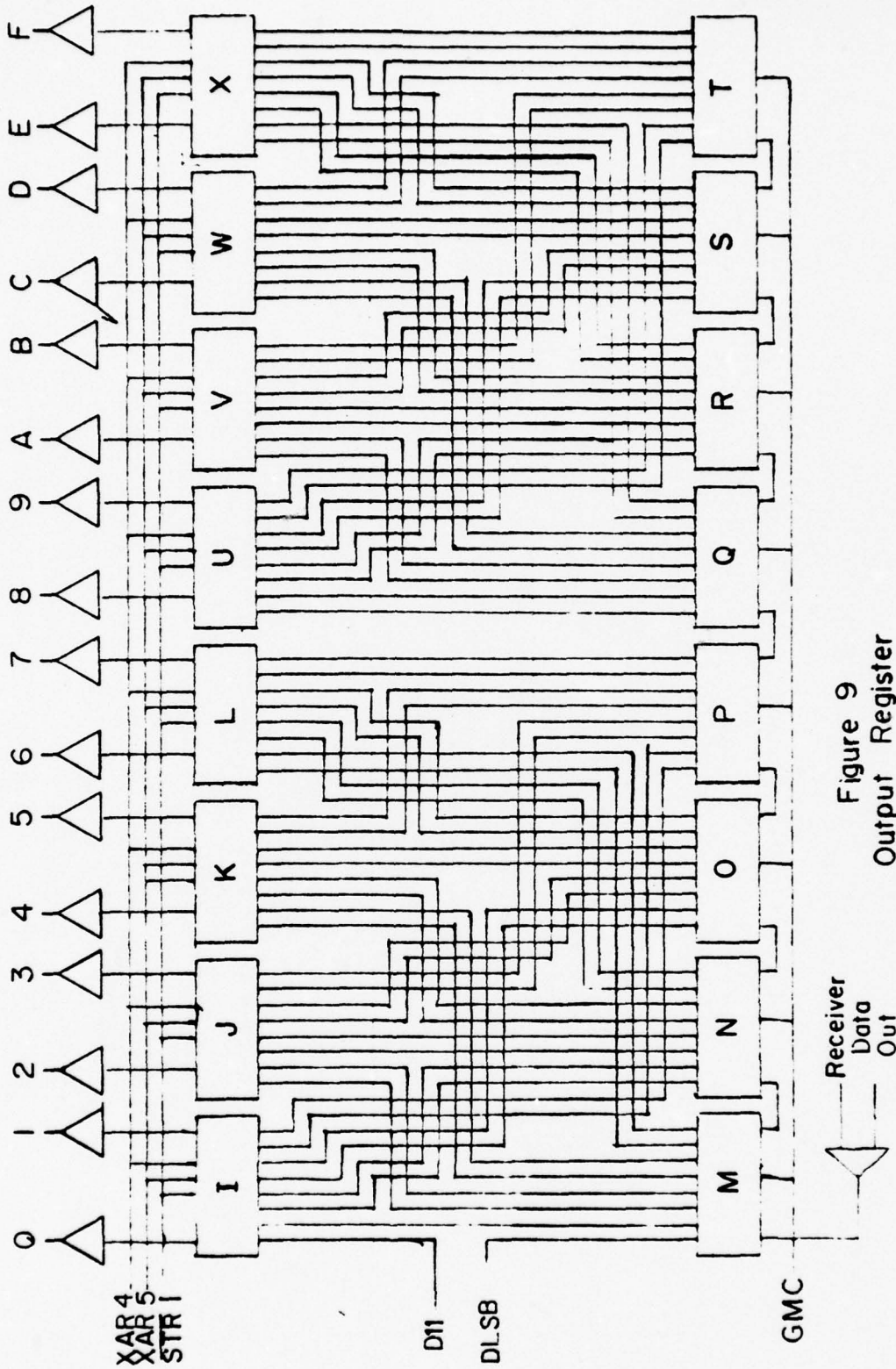


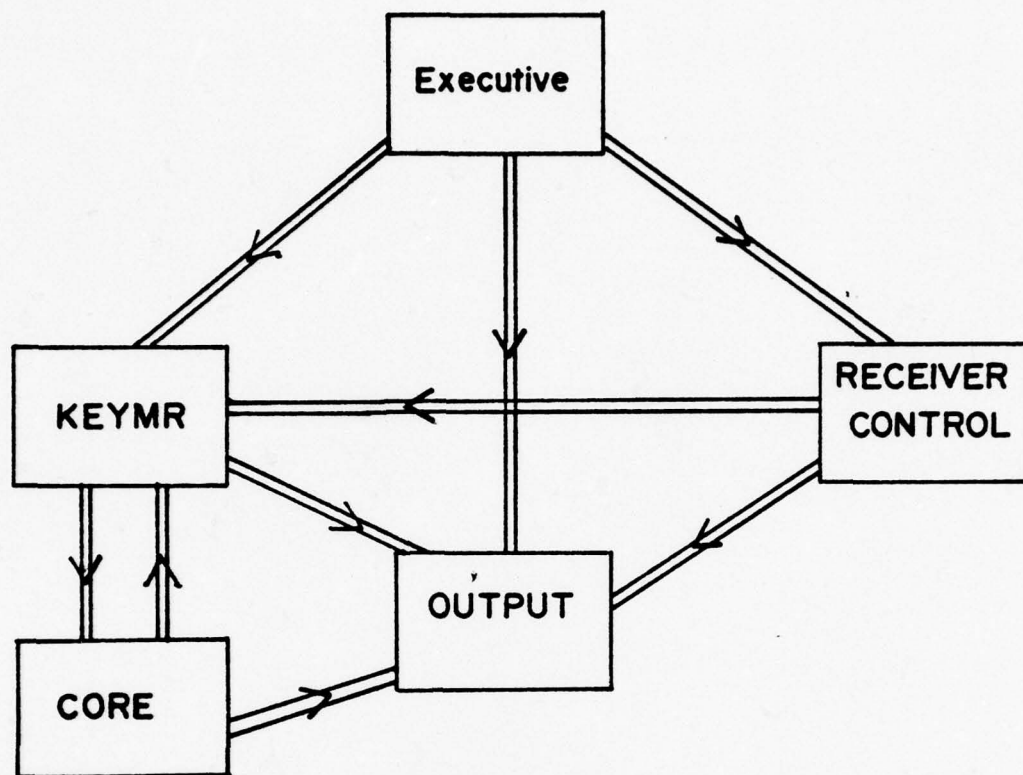
Figure 9
Output Register

V. THE PROGRAM

The ATAC program was written in two major sections; a system monitor and a control. The monitor is called the Main System and provides the operator the ability to program the ATAC from the operator's terminal. Receiver Control commands the interface and, therefore the receiver. Both programs were initially written prior to the construction of the interface, so many modifications were made using the Main System and its subroutines. After the interface was built and tested and the Receiver Control section modified to correctly control the tuning of the receiver, the complete program was saved on paper tape (Appendix C). Operation of the computer is discussed in Appendix B and a sample run can be found in Appendix F.

A. THE MAIN SYSTEM

The Main System section consists of a small executive and a group of interconnected subroutines (Figure 10). The executive provides a basis for the subroutines when the receiver control program is not being executed. It is these subroutines that control the input and output to the operator terminal. The input routine is called KEYMR and the output routine, OUTPUT. OUTPUT converts correctly-formatted computer words into ASCII and displays them on the



➡ ■ Subroutine Call

Figure 10
 ATAC Program Block Diagram

I. CORE Commands

- CO -- calls CORE from KEYMR.
- a. DU 'address' -- displays 80 memory locations beginning with 'address'.
- b. DT 'address' -- displays the contents of memory location 'address'.
- c. CH 'address' 'value' -- Replaces the contents of memory at 'address' with 'value'.
- d. CS 'address' -- Beginning at 'address', the contents of memory are replaced with the values typed on the lines following the command. Exit is accomplished by command DO.
- e. DO -- Returns execution to CORE if in CS, otherwise returns to calling routine.

II. Receiver Control Commands

- WJ -- Calls Receiver Control from the executive.
- a. 0 -- Set-up - Routine to input values for entry into Receiver.
- b. 1 -- Displays set-up control word.
- c. 2 -- Displays last control word sent to receiver.
- d. 3 -- Displays last control received from receiver.
- e. 4 -- Sends set-up control word to receiver.
- f. 5 -- Routine to input scan variables and execute a scan.
- g. 6 -- Receive and Display control word from the receiver.
- h. 7 -- Exit program and return to caller.
- i. 8 -- Reinitialize program as if entering.

Table IV
AFAC Program Commands

terminal. KEYMR does the reverse, and stores the input in a buffer for use by the caller. KEYMR and OUTPUT were programmed to accept and display only uppercase letters, numerals, and a small number of needed symbols. But, because of the method employed to convert ASCII to machine code, it was found that each lower case letter entered from the keyboard was automatically mapped into its respective upper case twin. This relieves the operator of the responsibility of using the shift key. A part of the KEYMR, called CORE, is available for use by the operator to display and/or change sections of memory. The four available commands in this routine and their functions are displayed in Table IV. Care must be taken not to change memory locations which are used by the Main System. This could result in complete erasure of the ATAC's memory. Without KEYMR, OUTPUT, and CORE, or routines similar to them, it would have been extremely difficult to perform any amount of troubleshooting or modification of the Receiver Control section.

8. RECEIVER CONTROL

This section of the system is a branch of the executive. Its main objective is to control both outputs and inputs of the interface from the operator's terminal. To assist those operators with little experience in this system, the Receiver Control section is equipped with uncomplicated

instructions and program safeguards. This produces almost foolproof operation but, it does so at the expense of program simplicity. Discussion of this section is separated into two parts. First a broad description of the complete section is discussed, followed by a detailed look at the two subroutines which interact with the interface.

When the Receiver Control program is entered, it performs five important actions. It initializes all necessary flags; enables the receiver and opens the MC gate; sends and receives a complete receiver word; and displays the instruction set to the operator. After this, it calls on KEYMR and waits for a command. When an input is delivered, the program checks its legality. If it is not a valid command, KEYMR is called again.

A valid command is a numeral between zero and eight (Table IV). These can be separated for discussion into three groups. The display group (0-3) inputs and exchanges information with the operator. The receiver group (4-6) performs operations with the receiver. The final group of commands (7-8) are used to exit or reinitialize the program. Group one has one input and three display commands. Command zero instructs the operator to input the parameters desired. It stores these parameters in memory in the display format, as opposed to control word format. Commands one, two, and three all display parameters. One displays the last parameters set-up by command zero. Two displays the last parameters sent to the receiver. Three displays the last

word received from the receiver. Commands seven and eight make up group three. Seven exits the program entirely and returns to the executive after disabling the receiver. Eight, on the other hand, returns the program to its beginning as if it had just been entered.

The remaining three commands are the most important. Group two commands control the actions of the interface. Command four converts the parameters set-up by command zero into control word format. It then calls the I/O subroutine described below, and outputs and inputs a receiver word. To merely receive a word from the receiver, command six is used. The program calls the input subroutine below and then exits to command three to display the parameters received. Command five scans a band of frequencies selected by the operator in search of a specified signal strength. All other parameters remain the same as those set-up by command zero.

With the exception of the instructions executed when entering and exiting Receiver Control, complete control of the interface and the receiver is resident in approximately forty computer instructions. These forty are grouped into the two subroutines WJR and WJS. WJS sends words to the receiver and WJR receives them. WJS loads the information and addresses to be sent to the receiver into the computer registers. The addresses are then matched to a word of data and sent to the interface input register. The routine now waits for the appropriate signal generated by RLP. When

this is received, a trigger command is sent to load the word into the receiver. At this point the routine checks the value of a counter. This test is to prevent the computer entering an infinite loop if either the interface or receiver is not turned on. If the test is unsatisfactory, the routine prints:

INFINITE LOOP
PLEASE CHECK RECEIVER AND INTERFACE

and reverts to operator control. If the test is satisfactory, the subroutine automatically continues to WJR. WJR loads another set of addresses into the computer registers. Here, a short wait for the RLP signal is necessary before any action is taken. The MC gate is closed immediately upon receipt of this signal. The receiver word is then loaded into the ATAC by outputting the address on the XAP lines and reading the data on the PIO lines. When the complete word is received, the MC gate is opened. At this point it is necessary to test for command six. This test determines whether the computer is sending and receiving or only receiving. If the execution of both WJS and WJR is being performed, a comparison between the word sent and the word received is necessary. This comparison is skipped if the computer is only executing WJR (command six). The first three control words sent by WJS and received by WJR are used for this comparison, when it is performed. If

any words differ, the computer returns to WJS to repeat the cycle until one of two conditions are met: either the words match or the WJS counter test discussed earlier fails. If the words match, WJP continues on to convert the received control words into the display format and then returns to the caller.

VI. RECOMMENDATIONS

The system as it stands now is but a beginning. Additions and modifications for future work should include; A/D converters for the receiver outputs; Morse and/or teletype decoders; and an expansion of the computer program. Implementation of either of the first two implies the third. There are some operator assistance program modifications that need to be made. The two that come immediately to mind are (1) a method to abort the scan routine from the operator's console, and (2) the ability to change individual parameters in addition to the set-up command already located in the program. Addition of the A/D converters implies a program increase to decode and process this new data. Switching routines and probably some hardware will be needed for the decoders. The capabilities of the system are limited only by the abilities of the operator and programmer.

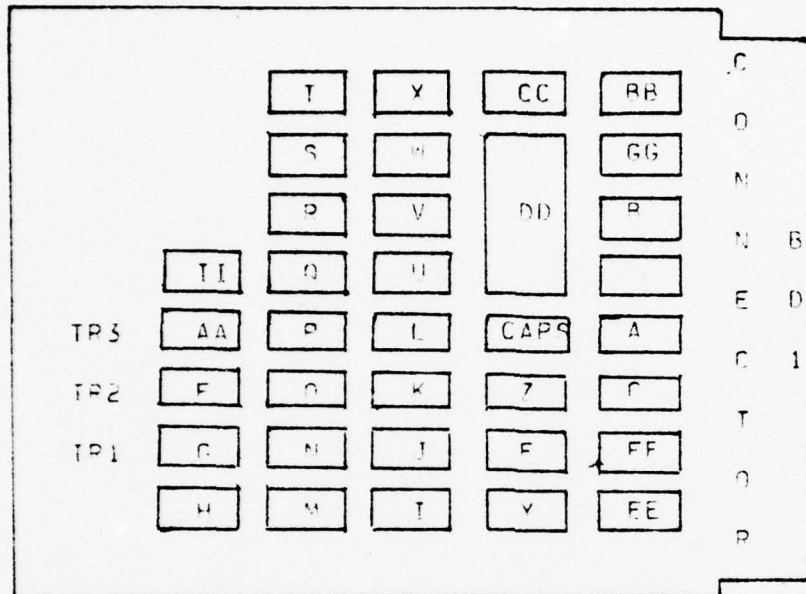
VII. CONCLUSION

As long as the computer requires only that data obtained from the receiver's word, the interface is flexible enough to provide reliable results. At this time there are no known "bugs" in either the interface or the program. Both have been thoroughly tested to provide the operator with the most dependable system possible.

APPENDIX A
INTERFACE WIRING

A. Board 1

Integrated circuit locations (from Top of board).



Integrated Circuit

Pin	A	B	C	F	F	G	H
1	8820 RD1-M	8820 RD1-K	8830 C-2/Z-3	7476 VCC	7404 M-3	7400 RD2-E	74157 DD-14
2	nc	nc	C-1/C-3	DD-8	H-3	Y-9	II-14
3	BD1-L	BD1-J	C-2/C-4	DD-15	A-6/Y-8	F-11	F-2
4	nc	nc	C-3	VCC	G-5	E-11	I-6
5	nc	nc	RD1-F	VCC	AA-6	F-4	nc
6	F-3/Y-8	M-1	BD1-H	VCC	G-12	F-9	nc
7	GRD	GRD	GRD	DD-7	GRD	GRD	GRD
8	nc	nc	nc	DD-6	I-8	nc	nc
9	nc	nc	nc	VCC	G-6	nc	nc
10	nc	nc	nc	nc	II-3	nc	nc
11	nc	nc	nc	G-4	G-3	F-13	nc
12	nc	nc	nc	VCC	I-15	F-6	nc
13	nc	nc	nc	GRD	G-11	DD-14	GRD
14	VCC	VCC	VCC	nc	VCC	VCC	VCC
15	XXXXXXXX	XXXXXXXX	XXXXXXXX	RD1-Y	XXXXXXXX	XXXXXXXX	XXXXXXXX
16	XXXXXXXX	XXXXXXXX	XXXXXXXX	VCC	XXXXXXXX	XXXXXXXX	XXXXXXXX

Integrated Circuit

	I	J	K	L	M	N
pin:	7214	7214	7214	7214	74164	74164
1	X-15	I-15/J15	J-15/K15	K-15/L15	R-6/M-2	M-13/N-2
2	DD22/J-2	I-2/K-2	J-2/L-2	K-2/U-2	M-1	N-1
3	S-3	S-5	S-10	S-12	F-1	U-6
4	Q-3	Q-5	Q-10	Q-12	I-10	U-10
5	Q-3	Q-5	Q-10	Q-12	J-6	V-6
6	H-4	M-5	M-10	M-12	J-10	V-10
7	EE-1	EE-5	EE-11	FF-1	GRD	GRD
8	GRD	GRD	GRD	GRD	N-8	M-8/O-8
9	EE-3	FE-9	EE-13	FF-3	VCC	VCC
10	M-4	M-6	M-11	M-13	K-6	W-6
11	Q-4	Q-6	Q-11	Q-13	K-10	W-10
12	Q-4	Q-6	Q-11	Q-13	L-6	X-6
13	S-4	S-6	S-11	S-13	L-10	X-10
14	DD23/J14	K-14/I14	J-14/L14	K-14/U14	VCC	VCC
15	F-12/J-1	J-1/K-1	K-1/L-1	L-1/U-1	XXXXXXXXXX	XXXXXXXXXX
16	VCC	VCC	VCC	VCC	XXXXXXXXXX	XXXXXXXXXX

	O	P	Q	R	S	T
pin:	74164	74164	74164	74164	74164	74164
1	N-13/O-2	O-13/P-2	P-13/Q-2	Q-13/R-2	R-13/S-2	S-13/T-2
2	O-1	P-1	Q-1	R-1	S-1	T-1
3	T-5	U-5	T-4	U-4	I-3	U-3
4	T-11	U-11	T-12	U-12	T-13	U-13
5	J-5	V-11	J-4	V-4	J-3	V-3
6	J-11	V-11	J-12	V-12	J-13	V-13
7	GRD	GRD	GRD	GRD	GRD	GRD
8	N-8/P-8	O-8/Q-8	P-8/R-8	Q-8/S-8	R-8/T-8	S-8/V-8
9	VCC	VCC	VCC	VCC	VCC	VCC
10	K-5	W-5	K-4	W-4	K-3	W-3
11	K-11	W-11	K-12	W-12	K-13	W-13
12	L-5	X-5	L-4	X-4	L-3	X-3
13	L-11	X-11	L-12	X-12	L-13	X-13
14	VCC	VCC	VCC	VCC	VCC	VCC

Integrated Circuit

	U	V	W	X	Y	7
pin:	7214	7214	7214	7214	556	555
1	L-15/U15	U-15/V15	V-15/W15	W-15/X15	Y2/Tr-1A	GRD
2	L-2/V-2	U-2/W-2	V-2/X-2	W-2	Y-1/HH-6	PD1-E
3	T-3	T-5	T-10	T-12	HH-5	C-1
4	R-3	R-5	R-10	R-12	VCC	VCC
5	P-3	P-5	P-10	P-12	BD1C/II1	HH-1
6	N-3	N-5	N-10	N-12	Y-9	HH-2/Z-7
7	FF-5	FF-11	GG-1	GG-5	GRD	Z6/Tr-3A
8	GRD	GPD	GRD	GRD	A-6/F-3	VCC
9	FF-9	FF-13	GG-3	GG-9	Y-6	XXXXXXXXXX
10	N-4	N-6	N-11	N-13	VCC	XXXXXXXXXX
11	P-4	P-6	P-11	P-13	HH-4	XXXXXXXXXX
12	R-4	R-6	R-11	R-13	Y-13/HH4	XXXXXXXXXX
13	T-4	T-6	T-11	T-13	Y12/Tr2A	XXXXXXXXXX
14	L-14/V14	U-14/W14	V-14/X14	W-14	VCC	XXXXXXXXXX
15	U-1/V-1	V-1/W-1	W-1/X-1	X-1/Y-1	XXXXXXXXXX	XXXXXXXXXX
16	VCC	VCC	VCC	VCC	XXXXXXXXXX	XXXXXXXXXX

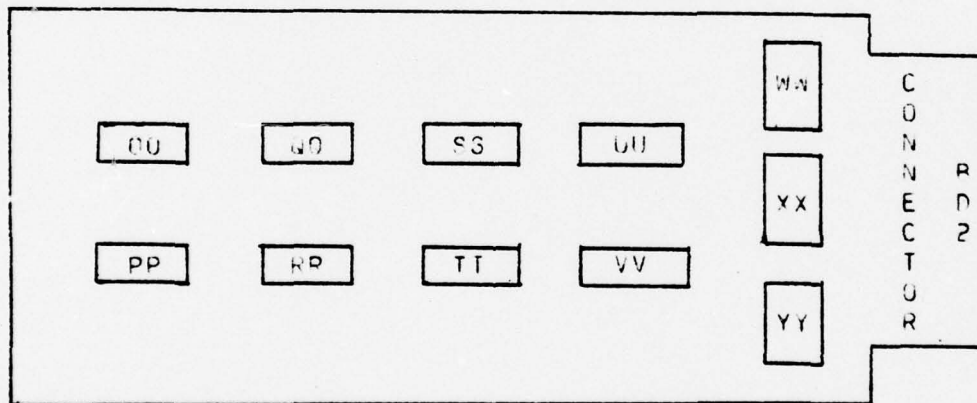
	AA	BB	CC	EE	FF	GG	HH	II
pin:	7420	74s04	74s04	7407	7407	7407	CAPS	7476
1	DD-10	CC-6	BD1-17	T-7	L-7	W-7	Z-5	Y-5
2	DD-11	DD-18	DD-23	RD1-1	BD1-7	BD1-13	Z-6	VCC
3	nc	BD1-21	nc	I-9	L-9	W-9	Y-11	F-10
4	DD-9	BR-5	nc	RD1-2	BD1-8	RD1-14	Y-12	VCC
5	DD-13	BR-4	RD1-1	J-7	U-7	X-7	Y-3	nc
6	F-5	DD-19	BB-1	RD1-3	BD1-9	RD1-15	nc	nc
7	GRD	GRD	GRD	GRD	GRD	GRD	Y-2	nc
8	nc	DD-20	nc	RD1-4	BD1-10	BD1-16	GRD	nc
9	nc	RD1-20	nc	J-9	U-9	X-9	GPD	nc
10	nc	DD-21	nc	RD1-5	BD1-11	nc	GRD	nc
11	nc	RD1-19	nc	K-7	V-7	nc	GRD	nc
12	nc	DD-22	nc	RD1-6	BD1-12	nc	GRD	nc
13	nc	RD1-18	nc	K-9	V-9	nc	GRD	GRD
14	VCC	VCC	VCC	VCC	VCC	VCC	GPD	H-2
15	XXXXXXXX	XXXXXXXX	XXXXXXXX	XXXXXXXX	XXXXXXXX	XXXXXXXX	XXXXXXXX	nc
16	XXXXXXXX	XXXXXXXX	XXXXXXXX	XXXXXXXX	XXXXXXXX	XXXXXXXX	XXXXXXXX	GRD

Integrated Circuit

		DD 74154			
pin		pin			
1	RD1-P	13	AA-5		
2	RD1-X	14	G-13/H-1		
3	RD1-W	15	nc		
4	RD1-V	16	nc		
5	RD1-U	17	F-3		
6	E-8	18	BR-2		
7	E-7	19	BR-6		
8	E-2	20	BR-8		
9	AA-4	21	BR-10		
10	AA-1	22	BR-12/I-2		
11	AA-2	23	CC-2/I-14		
12	GRD	24	VCC		

II. Board 2

Integrated Circuit Locations (from Top of board)



Integrated Circuit

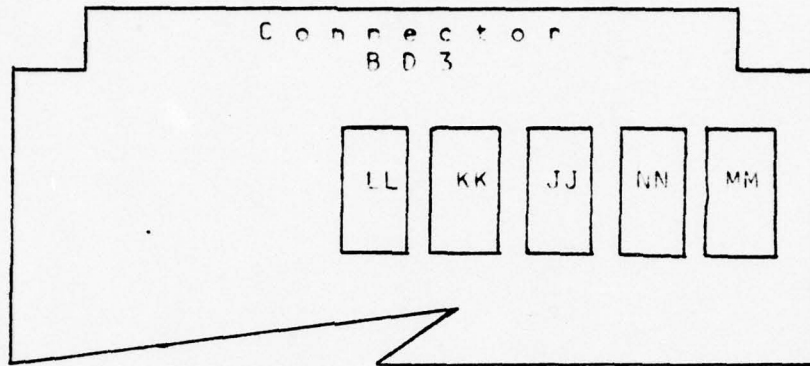
pin	OO	PP	QQ	RR	SS	TT
1	BD2-A	BD2-A	BD2-B	BD2-B	BD2-C	BD2-C
2	GRD	GRD	GRD	GRD	GRD	GRD
3	WW-2	YY-6	WW-2	YY-6	WW-2	YY-6
4	WW-12	YY-4	WW-12	YY-4	WW-12	YY-4
5	WW-4	YY-2	WW-4	YY-2	WW-4	YY-2
6	WW-10	YY-10	WW-10	YY-10	WW-10	YY-10
7	nc	nc	nc	nc	nc	nc
8	GRD	GRD	GRD	GRD	GRD	GRD
9	PP-10	QQ-10	RR-10	SS-10	TT-10	UU-10
10	VCC	QQ-9	PP-9	QQ-9	RR-9	SS-9
11	XX-2	XX-4	XX-2	XX-4	XX-2	XX-4
12	XX-12	XX-6	XX-12	XX-6	XX-12	XX-6
13	WW-8	XX-8	WW-8	XX-8	WW-8	XX-8
14	WW-6	YY-12	WW-6	YY-12	WW-6	YY-12
15	BD2-21	BD2-21	BD2-21	BD2-21	BD2-21	BD2-21
16	VCC	VCC	VCC	VCC	VCC	VCC

pin	UU	VV	WA	YX	YY
1	BD2-D	BD2-D	BD2-17	BD2-10	BD2-4
2	GRD	GRD	QQ-3 *	QQ-11 *	PP-5 *
3	WW-2	YY-6	BD2-15	BD2-9	BD2-3
4	WW-12	YY-4	QQ-5 *	PP-11 *	PP-4 *
5	WW-4	YY-2	BD2-13	BD2-8	BD2-2
6	WW-10	YY-10	QQ-14 *	PP-12 *	PP-3 *
7	nc	nc	GRD	GRD	GRD
8	GRD	GRD	QQ-13 *	PP-13 *	nc
9	VV-10	BD2-K	BD2-12	BD2-7	nc
10	TT-9	UU-9	QQ-6 *	nc	PP-6 *
11	XX-2	XX-4	BD3-14	nc	BD2-5
12	XX-12	XX-6	QQ-4 *	QQ-12 *	PP-14 *
13	WW-8	XX-8	BD2-16	BD2-11	BD2-6
14	WW-6	YY-12	VCC	VCC	VCC
15	BD2-21	BD2-21	XXXXXXXXXX	XXXXXXXXXX	XXXXXXXXXX
16	VCC	VCC	XXXXXXXXXX	XXXXXXXXXX	XXXXXXXXXX

* - bus connection - only first connection shown

C. Board Three

Integrated circuit locations (from Top of board)



Integrated Circuit

pin	JJ	KK	LL	MM	NN
1	74157	7476	8830	74126	8R20
2	BD3-1	VCC	LL-2/JJ-7	BD3-1	BD3-5
3	BD3-6	BD3-11	LL-1/LL-3	BD3-17	nc
4	LL-10	BD3-12/20	LL-2/LL-4	BD3-16	BD3-4
5	BD3-8	nc	LL-3	JJ-12	nc
6	BD3-9	VCC	BD3-7	GRD	nc
7	LL-1	nc	BD3-10	BD3-16	MM-9
8	GRD	nc	GRD	GRD	GRD
9	BD3-13	nc	BD3-19	BD3-2	nc
10	BD3-14	nc	BD3-18	NN-8	nc
11	nc	nc	LL11/JJ-4	BD3-1	nc
12	MM13/MM-4	nc	LL12/LL10	BD3-3	nc
13	GRD	GRD	LL13/LL11	MM-9	nc
14	KK-15	nc	LL-12	JJ-12	nc
15	GRD	JJ-2/JJ14	VCC	VCC	VCC
16	VCC	nc	XXXXXXXXXX	XXXXXXXXXX	XXXXXXXXXX

D. Connectors

Circuit Board Edge-connectors

	RD1	RD2	RD3
1	EE-2/RD2-10/CN1-2	GRD	SW1/JJ-1/MM-10
2	EE-4/RD2-11/CN1-3	YY-5/RD1-13	MM-8/RD2-21
3	EE-6/RD2-12/CN1-4	YY-3/RD1-14	MM-11/CN3-1
4	EE-8/RD2-13/CN1-5	YY-1/RD1-15	NN-2/CN2-2
5	EE10/RD2-17/CN1-6	YY-11/RD1-16	NN-1/CN2-3
6	EE12/RD2-16/CN1-7	YY-13/RD1-12	JJ-3/RD1-Y
7	FF-2/RD2-15/CN1-8	XX-9/RD1-11	LL-5/CN2-10
8	FF-4/RD2-14/CN1-9	XX-5/RD1-10	JJ-5/CN3-2
9	FF-6/RD2-9/CN1-10	XX-3/RD1-8	JJ-6/RD2-K
10	FF-8/RD2-8/CN1-11	XX-1/RD1-1	LL-6/CN2-11
11	FF10/RD2-7/CN1-12	XX-13/RD1-2	KK-2/CN3-3
12	FF12/RD2-6/CN1-13	WW-9/RD1-3	KK-3/CN3-4
13	GG-2/RD2-2/CN1-25	WW-5/RD1-4	JJ-9/RD1-F
14	GG-4/RD2-3/CN1-24	WW-11/RD1-8	JJ-10/RD1-P
15	GG-6/RD2-4/CN1-23	WW-3/RD1-7	JJ-11/CN3-5
16	GG-8/RD2-5/CN1-22	WW-13/RD1-6	nc
17	CC-1/CN1-11	WW-1/RD1-5	nc
18	BB-13/CN1-17	nc	LL-9/CN2-12
19	BB-11/CN1-19	nc	LL-8/CN2-11
20	BB-9/CN1-18	nc	KK-3/RESET
21	BB-3/CN1-20	GG-15/RD3-2	nc
22	nc	GRD	VCC
A	GRD	DD-1/RD1-X	GRD
B	nc	GG-1/RD1-W	nc
C	II-14/CN3-6	SS-1/RD1-V	nc
D	nc	UU-1/RD1-U	nc
E	7-2/RD3-13	nc	nc
F	C-5/CN2-9	nc	nc
H	C-6/CN2-8	nc	nc
J	B-3/CN2-7	nc	nc
K	B-1/CN2-6	nc	nc
L	A-3/CN2-5	nc	nc
M	A-1/CN2-4	nc	nc
N	nc	nc	nc
P	DD-1/RD3-14	nc	nc
R	nc	nc	nc
S	nc	nc	nc
T	CC-5/CN1-14	nc	nc
U	DD-5/RD2-D	nc	nc
V	DD-4/RD2-C	nc	nc
W	DD-3/RD2-B	nc	nc
X	DD-2/RD2-A	nc	nc
Y	E-15/RD3-6	nc	nc
Z	VCC	nc	nc

Cabinet Connectors

pin:	CN1	CN2	CN3
1	GRD	GRD	BD3-3 "CCK"
2	BD1-1 "PIO 0"	RD3-4 "CC -"	BD3-8 "DATA K"
3	BD1-2 "PIO 1"	RD3-5 "CC +"	RD3-11 "KAC"
4	BD1-3 "PIO 2"	RD1-M "MC -"	RD3-12 "KAF"
5	BD1-4 "PIO 3"	RD1-L "MC +"	RD3-15 "TRK"
6	BD1-5 "PIO 4"	RD1-K "DO -"	RD1-3 "PLP"
7	BD1-6 "PIO 5"	RD1-J "DO +"	nc
8	BD1-7 "PIO 6"	RD1-H "TR -"	nc
9	BD1-8 "PIO 7"	BD1-F "TR +"	nc
10	BD1-9 "PIO 8"	RD3-7 "DI -"	nc
11	RD1-10 "PIO 9"	RD3-10 "DI +"	nc
12	BD1-11 "PIO 10"	BD3-19 "AD -"	nc
13	BD1-12 "PIO 11"	BD3-18 "AD +"	nc
14	BD1-T "IOU"	nc	GRD
15	nc	nc	nc
16	BD1-17 "XAR 4"	nc	nc
17	RD1-18 "XAR 5"	nc	nc
18	BD1-20 "XAR 9"	nc	nc
19	RD1-19 "XAR 8"	nc	nc
20	BD1-21 "XAR 13"	nc	nc
21	nc	nc	nc
22	BD1-16 "PTO-15"	nc	nc
23	BD1-15 "PTO-14"	nc	nc
24	BD1-14 "PTO-13"	nc	nc
25	BD1-13 "PTO-12"	nc	nc

E. Discrete Components

Capacitors

.01 uF	HH-1/HH-14
.2 uF	HH-2/HH-13
.20 uF	HH-3/HH-12
.01 uF	HH-4/HH-11
.01 uF	HH-5/HH-10
.47 uF	HH-7/HH-8

Resistors

		Trimmer		
		1	2	3
A	Y-1	Y-13	Z-7	
B	VCC	VCC	VCC	
C	VCC	VCC	VCC	

APPENDIX B
ATAC OPERATING INSTRUCTIONS

Power Up

Turn on front panel power then turn on power supplies.

Power Down

'Halt'

'Master Clear'

Power off to supplies, power off to control panel.

Run Program

'Master Clear'

Dial 'IMR'

'AUX REG'

'ENTER' (associated with AUX REG)

'MEMORY'

Set start address +1 in keyboard (Hexadecimal)

'PCP'

'ENTER' (associated with PCP)

'RUN'

Stop a Program

'HALT'

Read Memory (from front panel)

'HALT'

Set desired address in key board

Select 'MAR'

'ENTER' (associated with MAP)

'INC' (increment)

'DEC' (decrement)

Address is displayed above MAR key, data is displayed in red LEDs above MEMORY key.

Use INC or DEC as necessary to arrive at memory location desired.

Write into Memory (from front panel)

'HALT'

Set address desired as described in Read Memory.

Set desired data into keyboard

'ENTER' (associated with MEMORY)

Value in keyboard will be entered into either Memory (MEMORY) or A computer Register (FILE).

Bootstrap Load (paper tape)

'HALT'

'MASTER CLEAR'

'AUX REG'

Dial 'IMR'

'ENTER' (associated with AUX REG)

'MEMORY'

Set 0001 in keyboard

(0001 = Load, 0002 = Verify only)

'RUN'

At end of tape check program status lights (red LEDs
below PCR and MAR pushbuttons)

0000 = Load good

FFFE = Parity error

FFFD = Verify error

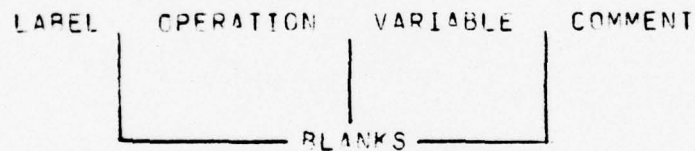
APPENDIX C

ATAC PROGRAM ASSEMBLY

Assembly of a program is divided into five parts; writing, producing absolute deck on IBM 360, conversion of absolute deck into ATAC format, punching paper tape, and loading ATAC.

A. Writing the program.

Programs for the ATAC must be written in the assembly language described in ATAC manuals Volumes One, and Eight. The finished program must be placed on cards for the IBM 360 in the following format:



B. Producing an absolute deck

The first step is to load the assembler on to the IBM 360 from magnetic tape. This is done by executing program A in Appendix E. This transfers the program from tape to disk and saves it for one year. Once the assembler is stored the

following cards placed in the front of a program written following the instructions in I above will produce an absolute deck and a print-out of the program.

```
//ATACASSM JOB (0729,0194,0052),'CCH ATAC ASSEM,',TIME=1
//ASSEM EXEC PGM=APSS,REGION=220K
//STEPLIB DD USN=S0729.ATAC.QNF,UNIT=3330,
// VOL=SER=DISK02,DTSP=SHR
//FT06F001 DD SYSOUT=A
//FT07F001 DD SYSOUT=B
//FT05F001 DD DDNAME=SYSIN
//FT08F001 DD UNIT=SYSDA,SPACE=(CYL,1)
//FT09F001 DD UNIT=SYSDA,SPACE=(CYL,(7,2)),
// DCB=(RECFM=VRS,BLKSIZE=7180,LRECL=92)
//FT10F001 DD UNIT=SYSDA,SPACE=(CYL,(7,2)),
// DCB=(RECFM=VRS,BLKSIZE=4204,LRECL=42)
//FT20F001 DD UNIT=SYSDA,SPACE=(CYL,(7,2)),
// DCB=(RECFM=VRS,BLKSIZE=2004,LRECL=500)
//SYSPRINT DD SYSOUT=A
//SYSIN DD *
$JOB
$ASSEM
IDT ATAC
```

(place written program here.)

```
END
$BASE
$LOAD P
$END
```

The absolute deck is in the form:

```
0500169c0009adbc3109bc9c3109c8bc7109c09c7109d8b60309bbd932/
/e10100009c015a
05101609f49c0109f6ed0008aee1010a02ed000230e10109f7ed0007ae/
/c1070509201258
```

which must be translated for the ATAC. The memory location of the first word is located in the first four columns.

Columns five and six contain the number of word fields on the card. The assembled program is located in columns 7 - 70. The remaining two columns are parity.

C. Conversion

The absolute deck received from the IBM 360 is loaded into the PDP-11. After the data from the cards is checked, the conversion program (convert 'filename' 'filename') can be executed. (Program C in Appendix F)

D. Punching Paper Tape

This code must then be transferred to the PDP-11 (A) where a paper tape can be punched. Here, the command to punch a tape is:

```
cat 'filename' >/dev/otc
```

E. Loading the ATAC

In order to load a tape the RS232 connector must be connected to the Paper Tape reader and the reader set to 1200 baud. The tape is loaded by following the instructions in Appendix B.

APPENDIX D
SAMPLE ATAC OUTPUT

Operator inputs are underlined.

Operator Display	*Comments
EXEC <u>+JDKDKJFJJjnmun</u> JDKDKJFJJJNMUH	*Executive echos *entries other than *commands
EXEC <u>+CO</u>	*Entry into CORE
CORE <u>+CH 0F00 0900</u> 0F00 0900	*Location 0F00 *changed to 0900
CORE <u>+DI 0F00</u> 0F00 0900	
CORE <u>+CS 0F00</u> <u>+0256</u>	*Locations 0F00 to 0F03 *changed
<u>+0123</u>	
<u>+4567</u>	
<u>+DO</u> 0F00 0256 0F01 0123 0F02 4567	*completion of change
CORE <u>+DO</u>	*Exit from CORE
<u>+WJ</u> 0=SET-UP 1=DISPLAY TENTATIVE 2=DISPLAY CONTROL 3=DISPLAY RECEIVED 4=ENTER TENTATIVE 5=SCAN	*Entry into Receiver *Control

6=RECEIVE CONTROL
7=DONE
8=REINITIALIZE

RECEIVER CONTROL

+0

FREQ(HZ)

+1240000

DETECT MODE

0=AM
1=FM
2=BFO FIXED
3=BFO VARIABLE
4=ISB
5=USB
6=LSB
7=AM-ML

+0

GAIN MODE

0=HOLD AGC
2=NORMAL AGC
3=MANUAL AGC

+0

IF BANDWIDTH

1=500 HZ
2=2 KHZ
3=4 KHZ
4=8 KHZ

+4

RF GAIN
(PERCENTAGE)

+88

RECEIVER CONTROL

+1

FREQ = 1240000 HZ
GAIN MODE = HOLD AGC
IF BANDWIDTH = 8 KHZ
DETECT MODE = AM
BFO FREQ = 455000 HZ
RF GAIN = 88%

RECEIVER CONTROL

+2

FREQ = 550000 HZ
GAIN MODE = NORMAL AGC
IF BANDWIDTH = 8 KHZ
DETECT MODE = AM
BFO FREQUENCY = 455000 HZ
RF GAIN = 85%

RECEIVER CONTROL

+3
FREQ = 550000 HZ
GAIN MODE = NORMAL AGC
IF BANDWIDTH = 8 KHZ
DETECT MODE = AM
BFO FREQUENCY = 455000 HZ
RF GAIN = 85%
SIGNAL STRENGTH = 66%

RECEIVER CONTROL

+4

RECEIVER CONTROL

+5

SCAN

START FREQ IN HZ

+1000000

END FREQ IN HZ

+1008000

FREQ INCREMENT IN HZ

+1000

SIGNAL STRENGTH %

+67

FREQ = 1001000
GAIN MODE = HOLD AGC
IF BANDWIDTH = 8 KHZ
DETECT MODE = AM
BFO FREQUENCY = 455000 HZ
RF GAIN = 88%
SIGNAL STRENGTH = 72%

RECEIVER CONTROL

+7

EYEC

+

*Exit from
*Receiver Control

APPENDIX E
CONVERSION PROGRAMS FOR THE ASSEMBLER

A. This program is run on the IBM-360 to transfer the ATAC assembler from tape ATI-006 to Disk and stores it there for one year.

```
// (GREEN JOB CARD)
//SYSPRINT DD   SYSOUT=A
//SYSUT1   DD   UNIT=SYSDA,SPACE=(TRK,(40),,CONTIG)
//DA1      DD   UNIT=2314,DSN=S0729.ATAC.ONE,
//          DD   SPACE=(TRK,(50,10,10),,CONTIG),
//          DD   DISP=(NEW,KEEP),VOL=SER=SPOOL3
//T1TAPE DD UNIT=(2400,,DEFER),DISP=(NEW,PASS),
//          DD   LABEL=(3,SL,,IN),
//          DD   DCB=(DEN=2,BLKSIZE=800,LRECL=80,RECFM=FB),
//          DD   VOL=SER=ATI006
//SYSIN     DD   *
//          COPY PDS=ATI.APSS.LOADLIB,TO=2314=SPOOL3,
//          FROMDD=T1TAPE,FROM=2400=(ATI006,3),
//          RENAME=S0729.ATAC.ONE

/*
//BUILD     EXEC PGM=IEWL,REGION=150K,
//          PARM='OVLY,XREF,LET,LIST,SIZE=(256K,20480)'
//SYSPRINT DD   SYSOUT=A
//LIBRARY DD DSN=S0729.ATAC.ONE,UNIT=2314,VOL=SER=SPOOL3,
//          DISP=SHR
//SYSLIB DD DSNAME=SYS1.FORTLIB,DISP=SHR
//SYSLMOD DD DSNAME=S0729.ATAC.ONE,
//          UNIT=3330,VOL=SER=DISK02,
//          DISP=(NEW,KEEP),LABEL=RETPD=360,
//          SPACE=(CYL,(5,1,2),RLSE)
//SYSUT1 DD UNIT=SYSDA,SPACE=(TRK,(19,19),,CONTIG),
//          SEP=SYSLMOD
//SYSLIN DD *
//          INCLUDE LIBRARY(PMODL)
//          CHANGE MSIM(IHESAPD)
//          INCLUDE LIBRARY(APSSMON)
//          INCLUDE LIBRARY(MSIM4,ASEM5,SIM16A,SIMTR1,SIMIO1,GUL)
//          INCLUDE LIBRARY(XPLMON)
//          OVERLAY A1
//          INSERT MSIMUL,*MSIMOLA,IHENTRY,IHESAP
//          INSERT XINT,IN,OUT
//          INSERT IHEDBN,IHXTD
```

```

INSERT IHERSM, IHECSM
INSERT IHERSK, IHEIOX, IHEIOP, THEIDID, IHEDOB
INSERT IHEDIR, IHEDCA, IHEIOB
INSERT IHEIOA, IHEOCL
INSERT IHERSD, IHEBSF
INSERT IHEJXS
INSERT IHEOSD, IHEOST, IHERST
INSERT IHEVPF, IHEDMA, IHEVFR
INSERT IHEDNC, IHEVFD, IHEVFA, IHEVPD, IHEVPB, IHEVSC
INSERT IHEVSD, IHEVFE, IHEDCN, IHEUPB
INSERT IHEVFC, IHEVPE, IHEVPG, IHEVOB, IHEVGC
INSERT IHEABN, IHEIOD, IHEIOF, IHEPRT, IHEVQA, IHESPRT
INSERT IHEREG, IHEERR, IHEISZ
INSERT MISEF
OVERLAY A1
INSERT ASSEM, REWIND, REW72, DSKOUT, CARDIN, DISKIN, ERPRT, PRIADD
INSERT WRDATA, PRICOM, PRINOP, ARTTEX, REFTIT, PREF, ERTIT
OVERLAY A1
INSERT PARMRD, PRESTM
OVERLAY A1
INSERT SMLTR, DRMTM, STPTSM, TPAGE
INSERT RDCPD, ABNPMT, ARTHP, TRACE, HGRAM, HGRAMI, HGRAMS
INSERT IOINIT, ACTIVE, STMTM
INSERT DEVDTA, ACT, TIME, INT, RAND, DEADT, DEBUG
OVERLAY A2
INSERT LEVEL, DMATDI, DMATIOA, DMATUD, RIOID
INSERT REMACT, DMA, DMATM, RIO, RIOTM, RIOINT, INTOLY, DMAINT
INSERT DTRAN, PUTACT, RANDOM
OVERLAY A1
INSERT HGPRNT
OVERLAY A1
INSERT LOADER
OVERLAY A1
INSERT LINK, ENTEXT, SLLH
OVERLAY A1
INSERT PLATAC, TOPACK
OVERLAY $ORJECT(REGION)
INSERT ORJECT, INIT, LIB, RCALPH, RCHEX, RCINT
INSERT MDATE
OVERLAY $DUMP(REGION)
INSERT SMDUMP, PAGE
ENTRY MAIN
NAME APSS
/*
//          EXEC PGM=IEBCOPY
//SYSPRINT DD   SYSCUT=A
//SYSUT1  DD   DISP=SHR, UNIT=2314, VOL=SER=SP00L3,
//          DSN=S0729.ATAC.ONE
//SYSUT2 DD   DISP=(NEW,PASS), UNIT=3330, VOL=SER=DISK02,
//          DSN=S0729.ATAC.TWO,

```

```

//          SPACE=(13030,(01,0,14),RLSE),
//          DCB=(RECFM=U,BLKSIZE=13030),
//          ,LABFL=RFIPD=360
//SYSUT3 DD UNIT=SYSDA,SPACE=(TRK,(20,5))
//SYSUT4 DD UNIT=SYSDA,SPACE=(TRK,(20,5))
//SYSIN DD *
COPY OUTDD=SYSUT2,INDD=SYSUT1

```

8. This program converts the IBM-360 absolute deck into correct format for the ATAC.

```

main (argc, *argv)
int argc;
char *argv [1];
(register crctr, index, jndex);
int stchr;
int tmpcy [731];
struct buffr
{int fldes;
int nleft;
char *nexto;
char *buffs [512];
} bufin, bufot, *pnt1, *pnt2;
stchr = 020;
if (argc != 3)
{printf ("Calling arguments are incorrect#");
exit (0);
}
bufin.fldes = open (argv [1], 0);
if (bufin.fldes < 0)
{printf ("Cannot open %s#", argv [1]);
exit (0);
}
pnt1 = &bufin.fldes;
bufot.fldes = creat (argv [2], 0777);
if (bufot.fldes < 0)
{printf ("Cannot open %s#", argv [2]);
exit (0);
}
pnt2 = &bufot.fldes;
putc (stchr, pnt2);
while (crctr >= 0 && index <= 72)
tmpcy [index++] = (crctr =getc (pnt1));
index -= 3;
jndex = 0;
while (jndex < 4 && jndex < index)

```

```

   putc (tmpary [jindex++], pnter2);
index = index + 2;
while (crctr >= 0)
    (while (jindex < index)
        (if (tmpary [jindex] == '#')
            jindex++;
        else
            putc (tmpary [jindex++], pnter2);
        )
    )
index = 0;
while (crctr >= 0 && index <= 72)
    tmpary [index++] = (crctr =getc (pnter1));
index -= 3;
jindex = 0;
}
putc (stchr, pnter2);
fflush (pnter2);
close (bufin.fdcs);
close (bufot.fdcs);
}

```

C. The following program executes the program above and converts the output into the correct code.

```

atac $1 $2 _
if ! -r $2 exit
mv $2 temp2
tr "[0*]" "[040*]" <temp2 >temp1
tr "[1*]" "[001*]" <temp1 >temp2
tr "[2*]" "[002*]" <temp2 >temp1
tr "[3*]" "[043*]" <temp1 >temp2
tr "[4*]" "[004*]" <temp2 >temp1
tr "[5*]" "[045*]" <temp1 >temp2
tr "[6*]" "[046*]" <temp2 >temp1
tr "[7*]" "[007*]" <temp1 >temp2
tr "[8*]" "[010*]" <temp2 >temp1
tr "[9*]" "[051*]" <temp1 >temp2
tr "[a*]" "[052*]" <temp2 >temp1
tr "[b*]" "[013*]" <temp1 >temp2
tr "[c*]" "[054*]" <temp2 >temp1
tr "[d*]" "[015*]" <temp1 >temp2
tr "[e*]" "[015*]" <temp2 >temp1
tr "[f*]" "[057*]" <temp1 >$2
rm temp1 temp2

```

APPENDIX F
ATAC PROGRAM

The following programs are listings of the Main System and Receiver Control programs for the ATAC. The assembly language is to the right of the absolute listing of the first three columns.

ATAC	LOC	OBJECT CODE	CARD IMAGE	CARDNUM
	0117	A446	IOR R,EU,EL	55
	0118	B106 776A	CMP I,EU,0776A	56
	011A	C102 012A	BRCL EQ,P12	57
	011C	0000	NOP	58
	011D	0000	NOP	59
	011E	0000	NOP	60
	011F	0000	NOP	61
	0120	BCF0 0136	.. ECHO EXIT - INVALID REQUEST	62
	0122	ED00 0230	.. LDRM D,0,EXECS,16	63
	0124	E101 014A	.. BAL I,0,OUTPUT	64
	0126	ED00 0230	.. LDR I,1,EXCRLP	65
	0128	C107 0100	.. BAL I,0,OUTPUT	66
	012A	ED00 0500	.. BRCL I,7,EXEC	67
	012C	C107 0100	.. CALL RECEIVER CONTROL PROGRAM	68
	012E	0000	EX2 BAL I,RET,WJ	69
	0130	0000	BRCL U,EXEC	70
	0131	0000	.. PATCH AREA FOR ANOTHER REQUEST	71
	0132	0000	..	72
	0133	0000	..	73
	0134	C107 0100	..	74
	0136	0001	..	75
	014A	000A	EXCRLE DC 20	76
	014B	0000	DC 1 0000A	77
	014C	0000	DC 0	78
	014D	0003	DC 3	79
	014E	4558	DC 04558	80
	014F	4543	DC 04543	81
	0150	0000	DC 0	82
			..	83
			..	84
			..	85
			..	86
			..	87
			..	88
			..	89
			..	90
			..	91
			..	92
			..	93
			..	94
			..	95
			..	96
			..	97
			..	98
			..	99
			..	100
			..	101
			..	102
			..	103
			..	104
			..	105
			..	106
			..	107

COMBINE FIRST TWO BYTES OF COMMAND
 SEE IF REQUEST FOR WJ
 GO CALL WJ
 PATCH AREA FOR ANOTHER REQUEST
 RESTORE REGISTERS
 ECHO INPUT BUFFER
 GET ADDRESS OF CR/LF BUFFER
 OUTPUT CR/LF
 GO TRY AGAIN
 GO TO WJ

SAVE AREA FOR REGISTERS

CR/LF
 COUNT
 EX
 EC
 NULL

LOC	OBJECT CODE	CARD IMAGE	CARDNUM
108	RET	EQU 0	108
109		RETURN REGISTER	109
110		*****	110
111		SYSTEM EQUATES	111
112		*****	112
113		THESE ARE EQUATES THAT ARE USED FOR LINKAGE BETWEEN	113
114		PROGRAMS OR THAT ARE USED BY AT LEAST TWO PROGRAMS IN	114
115		THE SYSTEM	115
116		*****	116
117		*****	117
118		*****	118
119		*****	119
120		*****	120
121		ROUTINE TO READ DATA FROM THE KEYBOARD	121
122		*****	122
123		CALLING PROCEDURE:	123
124		BAL I,0,KEYMR	124
125		*****	125
126		INPUTS: NONE	126
127		*****	127
128		*****	128
129		*****	129
130		*****	130
131		*****	131
132		*****	132
133		*****	133
134		*****	134
135		*****	135
136		*****	136
137		*****	137
138		*****	138
139		*****	139
140		*****	140
141		*****	141
142		*****	142
143		*****	143
144		*****	144
145		*****	145
146		*****	146
147		*****	147
148		*****	148
149		*****	149
150		*****	150
151		*****	151
152		*****	152
153		*****	153
154		*****	154
155		*****	155
156		*****	156
157		*****	157
158		*****	158
159		*****	159
160		*****	160

LOC	OBJECT CODE	CARD IMAGE	CARDNUM
KV2		EQU 12	161
KV3		EQU 13	162
KDATA		EQU 14	163
			164
			165
			166
			167
			168
			169
			170
			171
			172
			173
			174
			175
			176
			177
			178
			179
			180
			181
			182
			183
			184
			185
			186
			187
			188
			189
			190
			191
			192
			193
			194
			195
			196
			197
			198
			199
			200
			201
			202
			203
			204
			205
			206
			207
			208
			209
			210
			211
			212
			213

```

SAVE RETURN ADDRESS
GET ADDRESS OF BUFFER TO OUTPUT
OUTPUT INITIAL BUFFER
DEVICE ADDRESS FOR INPUT
POSITION COUNTER = 1
MAY POSITION USED = 1
ADDRESS OF BUFFER TO REGISTER
RESET CODE
RESET KEYBOARD
*****
SET KEYBOARD ENTRY BUFFER TO ALL SPACES
*****
SET COUNTER TO BUFFER LENGTH
GET BUFFER ADDRESS
CODE FOR SPACE
STORE BLANK CODE
DECREMENT COUNTER
DO AGAIN IF COUNTER GT 0

RESET CODE
RESET KEYBOARD

GET DATA
DATA PRESENT ?
NO DATA, WAIT

RESET CODE
RESET KEYBOARD
GET STATUS
DATA STILL PRESENT ?
KEEP TRYING TO CLEAR
*****
DATA OBTAINED
*****

```

```

STR D,KRET,KBSTRT
LDR I,KBUF,KBSTRT
BAL I,KRET,OUTPUT
LDR I,KOUT,OPBPF
LDR I,KPC,1
LDR I,KPM,1
LDR I,KBUF,KBUFFER
LDR I,KOR,08000
ROUT KOR,KOUT
*****
SET KEYBOARD ENTRY BUFFER TO ALL SPACES
*****
IS KV1,80
R,KV2,KBUF
LDR IS,KV3,020
STR RX,KV3,KV2,KV1
ADD IS,KV1,-1
BRCL GT,KMR2
*****
GET DATA FROM KEYBOARD
*****
LDR I,KOR,08000
ROUT KOR,KOUT

RIN KDATA,KIN
CMPL I,KDATA,04000
BRCL NE,KMR4

LDR I,KOR,08000
ROUT KOR,KOUT
RIN KIR,KIN
CHPL I,KIR,02000
BRCL EQ,KMR5
*****
DATA OBTAINED
*****

```

```

9C00 01D6
E101 0228
E155 E10A PFBF
E157 E109 BPF
E158 4015
E15C 4016
E15D E101 01D7
E15F E107 8000
D997

450B
E01C
E164 420D
E165 1BCD
E166 6PFB
E167 C101 0165

E107 8000
D997

8CAE 4000
9E0E 016C
C105 016C

E107 8000
D997
8CAB 2000
9E08 2000
C102 0171

```

LOC	OBJECT CODE	CARD IMAGE	CARDNUM
0179	A10E 007F	AND I, KDATA, 0007F	214
017B	208E	CMP WITH BACKSPACE	215
017C	C105 0184	BRCL NOT BACKSPACE	216
017E	C105 0184	ADD IS, KMR6	217
017F	C105 0169	BRCL DECREMENT POINTER	218
0181	C107 0169	BRCL LOOK FOR DATA IF PC NOT ZERO	219
0182	C107 0169	LDR SET PC = 1	220
		BRCL GET MORE DATA	221
0184	21CE	KMR6 COMPARE WITH FORWARD SPACE	222
0185	C105 0190	BRCL NOT FORWARD SPACE	223
0187	6015	KMR7 INCREMENT POSITION COUNTER	224
0188	B105 0050	CMP COMPARE WITH MAX VALUE	225
018A	C106 0169	BRCL LESS OR EQUAL MAX GET MORE DATA	226
018C	E105 0050	LDR SET POSITION COUNTER TO 80	227
018E	C107 01B9	BRCL GET OUT OF ROUTINE	228
0190	20DE	KMR8 COMPARE WITH CARRIAGE RETURN	229
0191	C102 01B9	BRCL CR, GET OUT	230
0193	22EE	EQ, KMR11 COMPARE WITH DECIMAL POINT	231
0194	C102 01B1	BRCL DECIMAL POINT, PUT IN BUFFER	232
0196	220E	EQ, KMR10 COMPARE WITH SPACE	233
0197	C102 01B1	BRCL SPACE, PUT IN BUFFER	234
0199	230E	LT, KMR9 COMPARE WITH 0	235
019A	C104 01AB	BRCL ILLEGAL CHARACTER	236
019C	239E	EQ, KMR10 COMPARE WITH 9	237
019D	C106 01B1	BRCL NUMERIC, PUT IN BUFFER	238
019F	241E	EQ, KMR10 COMPARE WITH UPPER CASE A	239
01A0	C104 01AB	BRCL ILLEGAL CHARACTER	240
01A2	25AE	EQ, KMR10 COMPARE WITH UPPER CASE Z	241
01A3	C106 01B1	BRCL UPPER CASE ALPHA, PUT IN BUFFER	242
01A5	261E	EQ, KMR9 COMPARE WITH LOWER CASE A	243
01A6	C104 01AB	BRCL ILLEGAL CHARACTER	244
01A8	27AE	EQ, KMR10 COMPARE WITH LOWER CASE Z	245
01A9	C106 01B1	BRCL LOWER CASE ALPHA, PUT IN BUFFER	246
		*****	247
		ILLEGAL ENTRY	248
		*****	249
01AB	E101 022C	KMR9 LDR GET ADDRESS OF ILLEGAL BUFFER	250
01AD	E000 0230	HAL OUTPUT BUFFER	251
01AF	C107 0153	BRCL START OVER	252

LOC	OBJECT CODE	CARD IMAGE	CARDNUM
01B1	9B5E 01D7	***** : VALID ENTRY - PUT IN BUFFER : *****	267
01E3	B056 01B7	KMR10 STR DX, KDATA, KBUFFER, KPC PUT DATA IN BUFFER	268
01B4	C103 01B7	CMPL R, KPM, KPC COMPARE MAX USED WITH LAST POSITIO	269
01B6	E056	LDR R, KMR7 GO INCREMENT POSITION COUNTER	270
01B7	C107 01B7	BRCL U, KMR7 PM = PC	271
		BRCL U, KMR7 KEEP GETTING DATA	272
		****	273
		: EXIT	274
		: ****	275
01B9	9C06 01D7	KMR11 STR D, KPM, KBUFFER PUT COUNT IN BUFFER	276
01EB	E01B	THIS IS THE SEQUENCE FOR CALLING 'CORE' (UTILITY TO DISPLAY AND	277
01BC	4005	CHANGE CORE IN A CALL TO THAT PROGRAM IS NO LONGER DESIRED.	278
01BD	601B	REMOVED WHEN R, KVI, KBU	279
01BF	6FF6	LDR R, KPC, 0	280
01C0	C106 01D3	ADD IS, KVI, KVI, KPC	281
01C2	220C 01B0	ADD R, KVI, KVI, KPC	282
01C3	C102 01B0	BRCL IS, KPM, 1	283
01C5	601B	CMPL NE, KMR12	284
01C6	AE7C	ADD IS, KVI, KVI, KPC	285
01C7	55BD	ADD EQ, KMR30	286
01C8	AE7D	ADD IS, KVI, 1	287
01C9	80DC	SHS LL, KVI, 8	288
01CB	B10C	LDR RL, KVI, KVI, KPC	289
01CD	C105 01D3	SHS LL, KVI, 8	290
01CF	E202 01D6	SHS RL, KVI, 8	291
01D1	C107 02F8	ADD R, KVI, KVI, KPC	292
01D3	E200 01D6	CMPL I, KVI, 0636F	293
01D5	BF07	BRCL NE, KMR12	294
		LDR D, 2, CORE	295
		BRC I, 7, CORE	296
		*****	297
		: KMR12 LDR D, KRET, RETURN	298
		: BRC R, 7, KRET	299
		: *****	300
		: DATA	301
		: ****	302
		: RETURN DS 1	303
		: *****	304
01D6		RESTORE RETURN ADDRESS	305
		RETURN	306
		*****	307
		: DATA	308
		: ****	309
		: RETURN DS 1	310
		: *****	311
		: DATA	312
		: ****	313
		: RETURN DS 1	314
		: *****	315
		: DATA	316
		: ****	317
		: RETURN DS 1	318
		: *****	319

LOC	OBJECT CODE	CARD IMAGE	CARDNUM
01D7	0050	KEYBOARD ENTRY BUFFER *****	320
01D8		KBUFFER DC 80 DS 80	321
		COUNT RESERVE	322
0228	0003	*****	323
0229	000A	START SYMBOL BUFFER *****	324
022A	002B	*****	325
022B	0000	*****	326
		KBSTRT DC 3 DC 0000A DC 002B DC 00000	327
		COUNT	328
		CR/LF	329
		↑	330
		NULL	331
		*****	332
		ILLEGAL CHARACTER BUFFER *****	333
		*****	334
022C	0003	KBILL DC 3	335
022D	0A07	DC 00A07	336
022E	0707	DC 00707	337
022F	0000	DC 00000	338
		COUNT	339
		CR,BELL	340
		BELL,BELL	341
		NULL	342
		*****	343
		*****	344
		*****	345
		*****	346
		*****	347
		*****	348
		*****	349
		*****	350
		*****	351
		*****	352
		*****	353
		*****	354
		*****	355
		*****	356
		*****	357
		*****	358
		*****	359
		*****	360
		*****	361
		*****	362
		*****	363
		*****	364
		*****	365
		*****	366
		*****	367
		*****	368
		*****	369
		*****	370
		*****	371
		*****	372

LOC	OBJECT CODE	CARD IMAGE	ADDRESS OF BUFFER TO OUTPUT	CARDNUM
373		*****	OUTPUT BUFFER ADDRESS	373
374		ESTABLISH EQUATES	INPUT DEVICE ADDRESS	374
375		*****	OUTPUT DEVICE ADDRESS	375
376			INPUT BUFFER LOWER BYTE	376
377			INPUT BUFFER UPPER BYTE	377
378			NUMBER OF WORDS TO OUTPUT	378
379			NUMBER OF WORDS TO INPUT	379
380			UPPER BYTE INDEX	380
381			LOWER BYTE INDEX	381
382			WORD TO OUTPUT TO TTY	382
383				383
384				384
385				385
386				386
387				387
388				388
389				389
390				390
391				391
392				392
393				393
394				394
395				395
396				396
397				397
398				398
399				399
400				400
401				401
402				402
403				403
404				404
405				405
406				406
407				407
408				408
409				409
410				410
411				411
412				412
413				413
414				414
415				415
416				416
417				417
418				418
419				419
420				420
421				421
422				422
423				423
424				424
425				425

LOC	OBJECT CODE	CARD IMAGE	GET RETURN ADDRESS	CARDNUM
02DB	E200 02E1	LDR D,RET,HEXRTN	GET RETURN ADDRESS	532
02DA	BF07	BRC R,7,RET	RETURN	533
		*****		534
		CONVERT ONE DIGIT		535
		*****		536
02DB	20A4	CHP IS,V1,00A	COMPARE WITH HEX 'A'	537
02DC	C104 02DF	BRC L,HEXA2	BRANCH IF LESS	538
02DE	6074	ADD IS,V1,7	ADD 7 TO VALUE	539
02DF	6304	ADD IS,V1,030	ADD HEX '30'	540
02FE	BF07	BRC R,7,RET	GO BACK TO WHERE CALLED FROM	541
		****		542
		DATA		543
		****		544
02E1		HEXRTN DS 1	SAVE LOCATION FOR RETURN ADDRESS	545
		*****		546
		AHEX		547
		*****		548
		ROUTINE TO CONVERT FOUR (4) OR LESS DIGITS IN ASCII		549
		CODE TO A TRUE HEX VALUE FOR THE MACHINE SIGN OF THE		550
		VALUE MUST BE HANDLED BY THE CALLING ROUTINE.		551
		*****		552
		CALLING PROCEDURE:		553
		BAL I,0,AHEX		554
		*****		555
		INPUTS:		556
		REG 1 ADDRESS OF FIRST CONSECUTIVE LOCATION IN		557
		CORE WHERE THE ASCII CHARACTERS ARE LOCATED		558
		(ONE CHARACTER PER CORE LOCATION)		559
		*****		560
		OUTPUT:		561
		REG 2 HEX VALUE		562
		NONE		563
		ROUTINES CALLED:		564
		*****		565
		REGISTERS 8 THROUGH 16 ARE PRESERVED		566
		*****		567
		*****		568
		ESTABLISH EQUATES		569
		*****		570
		*****		571
		AHADD EQU 1	ADDRESS OF ASCII CHARACTERS	572
		AHOUT EQU 2	OUTPUT HEX VALUE	573
		AHVT EQU 5	VARIABLE 1	574
		*****		575
		*****		576
		*****		577
		*****		578
		*****		579
		*****		580
		*****		581
		*****		582
		*****		583
		*****		584

LOC	OBJECT CODE	CARD IMAGE	ATAC	LOC	OBJECT CODE	CARD IMAGE	ATAC
585				585			
586				586			
587				587			
588				588			
589				589			
590				590			
591				591			
592				592			
593				593			
594				594			
595				595			
596				596			
597				597			
598				598			
599				599			
600				600			
601				601			
602				602			
603				603			
604				604			
605				605			
606				606			
607				607			
608				608			
609				609			
610				610			
611				611			
612				612			
613				613			
614				614			
615				615			
616				616			
617				617			
618				618			
619				619			
620				620			
621				621			
622				622			
623				623			
624				624			
625				625			
626				626			
627				627			
628				628			
629				629			
630				630			
631				631			
632				632			
633				633			
634				634			
635				635			
636				636			
637				637			

```

*****
CONVERT ASCII TO HEX
*****
AHX LDR IS,AHOUT,0
LDR IS,AHV1,6
LDR IS,AHZ,6
AH1 LDR IS,AHV2,AHZ,AHADD
CMP EQ,AHV2,020
BRCL EQ,AH3
SHS EQ,AHOUT,4
CMP IS,AHV2,040
AH2 LDR IS,AHV2,9
ADD IS,AHV2,000F
AND I,AHOUT,7,AHV2
ADD IS,AHV1,-1
BRCL Z,AH3
AH3 IS,AHADD,1
BRC U,7,RET

```

```

CLEAR REGISTER
SET UP COUNTER
SET UP ZERO REGISTER
GET ASCII CHARACTER
COMPARE WITH SPACE
DON'T TRANSLATE SPACES
MOVE DIGIT OVER
COMPARE WITH MIN LETTER
SKIP IF NUMERIC
INCREMENT ALPHA
CLEAR ALL BUT DIGIT
PUT IN OUTPUT REGISTER
DECREMENT COUNTER
DONE INCREMENT CHARACTER ADDRESS
KEEP GOING
GET OUT

```

```

*****
CORE
*****
ROUTINE TO CONTROL CORE INTERACTIVELY FROM THE KEYBOARD.
*****
***WARNING***
THIS ROUTINE SHOULD ONLY BE CALLED BY 'KEYMR'. THERE ARE
SPECIAL CONSIDERATIONS IN CALLING WHICH ARE TAKEN INTO
ACCOUNT BY 'KEYMR'. (TAKE SPECIAL NOTE OF REGISTER 2 WHEN
'CORE' IS CALLED AND THE WAY 'CORE' EXITS THROUGH 'KEYMR'.
MAY BE INITIATED BY THE OPERATOR AT ANY TIME WHEN THE
KEYBOARD IS ACCEPTING DATA BY TYPING 'CORE'.
ENTERED. IT REMAINS IN CONTROL UNTIL RELEASED BY THE
TYPED COMMAND 'DONE'. THE COMMANDS AND FUNCTIONS ARE
AS LISTED:
COMMAND FUNCTION
DUHE 'ADD' PRINTS 10 LINES OF B VALUES

```

```

* OP CORE STARTING WITH THE
* ADDRESS SPECIFIED 'ADD'
* MUST BE HEXADECIMAL - ALPHA
* CHARACTERS IN UPPER CASE.
* 'DUMP' MUST BE TYPED IN LOWER
* CASE LETTERS
*
* CHANGES THE CONTENTS OF THE
* CORE LOCATION WHOSE ADDRESS
* IS SPECIFIED TO THE VALUE
* SPECIFIED
*
* DISPLAYS THE CONTENTS OF THE
* CORE LOCATION IN 'ADD'
*
* CS 'ADD'
*
* INITIATES THE CHANGING OF CORE
* SEQUENTIAL STARTING WITH THE
* SPECIFIED ADDRESS. AFTER THIS
* COMMAND THE FIRST FOUR CHARA-
* CTERS TYPED ARE PLACED IN CORE
* AND THE ADDRESS INCREMENTED BY
* ONE. THE CHANGE IS TERMINATED BY
* 'DUMP'. THIS IS TYPING
* OF 'DONE' DOES NOT CAUSE THE
* CORE PROGRAM TO BE EXITED)
*
* CALLING PROCEDURE:
* BAL I,O,CORE
*
* INPUTS:
* NONE
*
* OUTPUTS:
* NONE
*
* ROUTINES CALLED:
* KEYMR
* OUTPUT
* HEXA
* AHX
*
* THIS ROUTINE PRESERVES NO REGISTERS
* *****
* ESTABLISH EQUATES
* *****
* EQU 1
* EQU 1
* EQU 2
* EQU 3
*
* ADDRESS OF BUFFER RETURNED
* HEX VALUE TO INPUT TO HEXA
* ASCII MSD RETURNED BY HEXA
* ASCII LSD RETURNED BY HEXA

```

LOC	OBJECT CODE	CARD IMAGE	DESCRIPTION
691		EQV	VARIABLE 3
692		EQV	MAX NO. CHARACTERS INPUT BY KFYMR
693		EQV	PATH FLAG
694		EQV	BLANK FLAG
695		EQV	FIELD2
696		EQV	CONSTANT 0
697		EQV	ASCII CODE FOR SPACE
698		EQV	VARIABLE 1
699		EQV	VARIABLE 2
700		EQV	COMMAND
701		EQV	FIELD 1
702		EQV	FIELD 2
703		EQV	FIELD 3
704		EQV	FIELD 4
705		EQV	FIELD 5
706		EQV	FIELD 6
707		EQV	FIELD 7
708		EQV	FIELD 8
709		EQV	FIELD 9
710		EQV	FIELD 10
711		EQV	FIELD 11
712		EQV	FIELD 12
713		EQV	FIELD 13
714		EQV	FIELD 14
715		EQV	FIELD 15
716		EQV	FIELD 16
717		EQV	FIELD 17
718		EQV	FIELD 18
719		EQV	FIELD 19
720		EQV	FIELD 20
721		EQV	FIELD 21
722		EQV	FIELD 22
723		EQV	FIELD 23
724		EQV	FIELD 24
725		EQV	FIELD 25
726		EQV	FIELD 26
727		EQV	FIELD 27
728		EQV	FIELD 28
729		EQV	FIELD 29
730		EQV	FIELD 30
731		EQV	FIELD 31
732		EQV	FIELD 32
733		EQV	FIELD 33
734		EQV	FIELD 34
735		EQV	FIELD 35
736		EQV	FIELD 36
737		EQV	FIELD 37
738		EQV	FIELD 38
739		EQV	FIELD 39
740		EQV	FIELD 40
741		EQV	FIELD 41
742		EQV	FIELD 42
743		EQV	FIELD 43
9C02	03D3	STR	SAVE REGISTER 2
400C		LDR	GET ZERO
9C0C	0412	STR	INITIALIZE SEQUENTIAL CHANGE FLAG
E101	0405	STR	INITIALIZE ADDRESS FOR TITLE
E000	0230	BAL	OUTPUT TITLE
E10B	0151	BAL	GET KEYBOARD COMMAND
400A	2020	LDR	SET UP SPACE CONSTANT
400E		LDR	ZERO COMMAND REGISTER
400F		LDR	ZERO FIELD 1
4009		LDR	ZERO FIELD 2
4008		LDR	ZERO FLAG
4007		LDR	ZERO PATH FLAG
51A6		LDR	GET NO. OF ENTRIES IN BUFFER
6011		ADD	SET FLAG FOR READY
6PF6		ADD	INCREMENT BUFFER ADDRESS
51AC	0327	BRCL	DECREMENT CHARACTER COUNTER
22AC		LDR	DO A CHARACTER FROM THE BUFFER
5102	030C	CHP	COMPARE WITH SPACE
200B	030D	BRCL	KEEP GOING IF SPACE
C105		CHP	SEE IF IN MIDDLE OF ENTRY
		NE,C3	IN ENTRY, KEEP LOOKING
E370	031B	LDR	GET PROCESSING ADDRESS
BF07		BRC	PROCESS ACCORDING TO FIELD FOUND
031A			BRANCH TABLE

LOC	OBJECT CODE	CARD IMAGE	COMMAND FIELD	CARDNUM
031B	031E	DC	FIELD 1	744
031C	0323	DC	FIELD 2	745
031D	0326	DC		746
031E	E01E	LDR	ADDRESS OF COMMAND	747
0320	6017	ADD	INCREMENT PATH INDEX	748
0321	4PF8	LDR	SET FLAG FOR IN ENTRY	749
0322	C107	PRCL	KEEP GOING	751
0323	E01F	LDR	ADDRESS OF FIELD 1	752
0324	C107	BRCL	KEEP GOING	753
0326	E019	LDR	ADDRESS OF FIELD 2	754
0327	200F	CMP	SEE IF FIELD 1 IS PRESENT	755
0328	C102	BRCL	GO PROCESS COMMAND ONLY	756
032A	E0F1	LDR	PUT ADDRESS IN REGISTER	757
032B	E000	BAL	CONVERT VALUE	758
032D	E02F	LDR	PUT HEX VALUE IN FIELD	759
032E	2009	CMP	SEE IF FIELD 2 IS PRESENT	760
032F	C102	BRCL	GO PROCESS COMMAND	761
0331	E091	LDR	PUT ADDRESS IN REGISTER	762
0332	FD00	FAL	CONVERT VALUE	763
0334	E029	LDR	PUT HEX VALUE IN FIELD	764
0335	5PAC	LDR	GET FIRST CHARACTER OF COMMAND	765
0336	601E	ADD	INCREMENT ADDRESS	766
0337	5PAD	LDR	GET SECOND CHARACTER OF COMMAND	767
0338	A10D	AND	CLEAR UPPER BITS	768
033A	A87C	SMS	LEFT JUSTIFY 1ST CHARACTER	769
033B	80DC	ADD	PUT 1ST 2 CHARACTERS IN ONE WORD	770
033C	B10C	CMP	COMPARE WITH 'DO'	771
033E	C102	BRCL	GO PROCESS DONE	772
0340	B10C	CMP	COMPARE WITH 'DU'	773
0342	C102	BRCL	GO PROCESS DUMP	774
0344	B10C	CMP	COMPARE WITH 'DI'	775
0346	C102	BRCL	GO PROCESS DISPLAY	776
0348	B10C	CMP	COMPARE WITH 'CH'	777
0349	646F	CMP	COMPARE WITH 'DO'	778
034E	C102	BRCL	GO PROCESS DONE	779
0350	B10C	CMP	COMPARE WITH 'DU'	780
0352	C102	BRCL	GO PROCESS DUMP	781
0354	B10C	CMP	COMPARE WITH 'DI'	782
0356	C102	BRCL	GO PROCESS DISPLAY	783
0358	B10C	CMP	COMPARE WITH 'CH'	784
035A	6469	CMP	COMPARE WITH 'DO'	785
035C	C102	BRCL	GO PROCESS DONE	786
035E	B10C	CMP	COMPARE WITH 'DU'	787
035F	C102	BRCL	GO PROCESS DUMP	788
0360	B10C	CMP	COMPARE WITH 'DI'	789
0362	C102	BRCL	GO PROCESS DISPLAY	790
0364	B10C	CMP	COMPARE WITH 'CH'	791
0366	6469	CMP	COMPARE WITH 'DO'	792
0368	C102	BRCL	GO PROCESS DONE	793
036A	B10C	CMP	COMPARE WITH 'DU'	794
036C	C102	BRCL	GO PROCESS DUMP	795
036E	B10C	CMP	COMPARE WITH 'DI'	796
036F	C102	BRCL	GO PROCESS DISPLAY	797
0370	B10C	CMP	COMPARE WITH 'CH'	798
0372	646F	CMP	COMPARE WITH 'DO'	799
0374	C102	BRCL	GO PROCESS DONE	800
0376	B10C	CMP	COMPARE WITH 'DU'	801
0378	C102	BRCL	GO PROCESS DUMP	802
037A	B10C	CMP	COMPARE WITH 'DI'	803
037C	C102	BRCL	GO PROCESS DISPLAY	804
037E	B10C	CMP	COMPARE WITH 'CH'	805
037F	6469	CMP	COMPARE WITH 'DO'	806
0380	C102	BRCL	GO PROCESS DONE	807
0382	B10C	CMP	COMPARE WITH 'DU'	808
0384	C102	BRCL	GO PROCESS DUMP	809
0386	B10C	CMP	COMPARE WITH 'DI'	810
0388	C102	BRCL	GO PROCESS DISPLAY	811
038A	B10C	CMP	COMPARE WITH 'CH'	812
038C	646F	CMP	COMPARE WITH 'DO'	813
038E	C102	BRCL	GO PROCESS DONE	814
0390	B10C	CMP	COMPARE WITH 'DU'	815
0392	C102	BRCL	GO PROCESS DUMP	816
0394	B10C	CMP	COMPARE WITH 'DI'	817
0396	C102	BRCL	GO PROCESS DISPLAY	818
0398	B10C	CMP	COMPARE WITH 'CH'	819
039A	6469	CMP	COMPARE WITH 'DO'	820
039C	C102	BRCL	GO PROCESS DONE	821
039E	B10C	CMP	COMPARE WITH 'DU'	822
039F	C102	BRCL	GO PROCESS DUMP	823
03A0	B10C	CMP	COMPARE WITH 'DI'	824
03A2	C102	BRCL	GO PROCESS DISPLAY	825
03A4	B10C	CMP	COMPARE WITH 'CH'	826
03A6	646F	CMP	COMPARE WITH 'DO'	827
03A8	C102	BRCL	GO PROCESS DONE	828
03AA	B10C	CMP	COMPARE WITH 'DU'	829
03AC	C102	BRCL	GO PROCESS DUMP	830
03AE	B10C	CMP	COMPARE WITH 'DI'	831
03B0	C102	BRCL	GO PROCESS DISPLAY	832
03B2	B10C	CMP	COMPARE WITH 'CH'	833
03B4	6469	CMP	COMPARE WITH 'DO'	834
03B6	C102	BRCL	GO PROCESS DONE	835
03B8	B10C	CMP	COMPARE WITH 'DU'	836
03BA	C102	BRCL	GO PROCESS DUMP	837
03BC	B10C	CMP	COMPARE WITH 'DI'	838
03BE	C102	BRCL	GO PROCESS DISPLAY	839
03C0	B10C	CMP	COMPARE WITH 'CH'	840
03C2	646F	CMP	COMPARE WITH 'DO'	841
03C4	C102	BRCL	GO PROCESS DONE	842
03C6	B10C	CMP	COMPARE WITH 'DU'	843
03C8	C102	BRCL	GO PROCESS DUMP	844
03CA	B10C	CMP	COMPARE WITH 'DI'	845
03CC	C102	BRCL	GO PROCESS DISPLAY	846
03CE	B10C	CMP	COMPARE WITH 'CH'	847
03D0	6469	CMP	COMPARE WITH 'DO'	848
03D2	C102	BRCL	GO PROCESS DONE	849
03D4	B10C	CMP	COMPARE WITH 'DU'	850
03D6	C102	BRCL	GO PROCESS DUMP	851
03D8	B10C	CMP	COMPARE WITH 'DI'	852
03DA	C102	BRCL	GO PROCESS DISPLAY	853
03DC	B10C	CMP	COMPARE WITH 'CH'	854
03DE	646F	CMP	COMPARE WITH 'DO'	855
03E0	C102	BRCL	GO PROCESS DONE	856
03E2	B10C	CMP	COMPARE WITH 'DU'	857
03E4	C102	BRCL	GO PROCESS DUMP	858
03E6	B10C	CMP	COMPARE WITH 'DI'	859
03E8	C102	BRCL	GO PROCESS DISPLAY	860
03EA	B10C	CMP	COMPARE WITH 'CH'	861
03EC	6469	CMP	COMPARE WITH 'DO'	862
03EE	C102	BRCL	GO PROCESS DONE	863
03F0	B10C	CMP	COMPARE WITH 'DU'	864
03F2	C102	BRCL	GO PROCESS DUMP	865
03F4	B10C	CMP	COMPARE WITH 'DI'	866
03F6	C102	BRCL	GO PROCESS DISPLAY	867
03F8	B10C	CMP	COMPARE WITH 'CH'	868
03FA	646F	CMP	COMPARE WITH 'DO'	869
03FC	C102	BRCL	GO PROCESS DONE	870
03FE	B10C	CMP	COMPARE WITH 'DU'	871

LOC	OBJECT CODE	CARD IMAGE	CARDNUM
034A	C102 039C	BRCL EQ,C12	797
034C	B10C 6373	I,CV1,06373	798
034E	C102 03A0	BRCL EQ,C20	799
0350	C107 02FD	BRCL U,C1	800
		GO PROCESS 'CHANGE'	801
		COMPARE WITH 'CS'	802
		GO PROCESS CHANGE SEQUENTIAL	803
		INVALID COMMAND, GO TRY AGAIN	804
			805
			806
			807
			808
			809
0352	E200 03D3	LDR D,0,CSAVE	810
0354	C107 0151	BRC I,7,KEYMR	811
		GET ADDRESS OF CALLING ROUTINE	812
		EXIT THROUGH KEYMR	813
			814
			815
			816
			817
			818
			819
			820
0356	A10F FFFB	I,CF1,OFFPB	821
0358	E051	R,CV1,CF1	822
0359	40A7	IS,C1NDX,10	823
		CLEAR LAST THREE BITS	824
		SET UP ADDRESS TO DUMP	825
		SET UP LINE COUNTER	826
		OUTPUT LINE OF CORE WITH ADDRESS OF FIRST VALUE	827
			828
			829
			830
			831
			832
			833
			834
			835
			836
			837
			838
			839
			840
			841
			842
			843
			844
			845
			846
			847
			848
			849

ATAC LOC	OBJECT CODE	CARD IMAGE	CARDNUM
03A0	9C0F 0410	***** : CHANGE CORE SEQUENTIALLY : *****	892
03A2	9C0F 0411	D, CFI, CSTART	893
03A4	401C 0412	D, CFI, CSTOP	894
03A5	9C0C 0412	I, CV1, I	895
03A7	E000 0151	D, CV1, CSELG	896
03A9	400A	I, RET, KEVRR	897
03AB	6011	I, CZERO, 0	898
03AB	4015	I, CADD, 1	899
03AC	51AD	I, CV3, 1	900
03AD	51AD	RX, CV1, CZERO, CADD	901
03AE	AE7C	LL, CV1, 8	902
03AF	AE7D	LL, CV2, 8	903
03B0	AD7D	RL, CV1, CV2	904
03B1	80DC	R, CV1, 0646F	905
03B2	B10C	I, CV2, C22	906
03B4	C102	I, RET, HEX	907
03B6	E000	D, CFI, CSTOP	908
03B8	E20F 0411	D, CA, 1, CP1	909
03BA	9BF2 0000	I, CFI, CSTART	910
03BC	601F	D, CFI, CSTOP	911
03BD	9C0F 0411	D, CFI, CSTART	912
03BE	C107 03A7	LDR D, CV1, CSTOP	913
03C1	E20F 0410	LDR U, CV1, CSTOP	914
03C1	E107 0383	LDR D, CFI, CSTART	915
03C3	E107 0383	LDR U, CFI, CSTART	916
03C5	E20C 0410	D, CV1, CSTART	917
03C7	9C0C 0410	I, CV1, I	918
03C8	9C0C 0411	D, CV1, CSTART	919
03CA	E20C 0411	I, CV1, CSTOP	920
03CC	C104 03C1	LDR D, CV1, C22	921
03CE	400C	I, CV1, 0	922
03CF	9C0C 0412	D, CV1, CSFLG	923
03D1	C107 02FD	U, C1	924
03D3		***** : DATA AND BUFFERS : *****	925
03D4	001C	CSAVE DS 1	926
03D5	001C	CBBCNT DC 28	927
03F0	0D0A	CBUFF DC 27	928
	0D0A	DC 00D0A	929
		***** : ADDRESS OF ROUTINE CALLING CORE : *****	930
		***** : COUNT OF VALUES IN BUFFER TO OUTPUT : *****	931
		***** : OUTPUT BUFFER : *****	932
		***** : CR/LF : *****	933
		***** : ADDRESS OF ROUTINE CALLING CORE : *****	934
		***** : COUNT OF VALUES IN BUFFER TO OUTPUT : *****	935
		***** : OUTPUT BUFFER : *****	936
		***** : CR/LF : *****	937
		***** : ADDRESS OF ROUTINE CALLING CORE : *****	938
		***** : COUNT OF VALUES IN BUFFER TO OUTPUT : *****	939
		***** : OUTPUT BUFFER : *****	940
		***** : CR/LF : *****	941
		***** : ADDRESS OF ROUTINE CALLING CORE : *****	942
		***** : COUNT OF VALUES IN BUFFER TO OUTPUT : *****	943
		***** : OUTPUT BUFFER : *****	944
		***** : CR/LF : *****	945

LOC	OBJECT CODE	CS1	DS	CARD IMAGE	REG SAVE AREA FOR INTERNAL USE	CARDNUM
03F1			20			945
0405	0003	CTITLE	DC	3	COUNT	946
0406	0D0A		DC	00D0A	CR/LF	947
0407	434F		DC	0434F	CO	948
0408	5245		DC	05245	RE	949
0409	0006	CBUD	DC	6	COUNT	950
040A		CBUD1	DS	2	2 LOCATIONS FOR ADDRESS	951
040C	2020		DC	02020	BLANKS	952
040D		CBUD2	DS	2	2 LOCATIONS FOR CONTENTS	953
040F	0D0A		DC	00D0A	CR/LF	954
0410	0000	CSTRT	DC	0	START ADDRESS FOR SEQUENTIAL CORE	955
0411	0000	CSTOR	DC	0	CURRENT ADDRESS FOR STORING IN SEQ	956
0412	0000	CSFLG	DC	0	FLAG FOR SEQUENTIAL CHANGE	957
						958
						959
						960
						961
						962
						963
						964
						965
						966

LOC	OBJECT CODE	CARD IMAGE	DESCRIPTION	CARDNUM
0539	C107 0531	BRCL	ILLEGAL FREQUENCY	108
053B	9C03 09D0	STR	STORE TENTATIVE FREQUENCY (LOWER)	109
053D	9C04 09D1	STR	STORE TENTATIVE FREQUENCY (UPPER)	110
			: OUTPUT TITLE AND GET KEYBOARD ENTRY FOR DEFECT MODE	111
053F	E101 0A89	LDR	GET ADDRESS OF DEFECT MODE TITLE	112
0541	ED00 07AE	BAL	OUTPUT TITLE AND GET ENTRY	113
0543	C107 053F	BRCL	ILLEGAL ENTRY AND GET ENTRY	114
0545	2073	CHP	COMPARE WITH MAX VALUE	115
0546	C101 053F	BRCL	COMPARE WITH MAX VALUE	116
0548	9C03 09D2	STR	TOO LARGE	117
054A	E101 0004	LDR	SAVE TENTATIVE DEFECT MODE	118
054C	5C01 09D4	STR	GET UPPER OF DEFAULT BFO FREQUENCY	119
054E	E101 5500	LDR	SAVE TENTATIVE BFO FREQUENCY (UPPER)	120
0550	9C01 09D3	STR	GET LOWER OF DEFAULT BFO FREQUENCY	121
0552	2033	CHP	SAVE TENTATIVE BFO FREQUENCY (LOWER)	122
0553	C105 056E	BRCL	NOT VARIABLE BFO, USE DEFAULT FREQUE	123
			: OUTPUT TITLE AND GET KEYBOARD ENTRY FOR BFO FREQUENCY	124
0555	E101 0AB3	LDR	GET ADDRESS OF BFO FREQUENCY TITLE	125
0557	ED00 07AE	BAL	OUTPUT TITLE AND GET ENTRY	126
0559	C107 0555	BRCL	ILLEGAL ENTRY AND GET ENTRY	127
055B	2062	CHP	COMPARE WITH EXACT COUNT	128
055C	C105 0555	BRCL	COMPARE WITH EXACT COUNT	129
055E	E833	CHP	WRONG COUNT	130
055F	2044	CHP	POSITION FREQUENCY (10 HZ RES)	131
0560	C105 0555	BRCL	COMPARE UPPER WITH EXACT VALUE	132
0562	B105 4209	CHP	ILLEGAL ENTRY	133
0564	B104 0555	CHP	COMPARE LOWER WITH MIN VALUE	134
0566	B103 6500	CHP	ILLEGAL ENTRY	135
0568	C101 0555	BRCL	COMPARE LOWER WITH MAX VALUE	136
056A	9C03 09D3	STR	ILLEGAL ENTRY	137
056C	9C04 09D4	STR	SAVE TENTATIVE BFO FREQUENCY (LOWER)	138
			: OUTPUT TITLE AND GET KEYBOARD ENTRY FOR GAIN MODE	139
056E	E101 0A53	LDR	GET ADDRESS OF GAIN MODE TITLE	140
0570	ED00 07AE	BAL	OUTPUT TITLE AND GET ENTRY	141
0572	C107 056E	BRCL	ILLEGAL ENTRY	142
0574	2033	CHP	COMPARE WITH MAX VALUE	143
0575	C101 056E	BRCL	COMPARE WITH MAX VALUE	144
0577	2013	CHP	ILLEGAL ENTRY	145
0578	C102 056E	BRCL	COMPARE WITH ENTRY FO 1	146
057A	E201 09D2	LDR	ILLEGAL ENTRY	147
057C	2041	CHP	GET TENTATIVE DEFECT MODE	148
057D	C105 0582	BRCL	COMPARE WITH USE	149
057F	2003	CHP	BRANCH IF NOT ISB	150
0580	C102 056E	BRCL	MAKE SURE GAIN MODE NOT HOLD AGC	151
0582	9C03 09D5	STR	ILLEGAL ENTRY	152
			: OUTPUT TITLE AND GET KEYBOARD ENTRY FOR IF BANDWIDTH	153
0582	9C03 09D5	STR	SAVE TENTATIVE GAIN MODE	154
			: OUTPUT TITLE AND GET KEYBOARD ENTRY FOR IF BANDWIDTH	155
0582	9C03 09D5	STR	SAVE TENTATIVE GAIN MODE	156

AD-A039 950

NAVAL POSTGRADUATE SCHOOL MONTEREY CALIF
AN INTERACTIVE COMPUTER INTERFACE WITH A DIGITAL RECEIVER.(U)
MAR 77 W G BORRIES

F/G 9/2

UNCLASSIFIED

NL

2 OF 2
AD
A039 950



END

DATE
FILMED
6-77

```

*****
: DISPLAY CONTROL WORD (COMMAND=2)
*****
WJ30
LDR D,WJ2,WJCFU
LDR D,WJ1,WJCFU
BAL I,RET,WJ92
LDR D,WJ1,WJCGH
LDR D,WJ2,WJCFU
BAL I,RET,WJ93
LDR D,WJ1,WJCGH
LDR D,WJ2,WJCFU
BAL I,RET,WJ94
LDR D,WJ1,WJCRFG
LDR D,WJ2,WJ95
BAL I,RET,WJ95
BRCL U,WJ02

```

```

GET UPPER OF CONTROL FREQUENCY
GET LOWER OF CONTROL FREQUENCY
OUTPUT FREQUENCY
GET CONTROL IF BANDWIDTH
GET CONTROL IF BANDWIDTH AND IF BANDWIDTH
OUTPUT GAIN MODE AND IF BANDWIDTH
GET CONTROL BFO FREQUENCY (LOWER)
OUTPUT DETECT MODE AND BFO FREQUENCY
GET CONTROL RF GAIN
OUTPUT RF GAIN
DONE

```

```

E202 09D9
E201 09D8
ED00 07D9
E5CA E201 09DD
05CC E202 09DF
E5CE ED00 0801
05D0 E201 09DA
05E2 E202 0820
05E6 ED00 0820
05E8 E201 09DE
05FA ED00 084A
05DC C107 0519

```

```

*****
: DISPLAY RECEIVED WORD (COMMAND=3)
*****
WJ40
LDR D,WJ2,WJRFU
LDR D,WJ1,WJRFU
BAL I,RET,WJ92
LDR D,WJ1,WJRGH
LDR D,WJ2,WJRFU
BAL I,RET,WJ93
LDR D,WJ1,WJRDH
LDR D,WJ2,WJRFU
BAL I,RET,WJ94
LDR D,WJ1,WJRFU
LDR D,WJ2,WJ95
BAL I,RET,WJ95
LDR D,WJ1,WJRSS
LDR D,WJ2,0
SHD LL,WJ1,8
LDR LL,WJ2,02030
LDR LL,WJ2,WJDS1
LDR LL,WJ2,0
SHD LL,WJ2,4
LDR LL,WJ2,4
SHD LL,WJ1,03030
LDR LL,WJ2,WJDS1+1
LDR LL,WJ1,WJDS
BAL I,RET,OUTPUT
BRCL U,WJ02
DS

```

```

E202 09E1
E201 09E0
ED00 07D9
E5E4 E201 09E5
05E6 E202 09E7
E5E8 ED00 0801
05EA E201 09E2
05EC E202 09E3
05EE ED00 0820
05F0 E201 09E6
05F2 ED00 084A
05F4 E201 09E9
05F6 4002
05F7 8871
05F8 A502 2030
05FA 9C02 0B22
05FC 4002
05FE 4031
05FF AE32
0600 F831
0602 F831
0604 A502 3030
0606 9C02 0B23
0608 E101 0B18
ED00 0230
C107 0519
060A

```

```

GET UPPER OF RECEIVED FREQUENCY
GET LOWER OF RECEIVED FREQUENCY
OUTPUT RECEIVED FREQUENCY
GET RECEIVED IF BANDWIDTH
GET RECEIVED IF BANDWIDTH AND IF BANDWIDTH
OUTPUT GAIN MODE AND IF BANDWIDTH
GET RECEIVED DETECT MODE
GET RECEIVED BFO FREQUENCY (LOWER)
OUTPUT DETECT MODE AND BFO FREQUENCY
GET RECEIVED RF GAIN
OUTPUT RF GAIN
GET RECEIVED SIGNAL STRENGTH
CLEAR REGISTER
POSITION 1ST DIGIT
CONVERT TO ASCII
STORE IN OUTPUT BUFFER
CLEAR REGISTER 2 DIGITS
POSITION LAST 2 DIGITS

```

```

CONVERT TO ASCII
STORE IN OUTPUT BUFFER
GET ADDRESS OF BUFFER TO OUTPUT
OUTPUT BUFFER
DONE
SPACE FOR PATCHES

```

```

E202 09E1
E201 09E0
ED00 07D9
E5E4 E201 09E5
05E6 E202 09E7
E5E8 ED00 0801
05EA E201 09E2
05EC E202 09E3
05EE ED00 0820
05F0 E201 09E6
05F2 ED00 084A
05F4 E201 09E9
05F6 4002
05F7 8871
05F8 A502 2030
05FA 9C02 0B22
05FC 4002
05FE 4031
05FF AE32
0600 F831
0602 F831
0604 A502 3030
0606 9C02 0B23
0608 E101 0B18
ED00 0230
C107 0519
060A

```


ATAC	LOC	OBJECT CODE	CARD IMAGE	INITIALIZE PASS COUNT	GET ADDRESS OF SCAN TITLE	OUTPUT SCAN TITLE	SPACE FOR PATCHES	OUTPUT TITLE AND GET LOWER FREQUENCY LIMIT	GET ADDRESS OF START FREQUENCY TITLE	OUTPUT TITLE AND GET ENTRY	ILLEGAL ENTRY	MAKE SURE FREQ IS IN LEGAL BOUNDS	ILLEGAL ENTRY	SAVE UPPER LIMIT (LOWER)	SAVE LOWER LIMIT (UPPER)	SPACE FOR PATCHES
0658	9C01	09F0	STR	D, WJV1, WJSCNT												
065A	E101	0897	LDR	I, WJV1, WJSCAN												
065C	BAL		BAL	I, RET, WJSP0												
065E	ED00	0230	DS	10												
: OUTPUT TITLE AND GET LOWER FREQUENCY LIMIT																
0668	E101	089C	LDR	I, WJV1, WJSP5												
066A	E100	07AE	BAL	I, RET, WJ90												
066C	C107	06AB	BACL	U, WJ26, WJ90												
066E	ED00	07CC	BACL	I, RET, WJ91												
0670	C107	0668	BACL	U, WJ60, WJ91												
0672	9C03	09EC	STR	D, WJV3, WJSELL												
0674	9C04	09EC	STR	D, WJV4, WJSEFLU												
0676	DS		DS	10												
: OUTPUT TITLE AND GET UPPER FREQUENCY LIMIT																
0680	E101	08A7	LDR	I, WJV1, WJSP5												
0682	ED00	07AE	BAL	I, RET, WJ90												
0684	C107	0680	BACL	U, WJ60, WJ91												
0686	ED00	07CC	BACL	I, RET, WJ91												
0688	C107	0680	BACL	U, WJ60, WJ91												
068A	9C03	09EB	STR	D, WJV3, WJSEFLU												
068C	9C04	09EA	STR	D, WJV4, WJSP00												
068E	DS		DS	10												
: MAKE SURE UPPER LIMIT GREATER THAN LOWER LIMIT																
0698	E205	09EC	LDR	D, WJV5, WJSELU												
069A	B054		CRP	R, WJV5, WJV5												
069B	C104	0668	BACL	L, WJ60, WJV5												
069D	E101	06AE	BACL	GT, WJ60, WJV5												
069F	E205	09ED	LDR	D, WJV5, WJSEFLU												
06A1	B053	0668	CRP	R, WJV3, WJV5												
06A2	C106	0668	BACL	LE, WJ60, WJV5												
06A4	DS		DS	10												
: OUTPUT TITLE AND GET FREQUENCY INCREMENT (8000HZ MAX)																
06AE	E101	08B1	LDR	I, WJV1, WJSP5												
06E0	ED00	07AE	BAL	I, RET, WJ90												
06E2	C107	06AE	BACL	U, WJ60, WJ90												
06E4	E833		SHD	RL, WJV3, 4												
06E5	2042		CRP	IS, WJV2, 4												
06E6	C101	06AE	BACL	GT, WJ60, WJ90												
06E8	B103	0800	CRP	I, WJV3, 0800												
06EA	C101	06AE	BACL	GT, WJ60, WJ90												
06EC	2013		CRP	IS, WJV3, 1												
06ED	C104	06AE	BACL	LT, WJ60, WJ90												
06EF	9C03	09EC	STR	D, WJV3, WJSP5												
06C1	DS		DS	10												

OBJECT CODE CARD IMAGE

OUTPUT TITLE AND GET SIGNAL STRENGTH

WJ604

E101 09EB I, WJVI, WJISS
 E100 07AE I, RET, WJ90
 E107 06CB D, WJ604
 B103 0100 I, WJ3, 0100
 C101 06CB G, WJ604
 IS, WJ2, 3
 G, WJ604
 C101 06CB LDR R, WJ2, WJ3
 E032 098E BAL I, RET, WJBCDH
 E019 061B MUL I, WJV1, 07F
 C001 007F IS, WJV1, 032
 ADD IS, WJV3, 064
 LDR R, WJV1, WJV3
 FC31 4643 DIV R, WJV1, WJV3
 E06E 06DF AND I, WJV2, 07F
 A102 007F STR D, WJV2, WJFS5
 9C02 09EF DS 16

OUTPUT FREQUENCY TO RECEIVER AND GET RESPONSE

WJ605

E205 09ED LDR D, WJV5, WJSPLL
 E206 09EC LDR D, WJV6, WJSPLU
 E204 09C9 SHD I, WJV5, 4
 E203 09C8 LDR D, WJV3, WJCM2
 A103 000F AND D, WJV3, WJCM1
 A103 0000 AND I, WJV3, 0
 A153 IOR R, WJV4, WJV5
 A454 IOR R, WJV3, WJV6
 A464 STR D, WJV3, WJCM2
 9C03 09C9 STR D, WJV4, WJCM1
 9C04 09C8 I, RET, WJS
 E000 08AE DS 10

COMPARE RECEIVED SIGNAL STRENGTH WITH SET SIGNAL STRENGTH

WJ606

E201 09CF LDR D, WJV1, WJRS4
 A101 007F AND I, WJV1, 07F
 E202 09EF LDR D, WJV2, WJFS5
 B021 074E CMP R, WJV1, WJV2
 C103 074E BRCL GE, WJ609
 DS 10

INCREMENT FREQUENCY AND TRY AGAIN

WJ607

E201 09C9 LDR D, WJV1, WJCM2
 E202 09C8 LDR R, WJV1, 4
 E203 01EF SHD I, WJV1, 4
 A102 01EF AND I, WJV2, 01FF
 E203 09EE LDR D, WJV3, WJSPIN
 IS, WJV3, 0
 4004 0000 LDR I, WJV3, 0
 E000 0061 BAL I, RET, WJ96

GET ADDRESS OF SIGNAL STRENGTH TITLE
 OUTPUT TIME AND GET ENTRY
 ILLEGAL ENTRY MAX VALUE
 ILLEGAL - TOO LARGE MAX
 COMPARE COUNT WITH MAX
 ILLEGAL - TOO LARGE
 PUT SIGNAL STRENGTH IN REG 2
 CONVERT BCD TO HEX
 MULTIPLY BY 127
 ROUND
 SET UP DIVISOR
 DIVIDE BY 100
 CLEAR ALL BUT VALUE WANTED
 STORE DESIRED SIGNAL STRENGTH
 SPACE FOR PATCHES

GET LOWER LIMIT (LOWER)
 GET LOWER LIMIT (UPPER)
 POSITION FREQUENCY FOR WJ
 GET CONTROL WORD 2 (FREQ LOWER)
 GET CONTROL WORD 1 (FREQ UPPER)
 CLEAR OLD FREQ (LOWER)
 CLEAR OLD FREQ (UPPER)
 PUT IN NEW FREQ (LOWER)
 PUT IN NEW FREQ (UPPER)
 SAVE NEW CONTROL WORD 2
 SAVE NEW CONTROL WORD 1
 SEND / RECEIVE NEW CONTROL WORD
 SPACE FOR PATCHES

GET RECEIVED WORD 4
 CLEAR ALL BUT SIGNAL STRENGTH
 GET SPECIFIED SIGNAL STRENGTH
 COMPARE RECEIVED WITH SPECIFIED
 FIND
 SPACE FOR PATCHES

GET LAST FREQ (LOWER)
 GET LAST FREQ (UPPER)
 POSITION FOR INCREMENT
 CLEAR ALL BUT FREQUENCY
 GET INCREMENT
 CLEAR REGISTER
 INCREMENT FREQUENCY

ATAC	LOC	OBJECT CODE	CARD IMAGE	TIME	PAGE	CARDNUM
	072C	E201 09EB	LDR D, WJVI, WJSPUL			426
	072E	E202 09EA	LDR D, WJVI, WJSPUU			427
	0730	B062	CMP K, WJVI, WJSPU			428
	0731	C101 073F	BRCL EQ, WJ608			429
	0733	C102 073F	BRCL EQ, WJ608			430
	0735	E201 09F0	LDR D, WJVI, WJSCNT			431
	0737	6011	ADD IS, WJVI, 1			432
	0738	2641	CMP IS, WJVI, 100			433
	0739	C101 075E	BRCL GT, WJVI, 10			434
	073B	9C01 09F0	STR D, WJVI, WJSCNT			435
	073D	C107 06EE	BRCL U, WJ605			436
	073F	B051	CMP R, WJVI, WJVS			437
	0740	C104 0735	BRCL R, WJVI, WJVS			438
	0742	C107 06F2	BRCL R, WJVI, WJVS			439
	0744	C107 06F2	BRCL U, WJ608			440
			DS 10			441
						442
						443
						444
						445
						446
						447
						448
						449
						450
						451
						452
						453
						454
						455
						456
						457
						458
						459
						460
						461
						462
						463
						464
						465
						466
						467
						468
						469
						470
						471
						472
						473
						474
						475
						476
						477
						478

GET UPPER LIMIT (LOWER)
 GET UPPER LIMIT (UPPER)
 COMPARE UPPER LIMIT AND FREQ (UPPER)
 FREQ BELOW LIMIT, CHECK LOWERS
 UPPER FREQ MATCH, CHECK LOWERS
 GET COUNT OF NO. OF TIME THRU
 INCREMENT COUNT
 COMPARE WITH MAX TIMES THRU
 DONE - NO FIND
 SAVE NEW COUNT
 GO START AT LOWER LIMIT AGAIN
 COMPARE LOWERS OP UPPER LIMIT AND FR
 LESS GO START AGAIN
 GO DO WITH NEW FREQUENCY
 SPACE FOR PATCHES

GET RECEIVED FREQUENCY (DISPLAY)
 STORE IN CONTROL FREQ (DISPLAY)
 GO DISPLAY RECEIVED WORD
 SPACE FOR PATCHES

GET LAST RECEIVED FREQ (DISPLAY)
 STORE IN CONTROL FREQ (DISPLAY)
 GET ADDRESS OF NO FIND MESSAGE
 OUTPUT MESSAGE
 DONE
 SPACE FOR PATCHES

 RECEIVE CONTROL WORD FROM RECEIVER (COMMAND=6)

 LOAD FLAG TO INHIBIT COMPARE
 STORE FLAG
 INPUT RECEIVER CONTROL WORDS
 ZERO REGISTER
 CLEAR FLAG
 DISPLAY RECEIVED WORDS
 RETURN
 SPACE FOR PATCHES

 DONE (COMMAND=7)

ATAC	LOC	OBJECT CODE	CARD IMAGE	CONVERT TO ASCII PUT IN DISPLAY BUFFER GET ADDRESS OF OUTPUT BUFFER OUTPUT BUFFER GET RETURN ADDRESS RETURN TO CALLER	CARDNUM
07F7	0800	A442	R, WJ2, WJ4		565
07F8	0800	0AE0	D, WJ2, WJDF1+3		586
07FA	0800	E101	I, WJ1, WJDF		587
07FC	0800	E200	I, RET, OUTPUT		588
07FE	0800	E200	D, RET, WJ90R		589
0800	0800	BF07	R, 7, RET		590
0801	0801	9C00	D, RET, WJ90R		591
0802	0801	9C02	D, WJ1, WJ90S1		592
0803	0801	9C01	D, WJ1, WJ90S2		593
0804	0801	E101	I, WJ1, WJ90S2		594
0805	0801	E200	I, RET, OUTPUT		595
0806	0801	E202	I, WJ2, WJ90S2		596
0807	0801	E103	I, WJ3, WJ90S2		597
0808	0801	0B27	I, WJ3, WJ90S2		598
0809	0801	5321	R, WJ1, WJ2, WJ3		599
0810	0801	E200	I, RET, OUTPUT		600
0811	0801	E101	I, WJ1, WJDF		601
0812	0801	E101	I, WJ1, WJDF		602
0813	0801	E200	EAL I, RET, OUTPUT		603
0814	0801	E202	I, WJ2, WJ90S1		604
0815	0801	E103	I, WJ3, WJ90S1		605
0816	0801	0B42	I, WJ3, WJ90S1		606
0817	0801	5321	R, WJ1, WJ2, WJ3		607
0818	0801	E200	I, RET, OUTPUT		608
0819	0801	E200	I, RET, OUTPUT		609
081A	0801	0230	BAL I, RET, OUTPUT		610
081B	0801	E200	I, RET, OUTPUT		611
081C	0801	0230	BAL I, RET, OUTPUT		612
081D	0801	0230	BAL I, RET, OUTPUT		613
081E	0801	0230	BAL I, RET, OUTPUT		614
081F	0801	0230	BAL I, RET, OUTPUT		615
0820	0801	0230	BAL I, RET, OUTPUT		616
0821	0801	0230	BAL I, RET, OUTPUT		617
0822	0801	0230	BAL I, RET, OUTPUT		618
0823	0801	0230	BAL I, RET, OUTPUT		619
0824	0801	0230	BAL I, RET, OUTPUT		620
0825	0801	0230	BAL I, RET, OUTPUT		621
0826	0801	0230	BAL I, RET, OUTPUT		622
0827	0801	0230	BAL I, RET, OUTPUT		623
0828	0801	0230	BAL I, RET, OUTPUT		624
0829	0801	0230	BAL I, RET, OUTPUT		625
082A	0801	0230	BAL I, RET, OUTPUT		626
082B	0801	0230	BAL I, RET, OUTPUT		627
082C	0801	0230	BAL I, RET, OUTPUT		628
082D	0801	0230	BAL I, RET, OUTPUT		629
082E	0801	0230	BAL I, RET, OUTPUT		630
082F	0801	0230	BAL I, RET, OUTPUT		631
0830	0801	0230	BAL I, RET, OUTPUT		632
0831	0801	0230	BAL I, RET, OUTPUT		633
0832	0801	0230	BAL I, RET, OUTPUT		634
0833	0801	0230	BAL I, RET, OUTPUT		635
0834	0801	0230	BAL I, RET, OUTPUT		636
0835	0801	0230	BAL I, RET, OUTPUT		637

ATAC	LOC	OBJECT CODE	CARD IMAGE	TIME: 16:43:42	03/18/77	PAGE	CARDNUM
	0833	4002	LDR IS, WJ2, 0				636
	0834	F831	SHD LL, WJ1, 4				639
	0835	AE32	SHS LL, WJ2, 4				640
	0836	F831	IOR LL, WJ1, 63030				641
	0837	A502	STR D, WJ2, WJDRP1				642
	0838	9C02	LDR JS, WJ2, 0				643
	0839	4002	SHD LL, WJ1, 4				644
	083C	F831	SHS LL, WJ2, 4				645
	083D	AE32	SHD LL, WJ1, 4				646
	083E	F831	IOR LL, WJ2, 63030				647
	083F	A502	STR I, WJ2, WJDRP1+1				648
	0841	9C02	LDR D, WJ2, WJDRP1				649
	0843	E101	STR I, WJ1, WJDRP				650
	0845	E000	LDR I, RET, WJ90R				651
	0847	E200	BAL D, RET, WJ90R				652
	0849	BF07	LDR R, 7, RET				653
			BRC				654
							655
							656
							657
							658
							659
							660
							661
							662
							663
							664
							665
							666
							667
							668
							669
							670
							671
							672
							673
							674
							675
							676
							677
							678
							679
							680
							681
							682
							683
							684
							685
							686
							687
							688
							689
							690

```

CLEAR REGISTER
POSITION UPPER 2 DIGITS

CONVERT TO ASCII
STORE IN OUTPUT BUFFER
CLEAR REGISTER
POSITION LOWER 2 DIGITS

CONVERT TO ASCII
STORE IN OUTPUT BUFFER TO OUTPUT
GET ADDRESS OF BUFFER
OUTPUT BUFFER
GET RETURN ADDRESS
RETURN TO CALLER

*****
ROUTINE TO DISPLAY RP GAIN
WJ1 RE GAIN IN DISPLAY FORMAT
*****

WJ95
STR D, RET, WJ90R
LDR IS, WJ2, 0
SHD LL, WJ1, 8
IOR LL, WJ2, 02030
STR I, WJ2, WJDRP1
LDR D, WJ2, 0
SHD LL, WJ1, 4
SHS LL, WJ2, 4
SHD LL, WJ1, 63030
IOR I, WJ2, WJDRP1+1
STR D, WJ2, WJDRP1
LDR I, WJ1, WJDRP
BAL I, RET, WJ90R
LDR D, RET, WJ90R
BRC R, 7, RET

*****
ROUTINE TO INCREMENT FREQUENCY
WJ1 FREQUENCY IN DISPLAY FORMAT (LOWER)
WJ2 FREQUENCY IN DISPLAY FORMAT (UPPER)
WJ3 FREQUENCY INCREMENT IN DISPLAY FORMAT (LOWER)
WJ4 FREQUENCY INCREMENT IN DISPLAY FORMAT (UPPER)
WJ5 ANSWER FREQUENCY IN DISPLAY FORMAT (LOWER)
WJ6 ANSWER FREQUENCY IN DISPLAY FORMAT (UPPER)
*****

```

ATAC LOC OBJECT CODE CARD IMAGE

ATAC LOC	OBJECT CODE	CARD IMAGE	SAVE RETURN ADDRESS	PAGE	CARDNUM
0861	9C00	09F1	D,RET,WJ90R	691	
0863	E109	000F	I,WJV9,00000F	692	
0865	4006		I,S,WJV5,0	693	
0866	4006		I,S,WJV6,0	694	
0867	E038	08A4	R,WJV8,WJV3	695	
0868	E038	08A4	R,WJV8,WJV3	696	
0869	E038	08A4	R,WJV8,WJV3	697	
086A	E038	08A4	R,WJV8,WJV3	698	
086B	E038	08A4	R,WJV8,WJV3	699	
086C	E038	08A4	R,WJV8,WJV3	700	
086D	E038	08A4	R,WJV8,WJV3	701	
086E	E038	08A4	R,WJV8,WJV3	702	
086F	E038	08A4	R,WJV8,WJV3	703	
0870	E038	08A4	R,WJV8,WJV3	704	
0871	E038	08A4	R,WJV8,WJV3	705	
0872	E038	08A4	R,WJV8,WJV3	706	
0873	E038	08A4	R,WJV8,WJV3	707	
0874	E038	08A4	R,WJV8,WJV3	708	
0875	E038	08A4	R,WJV8,WJV3	709	
0876	E038	08A4	R,WJV8,WJV3	710	
0877	E038	08A4	R,WJV8,WJV3	711	
0878	E038	08A4	R,WJV8,WJV3	712	
0879	E038	08A4	R,WJV8,WJV3	713	
087A	E038	08A4	R,WJV8,WJV3	714	
087B	E038	08A4	R,WJV8,WJV3	715	
087C	E038	08A4	R,WJV8,WJV3	716	
087D	E038	08A4	R,WJV8,WJV3	717	
087E	E038	08A4	R,WJV8,WJV3	718	
087F	E038	08A4	R,WJV8,WJV3	719	
0880	E038	08A4	R,WJV8,WJV3	720	
0881	E038	08A4	R,WJV8,WJV3	721	
0882	E038	08A4	R,WJV8,WJV3	722	
0883	E038	08A4	R,WJV8,WJV3	723	
0884	E038	08A4	R,WJV8,WJV3	724	
0885	E038	08A4	R,WJV8,WJV3	725	
0886	E038	08A4	R,WJV8,WJV3	726	
0887	E038	08A4	R,WJV8,WJV3	727	
0888	E038	08A4	R,WJV8,WJV3	728	
0889	E038	08A4	R,WJV8,WJV3	729	
088A	E038	08A4	R,WJV8,WJV3	730	
088B	E038	08A4	R,WJV8,WJV3	731	
088C	E038	08A4	R,WJV8,WJV3	732	
088D	E038	08A4	R,WJV8,WJV3	733	
088E	E038	08A4	R,WJV8,WJV3	734	
088F	E038	08A4	R,WJV8,WJV3	735	
0890	E038	08A4	R,WJV8,WJV3	736	
0891	E038	08A4	R,WJV8,WJV3	737	
0892	E038	08A4	R,WJV8,WJV3	738	
0893	E038	08A4	R,WJV8,WJV3	739	
0894	E038	08A4	R,WJV8,WJV3	740	
0895	E038	08A4	R,WJV8,WJV3	741	
0896	E038	08A4	R,WJV8,WJV3	742	
0897	E038	08A4	R,WJV8,WJV3	743	
0898	E038	08A4	R,WJV8,WJV3		
0899	E038	08A4	R,WJV8,WJV3		
089A	E038	08A4	R,WJV8,WJV3		
089B	E038	08A4	R,WJV8,WJV3		
089D	E038	08A4	R,WJV8,WJV3		

LOC	OBJECT CODE	CARD IMAGE	CARDNUM
089E	A476	IOR	744
089F	A8B8	SHS	745
08A0	A8B6	IOR	746
08A1	E200	ADR	747
08A3	BF07	BRC	748
		R, WJ96	749
		R, WJ97	750
08A4	A097	AND	751
08A5	A098	AND	752
08A6	8087	ADD	753
08A7	4008	LDR	754
08A8	20A7	CMP	755
08A9	C104	BRCL	756
08AB	6E67	ADD	757
08AC	4018	LDR	758
08AD	BF07	BRC	759
		R, WJ96	760
		R, WJ97	761
		R, WJ98	762
		R, WJ99	763
		R, WJ9A	764
		R, WJ9B	765
		R, WJ9C	766
		R, WJ9D	767
		R, WJ9E	768
		R, WJ9F	769
		R, WJ9G	770
		R, WJ9H	771
		R, WJ9I	772
		R, WJ9J	773
		R, WJ9K	774
		R, WJ9L	775
		R, WJ9M	776
		R, WJ9N	777
		R, WJ9O	778
		R, WJ9P	779
		R, WJ9Q	780
		R, WJ9R	781
		R, WJ9S	782
		R, WJ9T	783
		R, WJ9U	784
		R, WJ9V	785
		R, WJ9W	786
		R, WJ9X	787
		R, WJ9Y	788
		R, WJ9Z	789
		R, WJ9A	790
		R, WJ9B	791
		R, WJ9C	792
		R, WJ9D	793
		R, WJ9E	794
		R, WJ9F	795
		R, WJ9G	796
		R, WJ9H	797
		R, WJ9I	798
		R, WJ9J	799
		R, WJ9K	800
		R, WJ9L	801
		R, WJ9M	802
		R, WJ9N	803
		R, WJ9O	804
		R, WJ9P	805
		R, WJ9Q	806
		R, WJ9R	807
		R, WJ9S	808
		R, WJ9T	809
		R, WJ9U	810
		R, WJ9V	811
		R, WJ9W	812
		R, WJ9X	813
		R, WJ9Y	814
		R, WJ9Z	815
		R, WJ9A	816
		R, WJ9B	817
		R, WJ9C	818
		R, WJ9D	819
		R, WJ9E	820
		R, WJ9F	821
		R, WJ9G	822
		R, WJ9H	823
		R, WJ9I	824
		R, WJ9J	825
		R, WJ9K	826
		R, WJ9L	827
		R, WJ9M	828
		R, WJ9N	829
		R, WJ9O	830
		R, WJ9P	831
		R, WJ9Q	832
		R, WJ9R	833
		R, WJ9S	834
		R, WJ9T	835
		R, WJ9U	836
		R, WJ9V	837
		R, WJ9W	838
		R, WJ9X	839
		R, WJ9Y	840
		R, WJ9Z	841
		R, WJ9A	842
		R, WJ9B	843
		R, WJ9C	844
		R, WJ9D	845
		R, WJ9E	846
		R, WJ9F	847
		R, WJ9G	848
		R, WJ9H	849
		R, WJ9I	850
		R, WJ9J	851
		R, WJ9K	852
		R, WJ9L	853
		R, WJ9M	854
		R, WJ9N	855
		R, WJ9O	856
		R, WJ9P	857
		R, WJ9Q	858
		R, WJ9R	859
		R, WJ9S	860
		R, WJ9T	861
		R, WJ9U	862
		R, WJ9V	863
		R, WJ9W	864
		R, WJ9X	865
		R, WJ9Y	866
		R, WJ9Z	867
		R, WJ9A	868
		R, WJ9B	869
		R, WJ9C	870
		R, WJ9D	871
		R, WJ9E	872
		R, WJ9F	873
		R, WJ9G	874
		R, WJ9H	875
		R, WJ9I	876
		R, WJ9J	877
		R, WJ9K	878
		R, WJ9L	879
		R, WJ9M	880
		R, WJ9N	881
		R, WJ9O	882
		R, WJ9P	883
		R, WJ9Q	884
		R, WJ9R	885
		R, WJ9S	886
		R, WJ9T	887
		R, WJ9U	888
		R, WJ9V	889
		R, WJ9W	890
		R, WJ9X	891
		R, WJ9Y	892
		R, WJ9Z	893
		R, WJ9A	894
		R, WJ9B	895
		R, WJ9C	896
		R, WJ9D	897
		R, WJ9E	898
		R, WJ9F	899
		R, WJ9G	900
		R, WJ9H	901
		R, WJ9I	902
		R, WJ9J	903
		R, WJ9K	904
		R, WJ9L	905
		R, WJ9M	906
		R, WJ9N	907
		R, WJ9O	908
		R, WJ9P	909
		R, WJ9Q	910
		R, WJ9R	911
		R, WJ9S	912
		R, WJ9T	913
		R, WJ9U	914
		R, WJ9V	915
		R, WJ9W	916
		R, WJ9X	917
		R, WJ9Y	918
		R, WJ9Z	919
		R, WJ9A	920
		R, WJ9B	921
		R, WJ9C	922
		R, WJ9D	923
		R, WJ9E	924
		R, WJ9F	925
		R, WJ9G	926
		R, WJ9H	927
		R, WJ9I	928
		R, WJ9J	929
		R, WJ9K	930
		R, WJ9L	931
		R, WJ9M	932
		R, WJ9N	933
		R, WJ9O	934
		R, WJ9P	935
		R, WJ9Q	936
		R, WJ9R	937
		R, WJ9S	938
		R, WJ9T	939
		R, WJ9U	940
		R, WJ9V	941
		R, WJ9W	942
		R, WJ9X	943
		R, WJ9Y	944
		R, WJ9Z	945
		R, WJ9A	946
		R, WJ9B	947
		R, WJ9C	948
		R, WJ9D	949
		R, WJ9E	950
		R, WJ9F	951
		R, WJ9G	952
		R, WJ9H	953
		R, WJ9I	954
		R, WJ9J	955
		R, WJ9K	956
		R, WJ9L	957
		R, WJ9M	958
		R, WJ9N	959
		R, WJ9O	960
		R, WJ9P	961
		R, WJ9Q	962
		R, WJ9R	963
		R, WJ9S	964
		R, WJ9T	965
		R, WJ9U	966
		R, WJ9V	967
		R, WJ9W	968
		R, WJ9X	969
		R, WJ9Y	970
		R, WJ9Z	971
		R, WJ9A	972
		R, WJ9B	973
		R, WJ9C	974
		R, WJ9D	975
		R, WJ9E	976
		R, WJ9F	977
		R, WJ9G	978
		R, WJ9H	979
		R, WJ9I	980
		R, WJ9J	981
		R, WJ9K	982
		R, WJ9L	983
		R, WJ9M	984
		R, WJ9N	985
		R, WJ9O	986
		R, WJ9P	987
		R, WJ9Q	988
		R, WJ9R	989
		R, WJ9S	990
		R, WJ9T	991
		R, WJ9U	992
		R, WJ9V	993
		R, WJ9W	994
		R, WJ9X	995
		R, WJ9Y	996
		R, WJ9Z	997
		R, WJ9A	998
		R, WJ9B	999
		R, WJ9C	1000

ATAC	LOC	OBJECT CODE	CARD IMAGE	YES-EXIT / NO-CONTINUE	SAVE COUNT	CARDNUM
08DD	C103	09A4	BRCL	GE, WJCKP		797
08DF	9C0F	09F6	STR	D, WJLPC, WJLPK		798
						799
						800
						801
						802
						803
						804
						805
						806
						807
						808
						809
						810
						811
						812
						813
						814
						815
						816
						817
						818
						819
						820
						821
						822
						823
						824
						825
						826
						827
						828
						829
						830
						831
						832
						833
						834
						835
						836
						837
						838
						839
						840
						841
						842
						843
						844
						845
						846
						847
						848
						849

CARDNUM

ATAC

LOC

OBJECT CODE

CARD IMAGE

BL, MJV1, 4

LOC	OBJECT CODE	CARD IMAGE	ATAC
0925	E831	SHD	03FF
0926	A102	AND	09E0
0928	9C01	STR	09E1
0929	9C02	STR	09E1
092C	E201	LDR	09CD
092E	A101	AND	000C
0930	A111	SBS	09E5
0931	9C01	STR	09CE
0933	E201	LDR	09CD
0935	E202	LDR	09CD
0937	F801	SHD	0007
0938	A102	AND	09E7
093A	9C02	STR	E000
093C	A101	AND	E000
093E	ADC1	SBS	09E2
093F	9C01	STR	0004
0941	E101	LDR	09E4
0943	9C01	STR	09CE
0945	E201	LDR	09CE
0947	A101	AND	07FF
0949	4002	LDR	IS, MJV2, 0
094A	CD01	MUL	I, MJV1, 07D0
094C	E103	LDR	I, MJV3, 07FF
094E	8101	ADD	I, MJV1, 0400
0950	FC31	DIV	R, MJV1, MJV3
0951	8102	DIV	I, MJV2, 01194
0953	4003	LDR	IS, MJV2, 0
0954	40A4	LDR	IS, MJV4, 10
0955	FC42	LDR	R, MJV2, MJV4
0956	E021	DIV	R, MJV1, MJV2
0957	E032	LDR	R, MJV2, MJV3
0958	4003	LDR	IS, MJV2, 0
0959	FC42	DIV	R, MJV2, MJV4
095A	A421	SBS	LL, MJV2, 4
095B	A421	LDR	LL, MJV1, MJV2
095C	E032	LDR	R, MJV2, MJV3
095D	E032	LDR	IS, MJV2, 0
095E	FC42	DIV	R, MJV2, MJV4
095F	AEB3	SBS	LL, MJV2, 8
0960	A421	LDR	LL, MJV2, 12
0961	A421	LDR	R, MJV1, MJV2
0962	A431	LDR	R, MJV1, MJV3
0963	9C01	STR	D, MJV1, MJRBL
0965	E202	LDR	D, MJV2, MJRS4
0967	A102	AND	I, MJV2, 07F00
0969	AD72	SBS	RL, MJV2, 8
096A	B421	LIC	R, MJV1, MJV2
096B	A101	AND	I, MJV1, 07F
096D	ED00	EAL	I, RET, MJR3
096F	9C01	STR	D, MJV1, MJRRFG
0971	E201	LDR	D, MJV1, MJRS4
0973	A101	AND	I, MJV1, 07F
0975	ED00	BAL	I, RET, MJR3

POSITION FREQUENCY FOR DISPLAY
 CLEAR ALL BUT FREQUENCY
 STORE IN RECEIVED DISPLAY (LOWER)
 STORE IN RECEIVED DISPLAY (UPPER)
 GET RECEIVED WORD 2
 CLEAR ALL BUT GAIN MODE
 POSITION GAIN MCDE
 STORE IN RECEIVED WORD 3
 GET RECEIVED WORD 3
 GET RECEIVED WORD 2
 POSITION RECEIVED BANDWIDTH
 CLEAR ALL BUT BANDWIDTH
 STORE IN RECEIVED DISPLAY
 CLEAR ALL BUT DETECT MODE
 POSITION DETECT MODE
 STORE IN RECEIVED DISPLAY
 GET BFO UPPER DISPLAY
 STORE IN RECEIVED DISPLAY
 GET RECEIVED BFO FREQUENCY
 CLEAR ALL BUT BFO FREQUENCY
 CLEAR REG 2
 MULTIPLY BY 2000
 LOAD DIVISOR
 ROUND
 DIVIDE
 ADD 4500
 CLEAR REG 3
 SET UP DIVISOR
 DIVIDE BY 10
 PUT REMAINDER IN MJV1 AS 1ST DIGIT
 GET QUOTIENT
 CLEAR REGISTER
 DIVIDE BY 10
 POSITION 2ND DIGIT
 PUT 2ND DIGIT IN ANSWER
 GET QUOTIENT
 CLEAR REGISTER
 DIVIDE BY 10
 POSITION 3RD DIGIT
 POSITION 4TH DIGIT
 PUT IN 3RD DIGIT
 PUT IN 4TH DIGIT
 STORE IN RECEIVED BFO FREQ DISPLAY ()
 GET RECEIVED WORD 4
 CLEAR ALL BUT RF GAIN
 RIGHT JUSTIFY
 COMPLEMENT
 CLEAR ALL BUT RF GAIN
 CONVERT TO BCD PERCENTAGE
 STORE IN RECEIVED DISPLAY
 GET RECEIVED WORD 4
 CLEAR ALL BUT SIGNAL STRENGTH
 CONVERT TO BCD PERCENTAGE

LOC	OBJECT CODE	CARD IMAGE	ATAC
0977	9C01 09E9	STR D, WJV1, WJRSS	903
0979	E200 09F1	LDR D, RET, WJ90R	904
097B	BF07	ERC R, 7, RET	905
097C	CD01 0064	MUL I, WJV1, 064	906
097E	6371	ADD IS, WJV1, 037	908
097F	47F3	LDR IS, WJV3, 037	909
0980	FC31	DIV R, WJV1, WJV3	910
0981	E021	LDR R, WJV1, WJV2	911
0982	4002	LDR IS, WJV2, 0	912
0983	40A3	LDR IS, WJV3, 10	913
0984	FC31	DIV R, WJV1, WJV3	914
0985	E014	LDR R, WJV4, WJV1	915
0986	E021	LDR R, WJV1, WJV2	916
0987	4002	LDR IS, WJV2, 0	917
0988	FC31	DIV R, WJV1, WJV3	918
0989	AE31	LDR LL, WJV1, 4	919
098A	AE72	LDR LL, WJV2, 8	920
098B	A441	LDR R, WJV1, WJV4	921
098C	A421	LDR R, WJV1, WJV2	922
098D	BF07	ERC R, 7, RET	923
098E	E021	LDR R, WJV1, WJV2	924
098F	A101	SHS I, WJV1, 04	925
0991	AD32	LDR RL, WJV2, 04	926
0992	E023	LDR R, WJV3, WJV2	927
0993	A103	SHS I, WJV3, 15	928
0995	CD03	MUL I, WJV3, 10	929
0997	8031	ADD RL, WJV1, WJV3	930
0998	AD32	LDR RL, WJV2, 4	931
0999	E023	LDR R, WJV3, WJV2	932
099A	A103	SHS I, WJV3, 064	933
099C	CD03	MUL I, WJV3, 064	934
099E	8031	ADD RL, WJV1, WJV3	935
099F	AD32	LDR RL, WJV2, 4	936
09A0	CD02	MUL I, WJV2, 01E8	937
09A2	8021	ADD R, WJV1, WJV2	938
09A3	BF07	ERC R, 7, RET	939
09B1	000F	LDR R, WJV1, WJV2	940
09B2	000F	SHS I, WJV1, 04	941
09B3	000A	LDR RL, WJV2, 4	942
09B4	000A	MUL I, WJV3, 10	943
09B5	000F	SHS R, WJV3, WJV2	944
09B6	0064	LDR I, WJV3, 064	945
09B7	03E8	MUL I, WJV3, 064	946
09B8	03E8	ADD RL, WJV1, WJV3	947
09B9	03E8	SHS RL, WJV2, 4	948
09BA	03E8	MUL I, WJV2, 01E8	949
09BB	03E8	ADD R, WJV1, WJV2	950
09BC	03E8	MUL I, WJV1, WJV2	951
09BD	03E8	ADD R, 7, RET	952
09BE	03E8	ERC R, 7, RET	953
09BF	03E8	ERC R, 7, RET	954

STORE IN RECEIVED DISPLAY
 GET RETURN ADDRESS
 RETURN TO CALLER
 MULTIPLY BY 100
 ROUND UP DIVISOR
 DIVIDE BY 127
 ROT DIVIDEND IN REG 1
 CLEAR REGISTER
 SET UP DIVISOR
 DIVIDE DIVIDEND
 PUT REMAINDER IN REG 4 (DIGIT 1)
 CLEAR REGISTER
 DIVIDE BY 10
 POSITION 2ND DIGIT
 POSITION 3RD DIGIT
 COMBINE 1ST AND 2ND DIGITS
 COMBINE ALL DIGITS
 RETURN TO CALLER

 BCD TO HEX ROUTINE (4 DIGIT BCD VALUE)
 BCD VALUE PASSED IN WJV2
 HEX VALUE RETURNED IN WJV1
 ROUTINE USES WJV1, WJV2, WJV3, WJV4

 LOAD LEAST SIGNIFICANT BITS, PART 1
 SHIFT FOR MULTIPLICATION
 LOAD DUMMY REG FOR MULTIPLICATION
 CLEAR ALL BUT LEAST SIG BITS
 MULTIPLY BY 16
 ADD CTR0 ANSWER
 SHIFT FOR MULTIPLICATION
 LOAD DUMMY REG FOR MULTIPLICATION
 CLEAR ALL BUT LEAST SIG BITS
 MULTIPLY BY 100
 ADD ONTO ANSWER
 SHIFT FOR MULTIPLICATION
 MULTIPLY BY 1000
 ADD ONTO ANSWER
 RETURN TO CALLER

LOC	OBJECT CODE	CARD IMAGE	CARDNUM
0944	E101 0BD2	***** ROUTINE TO OUTPUT LOOP MESSAGE *****	955
0946	400F 09E6	WJCKP LDR IS,WJVI,WJCF	956
0947	9C0F 09E6	IS,WJLFC,0	957
09A9	E000 0230	STR D,WJLPC,WJLPK	958
09AB	C107 0519	EAL I,RET,OUTPUT BRCL U,WJ02	959
		LOAD ADDRESS OF OUTPUT ZERO COUNTER COUNT SAVE ZERO COUNTER OUTPUT LOOP BUFFER RETURN TO RECEIVER CONTROL	960
		*****	961
		*****	962
		*****	963
		*****	964
		*****	965
		*****	966
		*****	967
		*****	968
		*****	969
		*****	970
		*****	971
09AD		RETURN ADDRESS	972
		WJBEI CS	973
		*****	974
		*****	975
		*****	976
		*****	977
		*****	978
		*****	979
		*****	980
		*****	981
		*****	982
		*****	983
		*****	984
		*****	985
		*****	986
		*****	987
		*****	988
		*****	989
		*****	990
		*****	991
		*****	992
		*****	993
		*****	994
		*****	995
		*****	996
		*****	997
		*****	998
		*****	999
		*****	1000
		*****	1001
		*****	1002
		*****	1003
		*****	1004
		*****	1005
		*****	1006
		*****	1007
		*****	1008
		*****	1009
		*****	1010
		*****	1011
		*****	1012
		*****	1013
		*****	1014
		*****	1015
		*****	1016
		*****	1017
		*****	1018
		*****	1019
		*****	1020
		*****	1021
		*****	1022
		*****	1023
		*****	1024
		*****	1025
		*****	1026
		*****	1027
		*****	1028
		*****	1029
		*****	1030
		*****	1031
		*****	1032
		*****	1033
		*****	1034
		*****	1035
		*****	1036
		*****	1037
		*****	1038
		*****	1039
		*****	1040
		*****	1041
		*****	1042
		*****	1043
		*****	1044
		*****	1045
		*****	1046
		*****	1047
		*****	1048
		*****	1049
		*****	1050
		*****	1051
		*****	1052
		*****	1053
		*****	1054
		*****	1055
		*****	1056
		*****	1057
		*****	1058
		*****	1059
		*****	1060
		*****	1061
		*****	1062
		*****	1063
		*****	1064
		*****	1065
		*****	1066
		*****	1067
		*****	1068
		*****	1069
		*****	1070
		*****	1071
		*****	1072
		*****	1073
		*****	1074
		*****	1075
		*****	1076
		*****	1077
		*****	1078
		*****	1079
		*****	1080
		*****	1081
		*****	1082
		*****	1083
		*****	1084
		*****	1085
		*****	1086
		*****	1087
		*****	1088
		*****	1089
		*****	1090
		*****	1091
		*****	1092
		*****	1093
		*****	1094
		*****	1095
		*****	1096
		*****	1097
		*****	1098
		*****	1099
		*****	1100

ATAC	LOC	OBJECT CODE	CARD IMAGE	RCVR CONTROL WORD	RCVR SENT WORD	CARDNUM
09C8		WJCM1 DS	1	1		1008
09C9		WJCM2 DS	1	2		1009
09CA		WJCM3 DS	1	3		1010
09CB		WJCM4 DS	1	4		1011
09CC		WJRS1 DS	1	1		1012
09CD		WJRS2 DS	1	2		1013
09CE		WJRS3 DS	1	3		1014
09CF		WJRS4 DS	1	4		1015
09D0		WJTEL DS	1			1016
09D1		WJTEU DS	1			1017
09D2		WJTDH DS	1			1018
09D3		WJTEB DS	1			1019
09D4		WJTEU DS	1			1020
09D5		WJTEB DS	1			1021
09D6		WJTEG DS	1			1022
09D7		WJTEG DS	1			1023
09D8		WJTEG DS	1			1024
09D9		WJTEG DS	1			1025
09EA		WJCFU DS	1			1026
09EB		WJCFU DS	1			1027
09EC		WJCFU DS	1			1028
09ED		WJCFU DS	1			1029
09EE		WJCFU DS	1			1030
09EF		WJCFU DS	1			1031
09F0		WJRFU DS	1			1032
09F1		WJRFU DS	1			1033
09F2		WJRFU DS	1			1034
09F3		WJRFU DS	1			1035
09F4		WJRFU DS	1			1036
09F5		WJRFU DS	1			1037
09F6		WJRFU DS	1			1038
09F7		WJRFU DS	1			1039
09F8		WJRFU DS	1			1040
09F9		WJRFU DS	1			1041
09EA		WJRFU DS	1			1042
09EB		WJRFU DS	1			1043
09EC		WJRFU DS	1			1044
09ED		WJRFU DS	1			1045
09EE		WJRFU DS	1			1046
09EF		WJRFU DS	1			1047
09F0		WJRFU DS	1			1048
09F1		WJRFU DS	1			1049
09F2		WJRFU DS	1			1050
09F3		WJRFU DS	1			1051
09F4		WJRFU DS	1			1052
09F5		WJRFU DS	1			1053
09F6		WJRFU DS	1			1054
09F7		WJRFU DS	1			1055
09F8		WJRFU DS	1			1056
09F9		WJRFU DS	1			1057
09FA		WJRFU DS	1			1058
09FB		WJRFU DS	1			1059
09FC		WJRFU DS	1			1060

RCVR CONTROL WORD 1
2
3
4

RCVR SENT WORD 1
2
3
4

TENTATIVE FREQ (LOWER)
TENTATIVE FREQ (UPPER)
TENTATIVE DETECT MODE
TENTATIVE IFO FREQ (LOWER)
TENTATIVE IFO FREQ (UPPER)
TENTATIVE GAIN MODE
TENTATIVE RF GAIN
TENTATIVE IF BANDWIDTH

CONTROL FREQUENCY (LOWER)
CONTROL FREQUENCY (UPPER)
CONTROL DETECT MODE
CONTROL IFO FREQUENCY (LOWER)
CONTROL IFO FREQUENCY (UPPER)
CONTROL GAIN MODE
CONTROL RF GAIN
CONTROL IF BANDWIDTH

RECEIVED FREQUENCY (LOWER)
RECEIVED FREQUENCY (UPPER)
RECEIVED DETECT MODE
RECEIVED IFO FREQUENCY (LOWER)
RECEIVED IFO FREQUENCY (UPPER)
RECEIVED GAIN MODE
RECEIVED RF GAIN
RECEIVED IF BANDWIDTH
RECEIVED SIGNAL STRENGTH

UPPER LIMIT (UPPER)
UPPER LIMIT (LOWER)
LOWER LIMIT (UPPER)
LOWER LIMIT (LOWER)
FREQUENCY INCREMENT (8 KHZ MAX)
SIGNAL STRENGTH
COUNT OF NUMBER OF SCAN PASSES

RECEIVED WORD FROM WJ

TENTATIVE VALUES - DISPLAY FORMAT

CONTROL VALUES - DISPLAY FORMAT

RECEIVED VALUES - DISPLAY FORMAT

SCAN VARIABLES

SAVE LOCATIONS FOR ALL WJ9- ROUTINES

ATAC	LOC	OBJECT CODE	CARD IMAGE	RE	CARDNUM
0A21	5245	DC	RE	1114	
0A22	4345	DC	CE	1115	
0A23	4956	DC	IV	1116	
0A24	4544	DC	ED	1117	
0A25	0D0A	DC	CR/LF	1118	
0A26	343D	DC	4=	1119	
0A27	454E	DC	FN	1120	
0A28	5445	DC	TE	1121	
0A29	5220	DC	R	1122	
0A2A	5445	DC	TR	1123	
0A2B	5445	DC	NT	1124	
0A2C	4154	DC	AT	1125	
0A2D	4956	DC	IV	1126	
0A2E	620D	DC	F/CR	1127	
0A2F	0A2E	DC	L6/5	1128	
0A30	5953	DC	=S	1129	
0A31	4341	DC	CA	1130	
0A32	4E20	DC	M	1131	
0A33	0D0A	DC	CR/LF	1132	
0A34	363D	DC	6=	1133	
0A35	2245	DC	RE	1134	
0A36	4345	DC	CE	1135	
0A37	4956	DC	IV	1136	
0A38	434F	DC	E	1137	
0A39	434F	DC	CO	1138	
0A3A	524F	DC	NT	1139	
0A3B	4E54	DC	RO	1140	
0A3C	4C20	DC	L	1141	
0A3D	0D0A	DC	CR/LF	1142	
0A3E	373D	DC	7=	1143	
0A3F	444F	DC	DO	1144	
0A40	4E45	DC	NE	1145	
0A41	0D0A	DC	CR/LP	1146	
0A42	3B3D	DC	8=	1147	
0A43	5245	DC	RE	1148	
0A44	4956	DC	IV	1149	
0A45	4954	DC	IT	1150	
0A46	495C	DC	IL	1151	
0A47	495A	DC	IZ	1152	
0A48	4520	DC	E	1153	
0A49	0D0A	DC	CR/LF	1154	
0A4A	0000	DC	NULL	1155	
0A4B	0006	DC	COUNT	1156	
0A4C	4652	DC	FR	1157	
0A4D	4551	DC	EQ	1158	
0A4E	2028	DC	HZ	1159	
0A4F	485A	DC	HZ	1160	
0A50	2920	DC	HZ	1161	
0A51	0D0A	DC	CR/LF	1162	
0A52	0000	DC	NULL	1163	
				1164	
				1165	
				1166	

· FREQUENCY TITLE BUFFER

· RJPB	TITLE BUFFER
6	04652
0	04551
0	02028
0	0485A
0	02920
0	0000A
0	0

GAIN MODE TITLE BUFFER
 WJGHB

IF BANDWIDTH TITLE BUFFER
 WJIFB

GA
IN
M
OD
PCR
LFO
=H
OL
D
AG
CCR
LF2
=N
OR
HA
L
AG
CCR
LF3
=H
AN
UA
L
CR/LP
NULL

GA
IF
B
AN
DW
ID
TH
CR/LP
L
50
O
HZ
CR/LP
2=
KH
ZCR
LF3
=4
K
HZ
CR/LP
4=
8

LOC	OBJECT CODE	CARD IMAGE	COUNT
0A53	0018	DC	1167
0A54	4741	DC	1168
0A55	494E	CC	1169
0A56	204D	DC	1170
0A57	4F44	DC	1171
0A58	450D	DC	1172
0A59	0A30	DC	1173
0A5A	3D48	LC	1174
0A5B	4F4C	DC	1175
0A5C	4420	EC	1176
0A5D	4147	DC	1177
0A5E	430D	DC	1178
0A5F	0A32	DC	1179
0A60	3D4E	DC	1180
0A61	4P52	DC	1181
0A62	4C20	DC	1182
0A63	4D41	DC	1183
0A64	4147	DC	1184
0A65	430D	DC	1185
0A66	0A33	DC	1186
0A67	3D4D	DC	1187
0A68	414E	DC	1188
0A69	5541	DC	1189
0A6A	4C20	DC	1190
0A6B	0D0A	DC	1191
0A6C	0000	DC	1192

LOC	OBJECT CODE	CARD IMAGE	COUNT
0A6D	001A	DC	1193
0A6E	4946	LC	1194
0A6F	2042	DC	1195
0A70	414E	DC	1196
0A71	4457	DC	1197
0A72	4944	DC	1198
0A73	5448	DC	1199
0A74	0D0A	DC	1200
0A75	313D	DC	1201
0A76	3530	DC	1202
0A77	3020	DC	1203
0A78	485A	DC	1204
0A79	0D0A	DC	1205
0A7A	323D	DC	1206
0A7B	2220	DC	1207
0A7C	4B48	DC	1208
0A7D	5A0D	LC	1209
0A7E	0A33	DC	1210
0A7F	3D34	DC	1211
0A80	204B	DC	1212
0A81	485A	DC	1213
0A82	0D0A	DC	1214
0A83	343D	DC	1215
0A84	3820	DC	1216

ATAC	LOC	OBJECT CODE	CARD IMAGE	TITLE BUFFER	KH	COUNT	CARDNUM
0A85	4B48	DC	04B48		2	1220	
0A86	5A20	LC	05A20		CR/LF	1221	
0A87	0D0A	DC	00D0A		NULL	1222	
0A88	0000	DC	0			1223	
0A89	0028	DC	04B48			1224	
0A8A	4445	DC	05445			1225	
0A8B	5445	DC	05445			1226	
0A8C	4354	DC	04354			1227	
0A8D	204D	DC	0204D			1228	
0A8E	4E44	DC	04E44			1229	
0A8F	450D	DC	0450D			1230	
0A90	9A30	DC	09A30			1231	
0A91	3D41	DC	03D41			1232	
0A92	4D0D	DC	04D0D			1233	
0A93	0A31	DC	00A31			1234	
0A94	3D46	DC	03D46			1235	
0A95	4D0D	DC	04D0D			1236	
0A96	0A32	DC	00A32			1237	
0A97	3D42	DC	03D42			1238	
0A98	464F	DC	0464F			1239	
0A99	2046	DC	02046			1240	
0A9A	4958	DC	04958			1241	
0A9B	4544	DC	04544			1242	
0A9C	0D0A	DC	00D0A			1243	
0A9D	333D	DC	0333D			1244	
0A9E	4246	DC	04246			1245	
0A9F	4F20	DC	04F20			1246	
0AA0	5641	DC	05641			1247	
0AA1	220D	DC	0220D			1248	
0AA2	0A34	DC	00A34			1249	
0AA3	2D49	DC	02D49			1250	
0AA4	2342	DC	02342			1251	
0AA5	9D0A	DC	09D0A			1252	
0AA6	353D	DC	0353D			1253	
0AA7	4C53	DC	04C53			1254	
0AA8	420D	DC	0420D			1255	
0AA9	0A36	DC	00A36			1256	
0AAA	3D55	DC	03D55			1257	
0AAB	5342	DC	05342			1258	
0AAC	0D0A	DC	00D0A			1259	
0AAD	373D	DC	0373D			1260	
0AAE	414D	DC	0414D			1261	
0AAF	2D4E	DC	02D4E			1262	
0AEO	4C20	DC	04C20			1263	
0AB1	0D0A	DC	00D0A			1264	
0AB2	0000	DC	0			1265	
0AB3	0015	DC	00015			1266	
0AB4	4246	DC	04246			1267	
0AB5	4F20	DC	04F20			1268	

ATAC	LOC	OBJECT CODE	CARD IMAGE	CARDNUM
				1273
				1274
				1275
				1276
				1277
				1278
				1279
				1280
				1281
				1282
				1283
				1284
				1285
				1286
				1287
				1288
				1289
				1290
				1291
				1292
				1293
				1294
				1295
				1296
				1297
				1298
				1299
				1300
				1301
				1302
				1303
				1304
				1305
				1306
				1307
				1308
				1309
				1310
				1311
				1312
				1313
				1314
				1315
				1316
				1317
				1318
				1319
				1320
				1321
				1322
				1323
				1324
				1325

ER	04652	DC	04652	ER
EQ	04551	DC	04551	EQ
HZ	02728	DC	02728	HZ
	0485A	DC	0485A	
	02320	DC	02320	
	00D0A	DC	00D0A	
	03434	DC	03434	
	03520	DC	03520	
	04848	DC	04848	
	05A20	DC	05A20	
	0544F	DC	0544F	
	02034	DC	02034	
	03635	DC	03635	
	0204B	DC	0204B	
	0485A	DC	0485A	
	02049	DC	02049	
	04E20	DC	04E20	
	0485A	DC	0485A	
	00D0A	DC	00D0A	
	0	DC	0	

RF GAIN	TITLE	BUFFER	COUNT
WJRPGB	DC	12246	RF
	DC	02047	PC
	DC	04149	AL
	DC	04E0D	MCR
	DC	00A28	LF (
	DC	05045	PC
	LC	05243	EN
	DC	0454E	TA
	DC	05441	GE
	DC	04745)
	DC	02920	CR/LF
	DC	00D0A	NULL
	DC	0	

FREQUENCY	TITLE	FOR DISPLAY	COUNT
WJDF	DC	11	ER
	DC	04652	EQ
	DC	04551	=
	DC	02030	XY
	DC	02020	XX
	DC	02020	XX
	DC	02020	XX
	DC	02020	H
	DC	02020	Z
	DC	05A20	CR/LF
	DC	00D0A	NULL
	DC	0	

DETECT MODE	TITLE	FOR DISPLAY
WJDF	DC	11
	DC	04652
	DC	04551
	DC	02030
	DC	02020
	DC	02020
	DC	02020
	DC	02020
	DC	02020
	DC	05A20
	DC	00D0A
	DC	0

ATAC	LOC	OBJECT CODE	CARD IMAGE	TITLE FOR DISPLAY	COUNT
	0A05	0007	WJDDH	7 04445	1326
	0A06	4445	DC	DC	1327
	0A07	5445	DC	05445	1328
	0A08	4354	DC	04354	1329
	0A09	204D	DC	0204D	1330
	0A0A	4F44	DC	04F44	1331
	0A0B	4520	DC	04520	1332
	0A0C	3D20	DC	03D20	1333
	0A0D	0000	DC	0	1334
	0A0E	000B	DC		1335
	0A0F	4246	DC	04246	1336
	0A10	4F20	DC	04F20	1337
	0A11	4E52	DC	04E52	1338
	0A12	4E51	DC	04E51	1339
	0A13	203D	DC	0203D	1340
	0A14	2034	DC	02034	1341
	0A15	2020	DC	02020	1342
	0A16	2020	DC	02020	1343
	0A17	3020	DC	03020	1344
	0A18	485A	DC	0485A	1345
	0A19	0D0A	DC	00D0A	1346
	0A1A	0000	DC	0	1347
	0A1B	0006	DC		1348
	0A1C	4741	DC	04741	1349
	0A1D	494E	DC	0494E	1350
	0A1E	204D	DC	0204D	1351
	0A1F	4F44	DC	04F44	1352
	0A20	4520	DC	04520	1353
	0A21	3D20	DC	03D20	1354
	0A22	0000	DC	0	1355
	0B03	0008	DC		1356
	0B04	4946	DC	04946	1357
	0B05	2042	DC	02042	1358
	0B06	414E	DC	0414E	1359
	0B07	4944	DC	04944	1360
	0B08	5448	DC	05448	1361
	0B09	203D	DC	0203D	1362
	0B0A	2020	DC	02020	1363
	0B0B	0000	DC	0	1364
	0B0D	0009	DC		1365
	0B0E	5246	DC	05246	1366
	0B0F	2047	DC	02047	1367
	0B10	4149	DC	04149	1368
	0B11	4E20	DC	04E20	1369
					1370
					1371
					1372
					1373
					1374
					1375
					1376
					1377
					1378

ATAC

LOC OBJECT CODE CARD IMAGE

OB12 3D20 03D20 DC = KK
 OB13 2020 02020 DC XX
 OB14 2020 02020 DC XX
 OB15 2520 02520 DC CR/LF
 OB16 0D0A 00D0A DC NULL
 OB17 0000 0

SIGNAL STRENGTH TITLE FOR DISPLAY

WJDSS 13 COUNT
 DC ST
 DC GN
 DC AL
 DC S
 DC TR
 DC EN
 DC GT
 DC H
 DC =
 DC XX
 DC XX
 DC X
 DC CR/LF
 DC NULL
 DC 0

TABLE OF GAIN MODES FOR DISPLAY

WJGM0 5 HOLD AGC
 DC DC NOT USED
 DC DC NORMAL AGC
 DC DC MANUAL
 DC DC COUNT/HOLD AGC
 DC DC HO
 DC DC LD
 DC DC A
 DC DC GC
 DC DC CR/LF
 DC DC NULL

WJGM1 -1 COUNT/INVALID ENTRY
 DC DC 0
 DC DC NULL

WJGM2 6 COUNT/NORMAL AGC
 DC DC NO
 DC DC RM
 DC DC AL
 DC DC A
 DC DC CC
 DC DC CR/LF
 DC DC NULL
 DC DC 0

1379
 1380
 1381
 1382
 1383
 1384
 1385
 1386
 1387
 1388
 1389
 1390
 1391
 1392
 1393
 1394
 1395
 1396
 1397
 1398
 1399
 1400
 1401
 1402
 1403
 1404
 1405
 1406
 1407
 1408
 1409
 1410
 1411
 1412
 1413
 1414
 1415
 1416
 1417
 1418
 1419
 1420
 1421
 1422
 1423
 1424
 1425
 1426
 1427
 1428
 1429
 1430
 1431

ATAC	LOC	OBJECT CODE	CARD IMAGE	CARDNUM
	0B8B	0000	DC 0	1538
	0B8C	0003	DC 3	1539
	0B8D	5553	DC 05533	1540
	0B8E	4220	DC 04220	1541
	0B8F	0D0A	DC 00D0A	1542
	0B90	0000	DC 0	1543
	0B91	0004	DC 4	1544
	0B92	414D	DC 0414D	1545
	0B93	2D4E	DC 02D4E	1546
	0B94	4C20	DC 04C20	1547
	0B95	0D0A	DC 00D0A	1548
	0B96	0000	DC 0	1549
	0B97	0003	DC 3	1550
	0B98	514E	DC 0514E	1551
	0B99	414E	DC 0414E	1552
	0B9A	0D0A	DC 00D0A	1553
	0B9B	0000	DC 0	1554
	0B9C	0009	DC 9	1555
	0B9D	5354	DC 05354	1556
	0B9E	4152	DC 04152	1557
	0B9F	5420	DC 05420	1558
	0BA0	4652	DC 04652	1559
	0BA1	4551	DC 04551	1560
	0BA2	2049	DC 02049	1561
	0BA3	4E20	DC 04E20	1562
	0BA4	485A	DC 0485A	1563
	0BA5	0D0A	DC 00D0A	1564
	0BA6	0000	DC 0	1565
	0BA7	0008	DC 8	1566
	0BA8	454E	DC 0454E	1567
	0BA9	4420	DC 04420	1568
	0BAA	4652	DC 04652	1569
	0BAB	4551	DC 04551	1570
	0BAC	2049	DC 02049	1571
	0BAD	4E20	DC 04E20	1572
	0BAE	485A	DC 0485A	1573
	0BAF	0D0A	DC 00D0A	1574
	0BB0	0000	DC 0	1575
	0BB1	000B	DC B	1576
	0BB2	4652	DC 04652	1577
	0BB3	4551	DC 04551	1578

NULL
COUNT/USB
US
B
CR/LF
NULL
COUNT/AH-NL
AH
-N
L
CR/LF
NULL
COUNT
SC
AH
CR/LF
NULL
COUNT
ST
AR
T
FR
EQ
I
N
HZ
CR/LF
NULL
COUNT
EN
D
FR
EQ
I
N
HZ
CR/LF
NULL
COUNT
FR
EQ

WJDN7
WJDM8
SCAN TITLE
WJSCAN
START FREQUENCY TITLE
WJSFS
END FREQUENCY TITLE
WJSPE
FREQUENCY INCREMENT TITLE
WJSFI

ATAC	LOC	OBJECT CODE	CARD IMAGE	CARDNUM
	OBRA	2049	DC	1591
	OBBA	4843	DC	1592
	OBBC	5345	DC	1593
	OBBD	4854	DC	1594
	OBBA	4854	DC	1595
	OBBA	2049	DC	1596
	OBBA	4820	DC	1597
	OBBA	485A	DC	1598
	OBBC	000A	DC	1599
	OBBD	0000	DC	1600
	OBBA	000A	DC	1601
	OBBA	0000	DC	1602
	OBBA	000A	DC	1603
	OBBA	0000	DC	1604
	OBBA	000A	DC	1605
	OBBA	0000	DC	1606
	OBBA	000A	DC	1607
	OBBA	0000	DC	1608
	OBBA	000A	DC	1609
	OBBA	0000	DC	1610
	OBBA	000A	DC	1611
	OBBA	0000	DC	1612
	OBBA	000A	DC	1613
	OBBA	0000	DC	1614
	OBBA	000A	DC	1615
	OBBA	0000	DC	1616
	OBBA	000A	DC	1617
	OBBA	0000	DC	1618
	OBBA	000A	DC	1619
	OBBA	0000	DC	1620
	OBBA	000A	DC	1621
	OBBA	0000	DC	1622
	OBBA	000A	DC	1623
	OBBA	0000	DC	1624
	OBBA	000A	DC	1625
	OBBA	0000	DC	1626
	OBBA	000A	DC	1627
	OBBA	0000	DC	1628
	OBBA	000A	DC	1629
	OBBA	0000	DC	1630
	OBBA	000A	DC	1631
	OBBA	0000	DC	1632
	OBBA	000A	DC	1633
	OBBA	0000	DC	1634
	OBBA	000A	DC	1635
	OBBA	0000	DC	1636
	OBBA	000A	DC	1637
	OBBA	0000	DC	1638
	OBBA	000A	DC	1639
	OBBA	0000	DC	1640
	OBBA	000A	DC	1641
	OBBA	0000	DC	1642
	OBBA	000A	DC	1643
	OBBA	0000	DC	1644
	OBBA	000A	DC	1645
	OBBA	0000	DC	1646
	OBBA	000A	DC	1647
	OBBA	0000	DC	1648
	OBBA	000A	DC	1649
	OBBA	0000	DC	1650
	OBBA	000A	DC	1651
	OBBA	0000	DC	1652
	OBBA	000A	DC	1653
	OBBA	0000	DC	1654
	OBBA	000A	DC	1655
	OBBA	0000	DC	1656
	OBBA	000A	DC	1657
	OBBA	0000	DC	1658
	OBBA	000A	DC	1659
	OBBA	0000	DC	1660
	OBBA	000A	DC	1661
	OBBA	0000	DC	1662
	OBBA	000A	DC	1663
	OBBA	0000	DC	1664
	OBBA	000A	DC	1665
	OBBA	0000	DC	1666
	OBBA	000A	DC	1667
	OBBA	0000	DC	1668
	OBBA	000A	DC	1669
	OBBA	0000	DC	1670
	OBBA	000A	DC	1671
	OBBA	0000	DC	1672
	OBBA	000A	DC	1673
	OBBA	0000	DC	1674
	OBBA	000A	DC	1675
	OBBA	0000	DC	1676
	OBBA	000A	DC	1677
	OBBA	0000	DC	1678
	OBBA	000A	DC	1679
	OBBA	0000	DC	1680
	OBBA	000A	DC	1681
	OBBA	0000	DC	1682
	OBBA	000A	DC	1683
	OBBA	0000	DC	1684
	OBBA	000A	DC	1685
	OBBA	0000	DC	1686
	OBBA	000A	DC	1687
	OBBA	0000	DC	1688
	OBBA	000A	DC	1689
	OBBA	0000	DC	1690
	OBBA	000A	DC	1691
	OBBA	0000	DC	1692
	OBBA	000A	DC	1693
	OBBA	0000	DC	1694
	OBBA	000A	DC	1695
	OBBA	0000	DC	1696
	OBBA	000A	DC	1697
	OBBA	0000	DC	1698
	OBBA	000A	DC	1699
	OBBA	0000	DC	1700

I
 NC
 RE
 ME
 NT
 I
 NZ
 CR/LF
 NULL
 COUNT
 ST
 GN
 AL
 S
 TR
 EN
 GT
 H
 %
 CR/LF
 NULL
 COUNT
 NO
 S
 LG
 NA
 L
 CR/LF
 NULL
 COUNT
 BELL
 IN
 FI
 NI
 TE
 L
 OO
 PCR
 LFP
 LE
 AS
 E
 CH
 EC
 K

· SIGNAL STRENGTH TITLE

· WJSSS
 DC 02049
 DC 04843
 DC 05345
 DC 04854
 DC 02049
 DC 04820
 DC 0485A
 DC 0000A
 DC 0

· NO FIND MESSAGE FOR SCAN

· WJSNF
 DC 6
 DC 04843
 DC 02053
 DC 04947
 DC 04841
 DC 04C20
 DC 0000A
 DC 0

· LCOE MESSAGE

· WJCF
 DC 29
 DC 00707
 DC 04948
 DC 04649
 DC 04849
 DC 05445
 DC 02049
 DC 04848
 DC 0500D
 DC 00A2D
 DC 04C25
 DC 04123
 DC 04520
 DC 04348
 DC 04543
 DC 04B20

ATAC	LOC	OBJECT CODE	CARD IMAGE	TIME: 16:43:42	03/18/77	PAGE	CARDNUM
							1644
							1645
							1646
							1647
							1648
							1649
							1650
							1651
							1652
							1653
							1654
							1655
							1656
							1657
							1658
							1659

IN
TR
AC
AC
AM
DR
RE
CE
IV
ER
BELL
CR/LF
NULL

049NE
054NS
052NS
041NS
04520
0414E
04420
05245
04345
04956
04552
00707
00D0A
0000

LOC	OBJECT CODE	CARD IMAGE	PAGE
0BE2	490E	DC	
0BE3	490E	DC	
0BE4	52NS	DC	
0BE5	41NS	DC	
0BE6	4520	DC	
0BE7	414E	DC	
0BE8	4420	DC	
0BE9	5245	DC	
0BEA	4345	DC	
0BEB	4956	DC	
0BEC	4552	DC	
0BED	C707	DC	
0BEE	0D0A	DC	
0BEF	0000	DC	

BIBLIOGRAPHY

1. Applied Technology Airborne Computer, v. 1, Principles of Operation, Itek Corp., 1974.
2. Applied Technology Airborne Computer, v. 7, Input/Output Description, Itek Corp., 1974.
3. Applied Technology Airborne Computer, v. 8, Assembler/Loader, Itek Corp., 1974.
4. Instruction Manual for WJ-8888 HF Receiver, Watkins Johnson Co., 1975.
5. Rosenberger, D. K., Signal Acquisition and Sampling using a Microprocessor, M.S. Thesis, United States Naval Postgraduate School, 1977.

INITIAL DISTRIBUTION LIST

	No. Copies
1. Defense Documentation Center Cameron Station Alexandria, Virginia 22314	2
2. Library, Code 0212 Naval Postgraduate School Monterey, California 93940	2
3. Professor D. E. Kirk, Code 62K1 Department Chairman Department of Electrical Engineering Naval Postgraduate School Monterey, California 93940	1
4. Associate Professor Stephen Jauregui, Jr. Code 62Ja Department of Electrical Engineering Naval Postgraduate School Monterey, California 93940	10
5. LT. William Glenn Borries, USN 2593 Lipton Road Columbus, Ohio 43227	1
6. Watkins Johnson Company 700 Quince Orchard Road Gaithersburg, Maryland 20760 ATTN: Roy Scherer	1
7. Applied Technology 645 Alamanor Avenue Sunnyvale, California 94086 ATTN: Ira Spector	1

8. Commander Naval Electronics Systems Command 3
PME-107
Washington, D.C. 20360
ATTN: Mr. R. LeSage
Capt. W. Flowers
Mr. P. Lowell
9. Commander Naval Security Group Command 1
G-82
3801 Nebraska Avenue
Washington, D.C. 20390
ATTN: CDR. Shoemaker
10. Watkins Johnson Company 1
3333 Hillview Avenue
Palo Alto, California 94304
ATTN: Mr. I. Harper