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NOVEL DIGITAL SIGNAL PROCESSING AND DETECTION TECHNIQUES.(U)
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INTERIM SCIENTIFIC REPORT

Grant AFOSR-76-3083

NOVEL DIGITAL SIGNAL PROCESSING AND DETECTION TECHNIQUES

(1 August 1979 - 31 July 1980)

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Errors due to arithmetic roundoff and coefficient quantization can be very large for second order recursive digital filters with poles near the unit circle. The read-only-memory/accumulator (ROM/ACC) implementation of two special filter structures are analyzed [2,3]. Expressions for the mean squared error and bounds on the zero-input limit cycles are derived for these structures and shown to be small. These new structures can offer significant hardware and speed advantages. Bounds on limit cycles in second order filters that uses error spectrum shaping have been derived and shown to be considerably smaller than a similar bound for the straightforward implementation [9]. By allowing one or two multiplications in the error spectrum shaping implementation of filters with poles near the unit circle, it is shown that the bounds for the limit cycles can not exceed two bits [10].

The sequential dead-zone limiter detector originally proposed by Shin and Kassam may be treated as a generalized ruin problem that includes ties, and the sequential four level sign detector may be treated as another generalization of the classical ruin problem [1]. These two detectors are analyzed using this approach and their performances compared to that of the more familiar sequential sign detector in terms of relative efficiency and asymptotic relative efficiency. The Neyman-Pearson optimal detector and a likelihood ratio detector can sometimes be designed by first transforming the input data and their statistical characteristics into a multinomial vector and a set of multinomial parameters [8]. These detectors can be simple, flexible and their performance can be quite good.

The need often arises in practice for a correlated non-Gaussian random sequence for purposes of testing and simulation. A scheme consisting of a white noise Gaussian input to a digital filter followed by a zero-memory nonlinearity is proposed and analyzed [4]. The nonlinearity is chosen so that the desired distribution is exactly realized and the digital filter is designed so that the desired autocorrelation function is approximated. A number of theoretical questions in connection with this scheme are answered.

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The prime factor FFT makes use of some recent computational complexity results by Winograd to compute the DFT with a fewer number of multiplications than required by the FFT. An expression for the MSE due to roundoff error when floating point arithmetic is used is derived and a simple bound on the MSE is obtained [5].

Presently, programmable CCD filters are implemented through involved additional complexity at each filter tap. A delta modulation like sampled analog filter structure for realizing low-pass filter is developed [6,7]. By employing decimation and interpolation, the filter uses only coefficients 0, +1, and -1 and can be fabricated as a programmable CCD filter.

Other works during this period also include a further refinement of narrowband spectrum analysis by direct decimation and the use of recursive filters for decimation and interpolation [11,12].

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September 1980

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PUBLICATIONS

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Papers Published

1. "Generalizations of the Sequential Sign Detector", C.C. Lee and J.B. Thomas, Proc. Seventeenth Annual Allerton Conf. on Comm. Control, and Computing, October 1979, pp. 848-857.
2. "ROC/ACC Realization of Digital Filters for Poles Near the Unit Circle", D.C. Munson Jr. and B. Liu, IEEE Trans. on Circuits & Systems, Vol. CAS-27, No. 2, February 1980, pp. 147-151.
3. "Low-Noise Realizations for Narrow-Band Recursive Digital Filters", D.C. Munson Jr. and B. Liu, IEEE Trans. on Acoustics, Speech, and Signal Processing, Vol. ASSP-28, No. 1, February 1980, pp. 41-54.
4. "On Computer Generation of Random Sequences", D.C. Munson and B. Liu, Proc. Fourteenth Annual Conf. on Info. Sciences & Systems, Princeton, NJ, March 1980.
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7. "An Approach to Programmable CTD Filters Using Coefficients 0,+1, and -1", M.R. Batemand and B. Liu, IEEE Trans. on Circuits and Systems, Vol. CAS-27, No. 6, June 1980, pp. 451-456.

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12. "Efficient Sampling Rate Alteration Using Recursive (IIR) Digital Filters," R. Ansari and B. Liu, IEEE Trans. on Acoustics, Speech, and Signal Processing.

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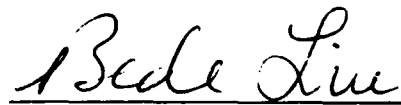
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20. ABSTRACT (Continue on reverse side if necessary and identify by block number) A number of results were obtained during the period 1 August 1979 to 30 July 1980. Studies were made of the implementation of narrowband digital filters with reduced roundoff error and limit cycle oscillations. Bounds on limit cycles of second order filters were derived for filters using error spectrum shaping. The performance of two sequential signal detectors were analyzed. A scheme for the computer generation of random sequences with a specified first order distribution and a specified autocovariance function is proposed and analyzed. A delta modulation like sampled analog filter structure is		

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