

MICROCOPY RESOLUTION TEST CHART
 NATIONAL BUREAU OF STANDARDS-1963-A

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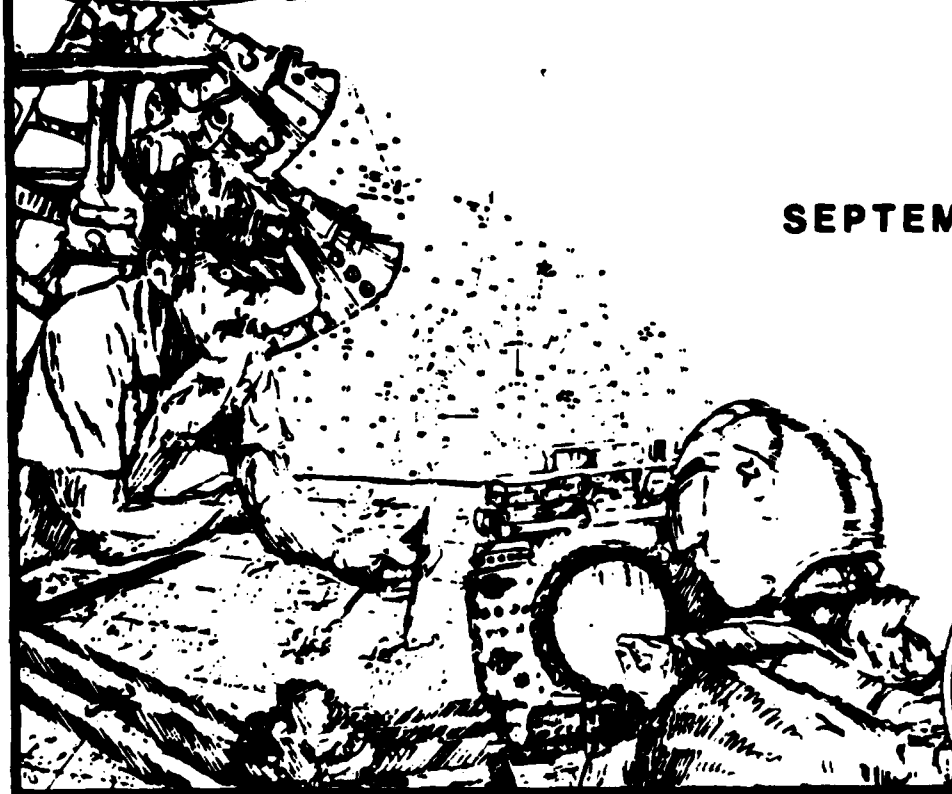
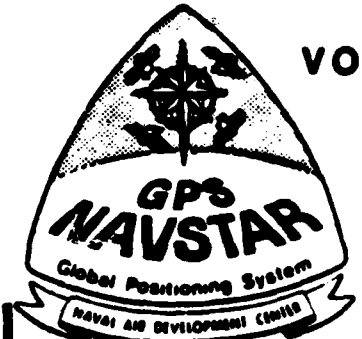
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RF LABORATORY

UE BENCH TEST PLANS AND REQUIREMENTS

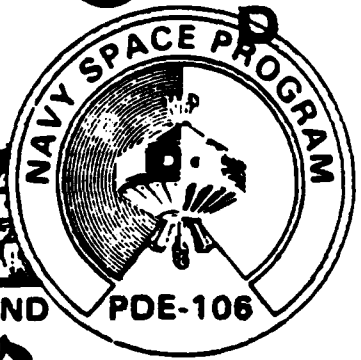
VOLUME 1. MAGNAVOX UE SET

Esscube Engineering Inc.
Marlton, NJ 08053
Contract No. N62269-82-D-0059



SEPTEMBER 1984

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UE BENCH TEST PLANS
AND REQUIREMENTS
VOLUME 1. MAGNAVOX UE SET

SEPTEMBER 1984

CEA/RFL-84-005

Prepared for:
Naval Air Development Center
Warminster, PA 18974

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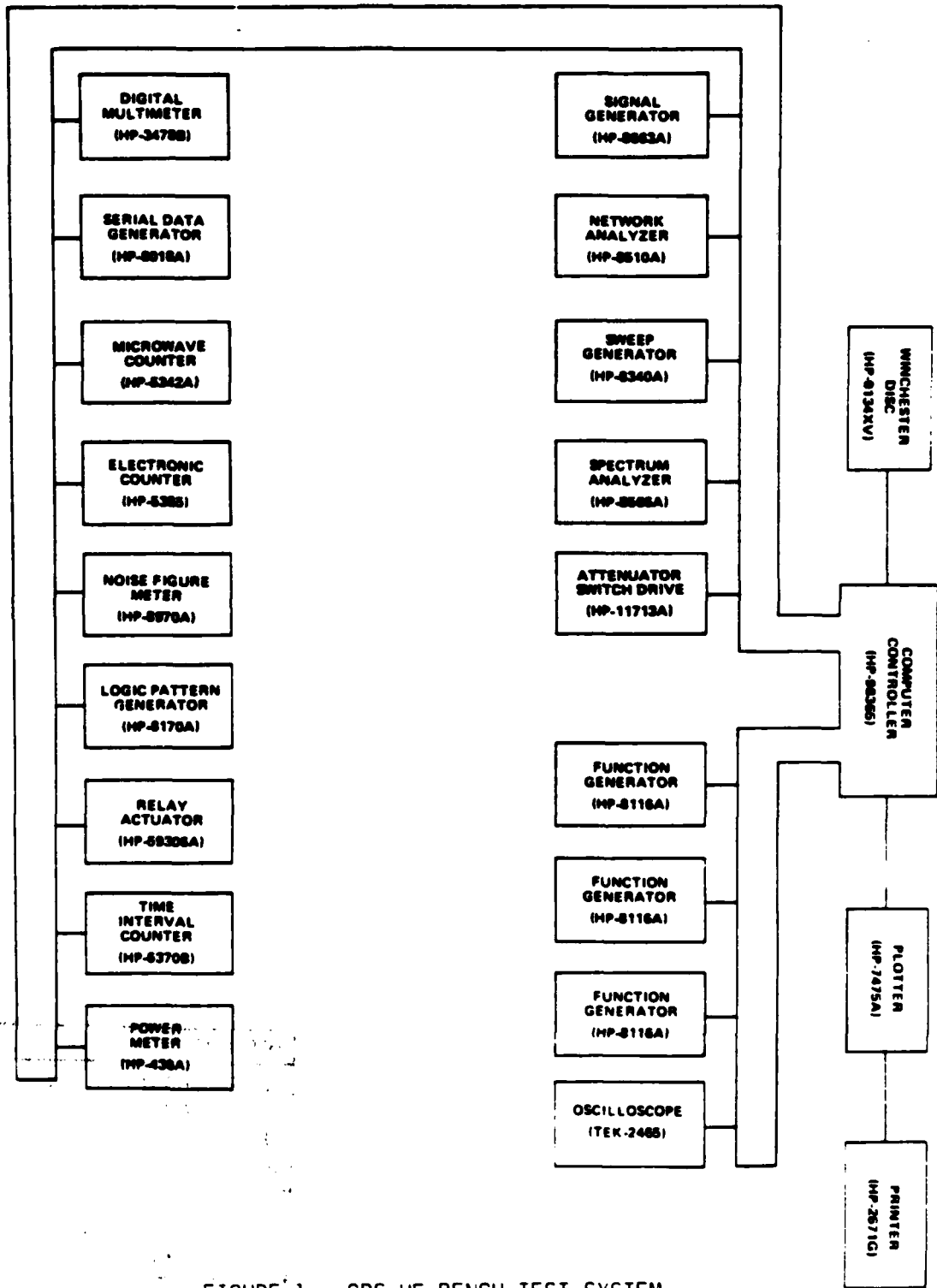


FIGURE 1. GPS UE BENCH TEST SYSTEM

BACKGROUND

When the GPS becomes operational, the RF Laboratory of the NAVAIRDEVCON GPS Central Engineering Activity will be required to provide a capability to resolve problems which relate to the performance of the RF elements of the UE.

In order to meet these requirements, the RF Laboratory is planning to provide for:

- o computer analysis of the problem using a UE RF Emulator,
- o RF bench test of the UE down to the component level using modern automatic testing techniques,
- o system level testing of the UE and antenna system using a Satellite Signal Generator and mockups of actual antenna installations.

The bench tests will involve many detailed measurements to determine the effect of UE components upon the performance of the UE as a receiver and processor of the GPS signal. Special purpose software and special purpose hardware will be required to perform tests, stimulate the various function generators, pattern generators, signal generators, sweep generators, jammers and noise sources, control the meters, analyzers, counters, oscilloscopes and data recording devices and to perform some real-time data reduction for quick-look analyses.

In order to define this special software and hardware, it was necessary to devise individual tests on UE RF components using available information on both the Magnavox and the Collins Phase II UE design.

The GPS UE Bench Test System is shown in Figure 1. It uses an IEEE-488 bus to control the required stimulus, measurement and recording equipment. The computer controller will have access to all test procedures and will be able to string a group of test procedures together for different tests using the same test setup. Test data will be reduced for quick look analysis and for comparison with standard or baseline data. The system will also produce discs for offline analysis and longterm data retention. Detailed specifications for each element of the UE Bench Test System can be found in Reference 4, Volume 1.

The UE Bench Test Procedures are organized into three volumes. Volume one contains the Magnavox Bench Test Procedures and software requirements, volume two contains the Rockwell-Collins Bench Test Procedures and software requirements and volume three contains the General Receiver Tests, Antenna Test Procedures for both contractors and a section on special purpose hardware requirements.

It is intended that this document will be expanded and refined as more detailed performance information becomes available on UE RF components.

GPS UE BENCH TEST PLANS AND REQUIREMENTS

VOLUME 1

1.0 PURPOSE

The purpose of this report is to provide a basis for defining the hardware and software required to perform RF related bench tests of the Magnavox UE in the RF Laboratory of the GPS Central Engineering Activity at NAVAIRDEVCEEN.

1.1 SCOPE

This report contains general test requirements for each test defining the:

- level of test (UE, board, component)
- test objective (parameter/function)
- test inputs
- test outputs
- equipment required for each input/output
- initial test procedure
- data reduction requirements
- summary list of test equipment (standard/special)
- block diagram of test)

Since most of these tests will be computer controlled, special software will be required to control the conditions, parameters and data collection needs of the test.

A section on special software requirements is provided which defines the software needs of each test in terms of flowcharts.

2.0 LIST OF REFERENCE DOCUMENTS

The following documents were used as references for UE RF configuration and performance requirements:

- | | | | |
|----|-----------------------------|--|--------------|
| 1. | ICD-GPS-204 | GPS Instrumentation and Connector Stds. | 5 June 1981 |
| 2. | SS-US-200 | System Segment Specification | 4 Nov. 1982 |
| 3. | Magnavox | Critical Design Review | 3 Sept. 1982 |
| 4. | ESSCUBE
N62269-82-D-0059 | Specification for a GPS UE Bench Test System | 1 May 1984 |

3.0 TEST REQUIREMENTS

The test requirements contained herein deal with the Magnavox UE set and are written at the board level. Specific values of parameters, ranges, tolerances and granularities are provided if known. As more detailed information on the sets become available, missing values will be provided and the test requirements will be refined and expanded down to the component level.

3.1 MAGNAVOX UE TEST REQUIREMENTS

This section contains the test requirements, procedures and block diagrams for board level testing of the Magnavox UE set. Initial functional test procedures have been compiled for the following boards or units:

1. RF Synthesizer
2. Converter/Channel Switcher
3. IF/BB/Coder
4. Pre Amplifier Unit Module
5. Reference Oscillator

Although information is available for these boards, there is not enough to completely identify board input and output levels for stimulation and measurement purposes. For example, some of the boards require digital input signals as stimuli. There is not enough information available to determine the required input levels for these signals or the levels for output signals. These digital signals also present a problem in that special purpose hardware may be required to interface the test equipment to the board being tested. This problem will be discussed further in Volume 3, Section 5.0 (Special Purpose Hardware Requirements).

No information has been located concerning Magnavox's Antenna Electronics or Power Supply. Test requirements for these units will be provided as information becomes available.

3.1.1 Magnavox Test Procedures

The following sheets contain the initial test requirements and procedures as listed in section 1.1 for each of the boards identified in section 3.1. These sheets will be refined and expanded as more information becomes available. Also as more detailed data on the boards becomes available sheets will be added for testing to the component level.

3.1.1.1 RF SYNTHESIZER MODULE

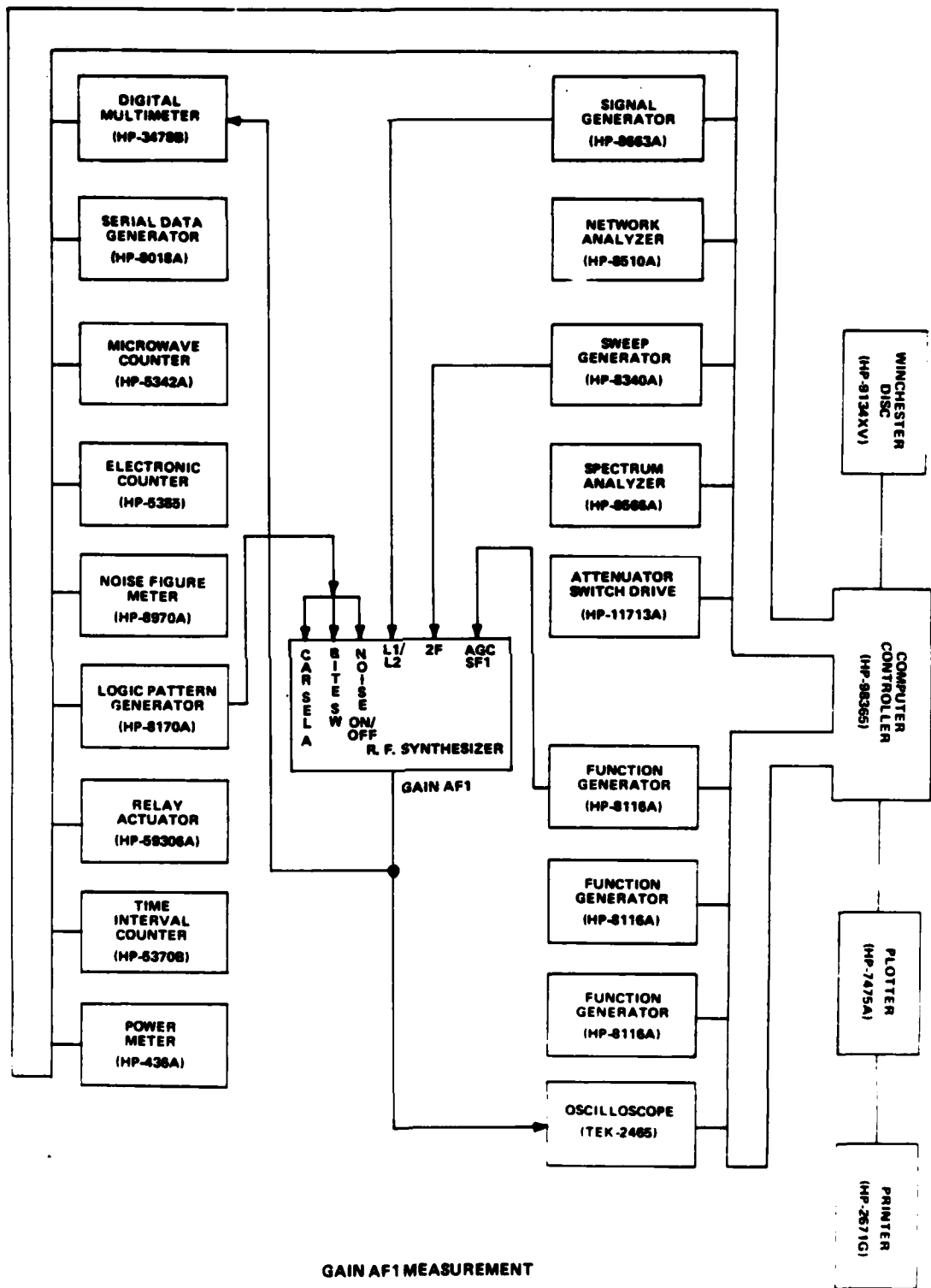
Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Verify GAIN AF1 operational

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Carrier Select A</u>	<u>TBD</u>	<u>Logic Pattern Gen. (HP-8170A)</u>
2.	<u>BITE SW</u>	<u>TBD</u>	<u>Logic Pattern Gen. (HP-8170A)</u>
3.	<u>Noise On/Off</u>	<u>TBD</u>	<u>Logic Pattern Gen. (HP-8170A)</u>
4.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Sweep Gen. (HP-8340A)</u>
5.	<u>L1/L2</u>	<u>-120dBm @ L1/L2</u>	<u>Signal Gen. (HP-8663A)</u>
6.	<u>AGC S/F1</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>



GAIN AF1 MEASUREMENT

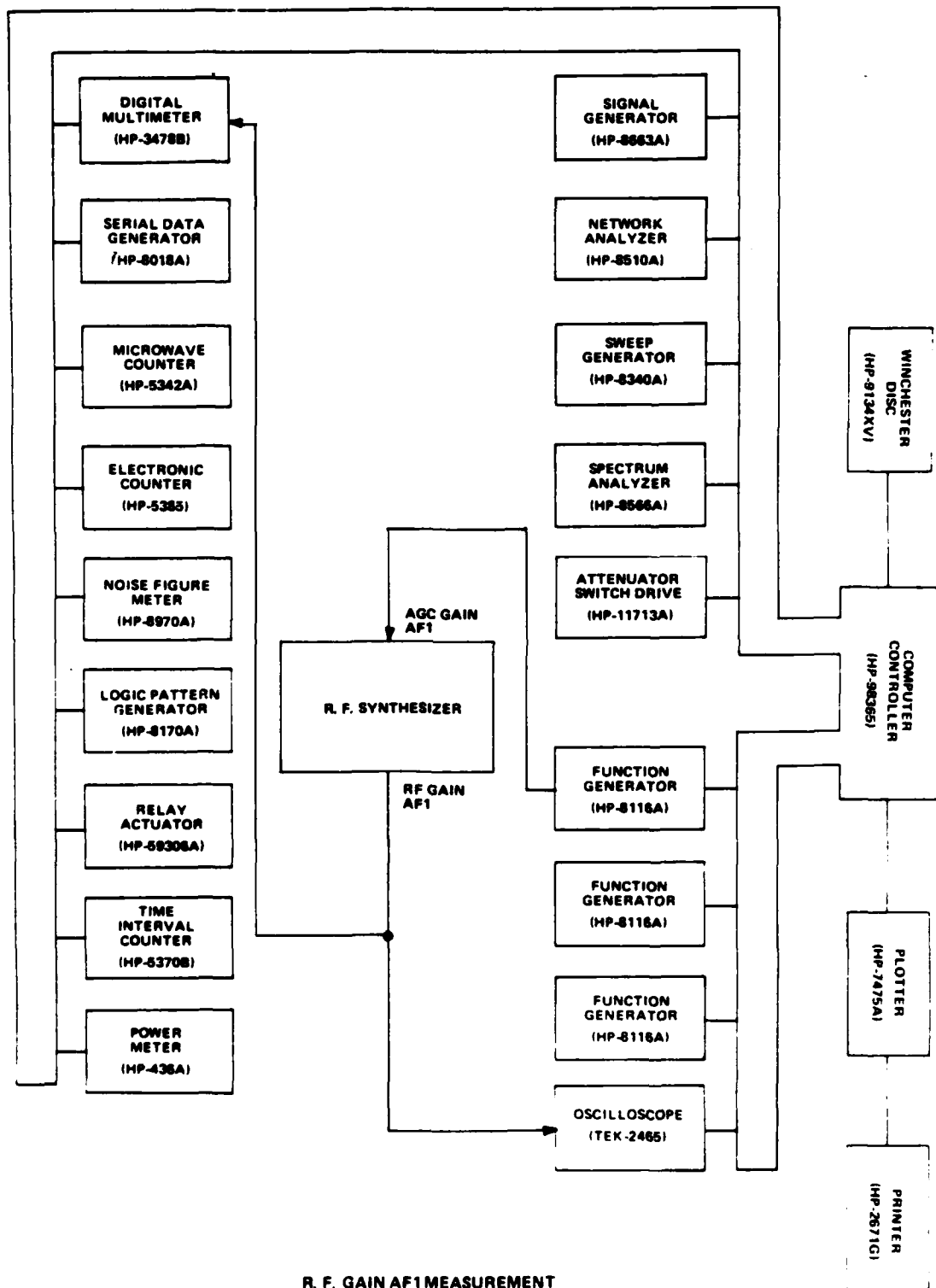
Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Verify RF GAIN AF1 operational

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>RF GAIN AF1</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
2.	<u></u>	<u></u>	<u></u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

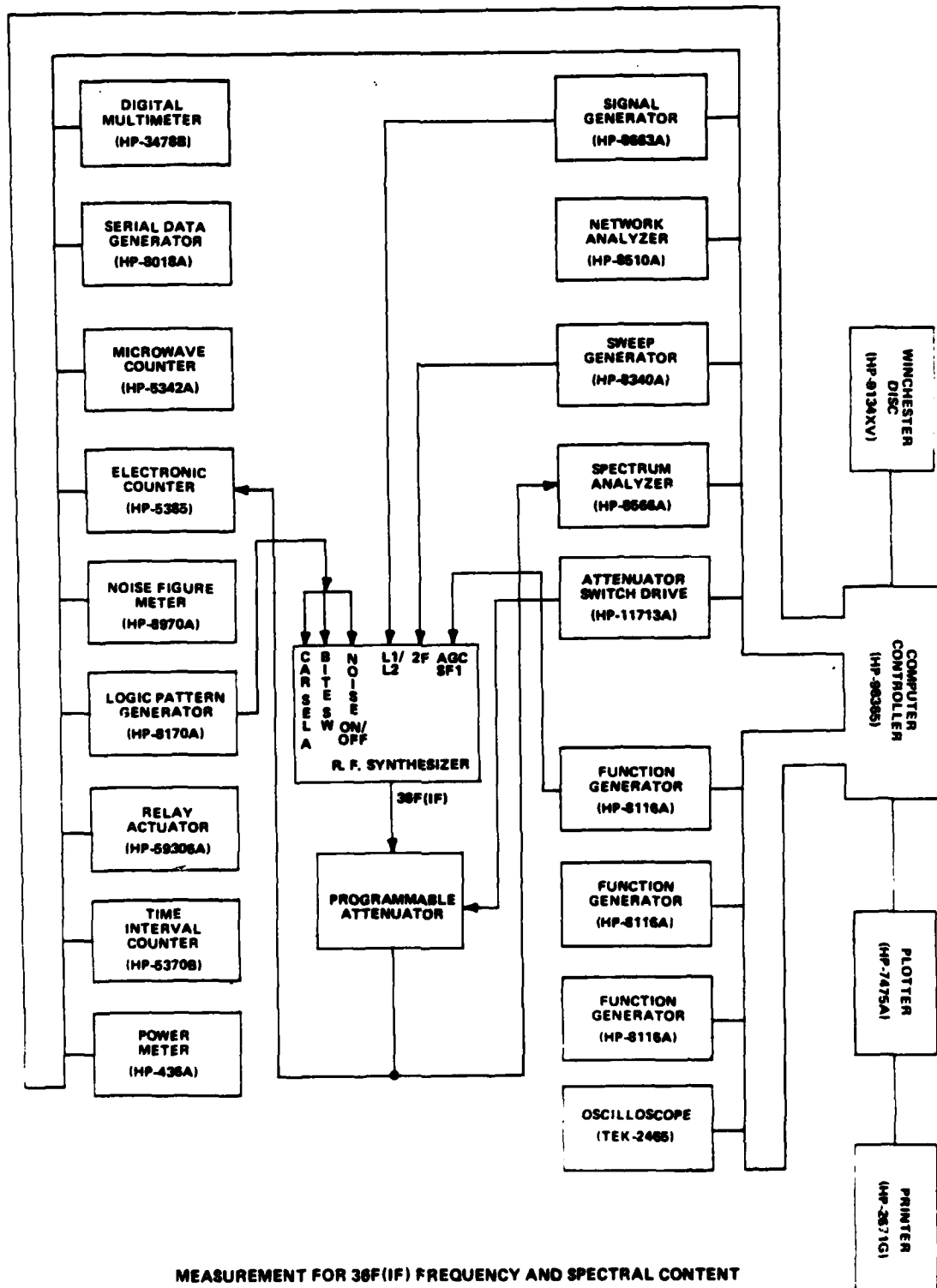


R. F. GAIN AF1 MEASUREMENT

Contractor: Magnavox
Board Tested: RF Synthesizer
Test Objective: Measure 36F(IF) output frequency and spectral content.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Carrier Select A</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
2.	<u>BITE SW</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
3.	<u>NOISE On/Off</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
4.	<u>L1/L2</u>	<u>-120dBm @ L1/L2</u>	<u>Sig. Gen. (HP-8663A)</u>
5.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Sweep Gen. (HP-8340A)</u>
6.	<u>AGC SF1</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>



MEASUREMENT FOR 36F(IF) FREQUENCY AND SPECTRAL CONTENT

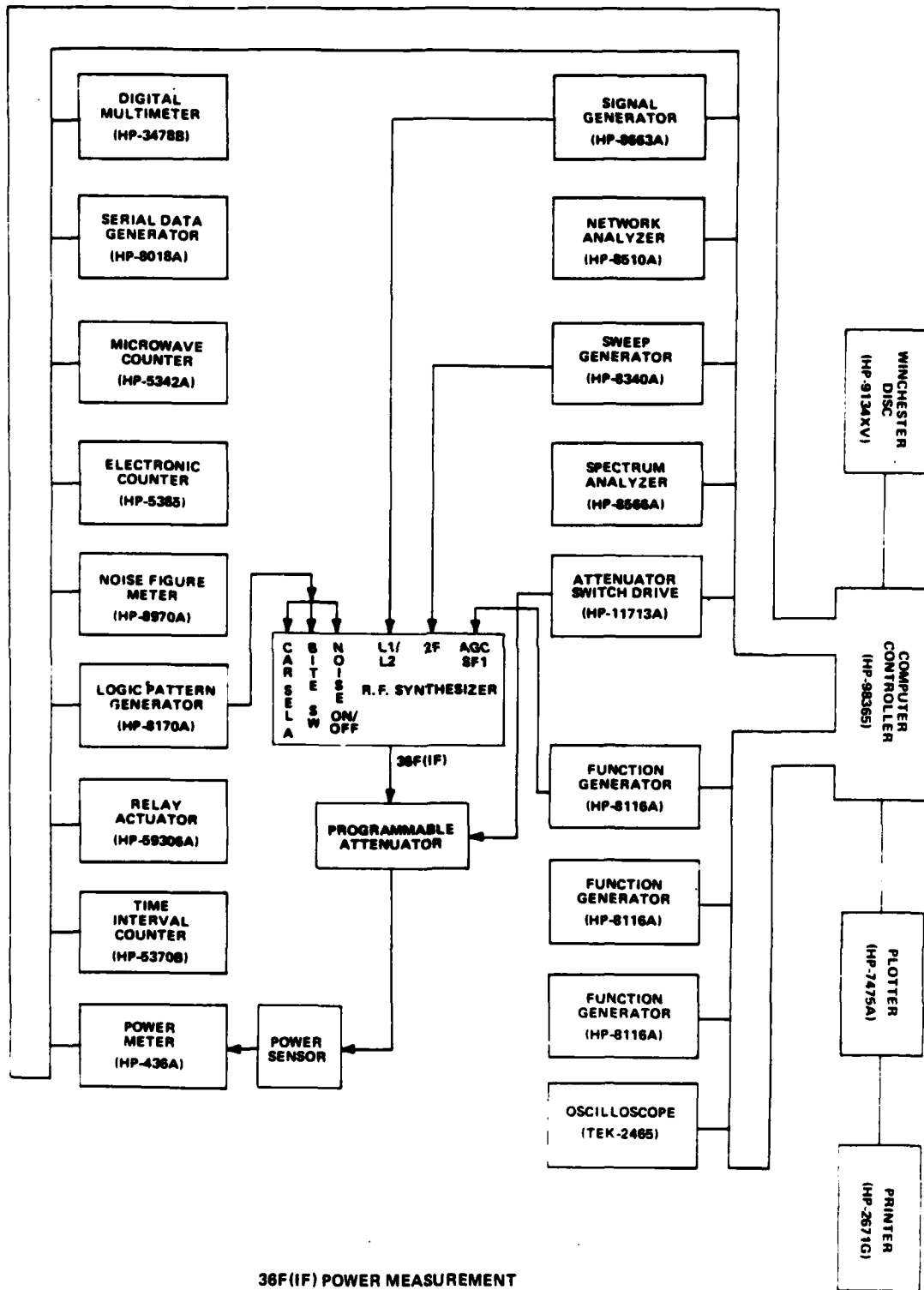
Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Power measurement for 36F(IF)

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Carrier Select A</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
2.	<u>BITE SW</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
3.	<u>Noise On/Off</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
4.	<u>L1/L2</u>	<u>-120dBm @ L1/L2</u>	<u>Sig. Gen. (HP-8663A)</u>
5.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Sweep Gen. (HP-8340A)</u>
6.	<u>AGC SF 1</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>



Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure VSWR and impedance of 36F(IF) output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>S-Par. Test Set</u>	<u>TBD</u>	<u>Sweep Gen. (HP-8340A)</u>
2.	<u>_____</u>	<u>_____</u>	<u>_____</u>
3.	<u>_____</u>	<u>_____</u>	<u>_____</u>
4.	<u>_____</u>	<u>_____</u>	<u>_____</u>
5.	<u>_____</u>	<u>_____</u>	<u>_____</u>
6.	<u>_____</u>	<u>_____</u>	<u>_____</u>
7.	<u>_____</u>	<u>_____</u>	<u>_____</u>
8.	<u>_____</u>	<u>_____</u>	<u>_____</u>
9.	<u>_____</u>	<u>_____</u>	<u>_____</u>
10.	<u>_____</u>	<u>_____</u>	<u>_____</u>
11.	<u>_____</u>	<u>_____</u>	<u>_____</u>
12.	<u>_____</u>	<u>_____</u>	<u>_____</u>

Outputs

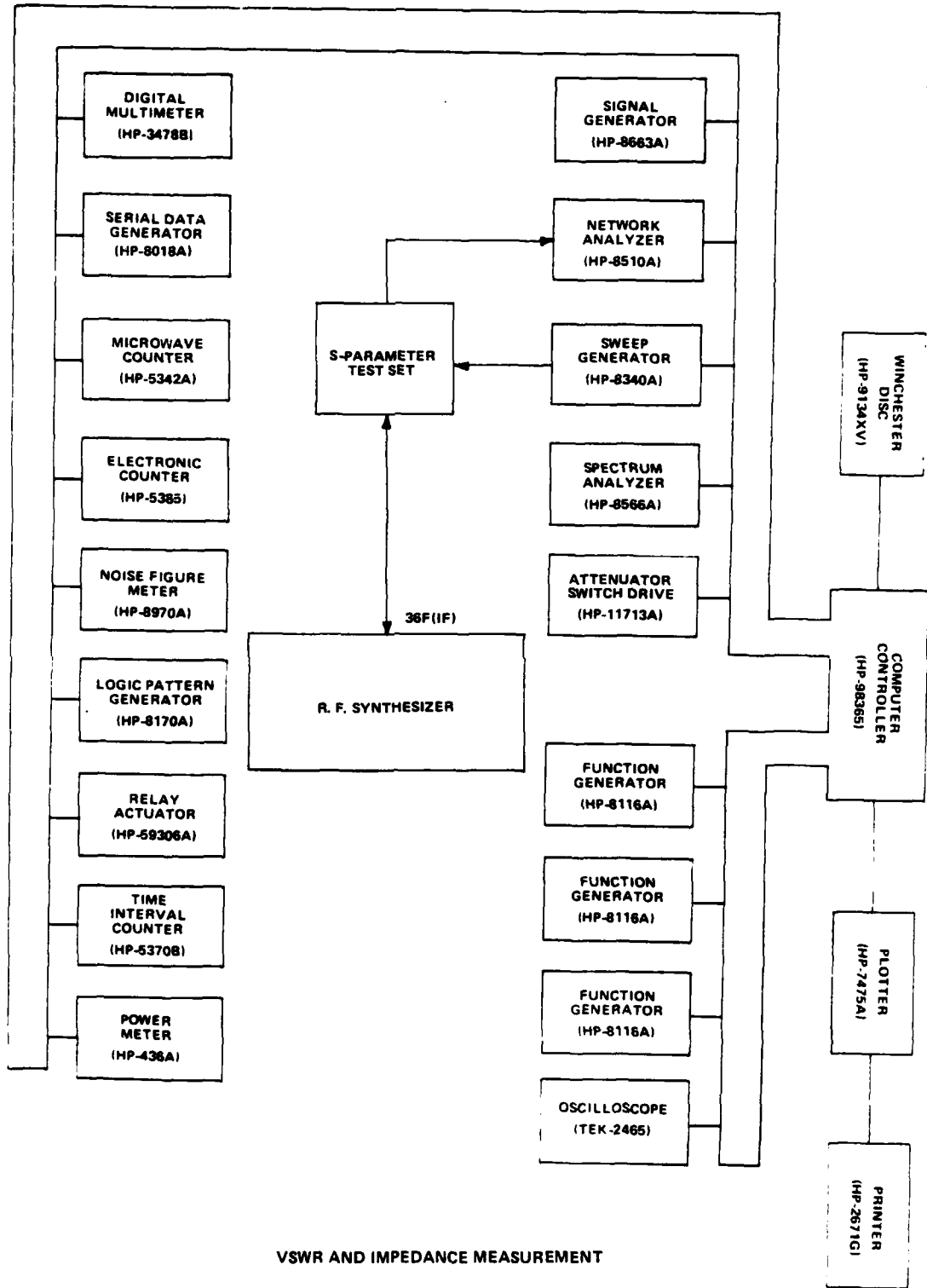
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>36F(IF)</u>	<u>TBD</u>	<u>Network Anal. (HP-8510A)</u>
2.	<u> </u>	<u> </u>	<u>S-Par. Test Set (HP-8515A)</u>
3.	<u> </u>	<u> </u>	<u> </u>
4.	<u> </u>	<u> </u>	<u> </u>
5.	<u> </u>	<u> </u>	<u> </u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply input signal to S-Parameter Test Set from Sweep
Generator. Measure VSWR and impedance with Network Analyzer.

Data Reduction: Send input levels to printer. Output of Network
Analyzer to plotter.

Equipment List:

- | | | |
|-------------------------------|-------------------------------------|--|
| 1. <u>Computer (HP-9836S)</u> | 4. <u>S-Par Test Set (HP-8515A)</u> | 7. <u> </u> |
| 2. <u>Printer (HP-2671G)</u> | 5. <u>Network Ana. (HP-8510A)</u> | 8. <u> </u> |
| 3. <u>Plotter (HP-7475A)</u> | 6. <u>Sweep Gen. (HP-8340A)</u> | 9. <u> </u> |



VSWR AND IMPEDANCE MEASUREMENT

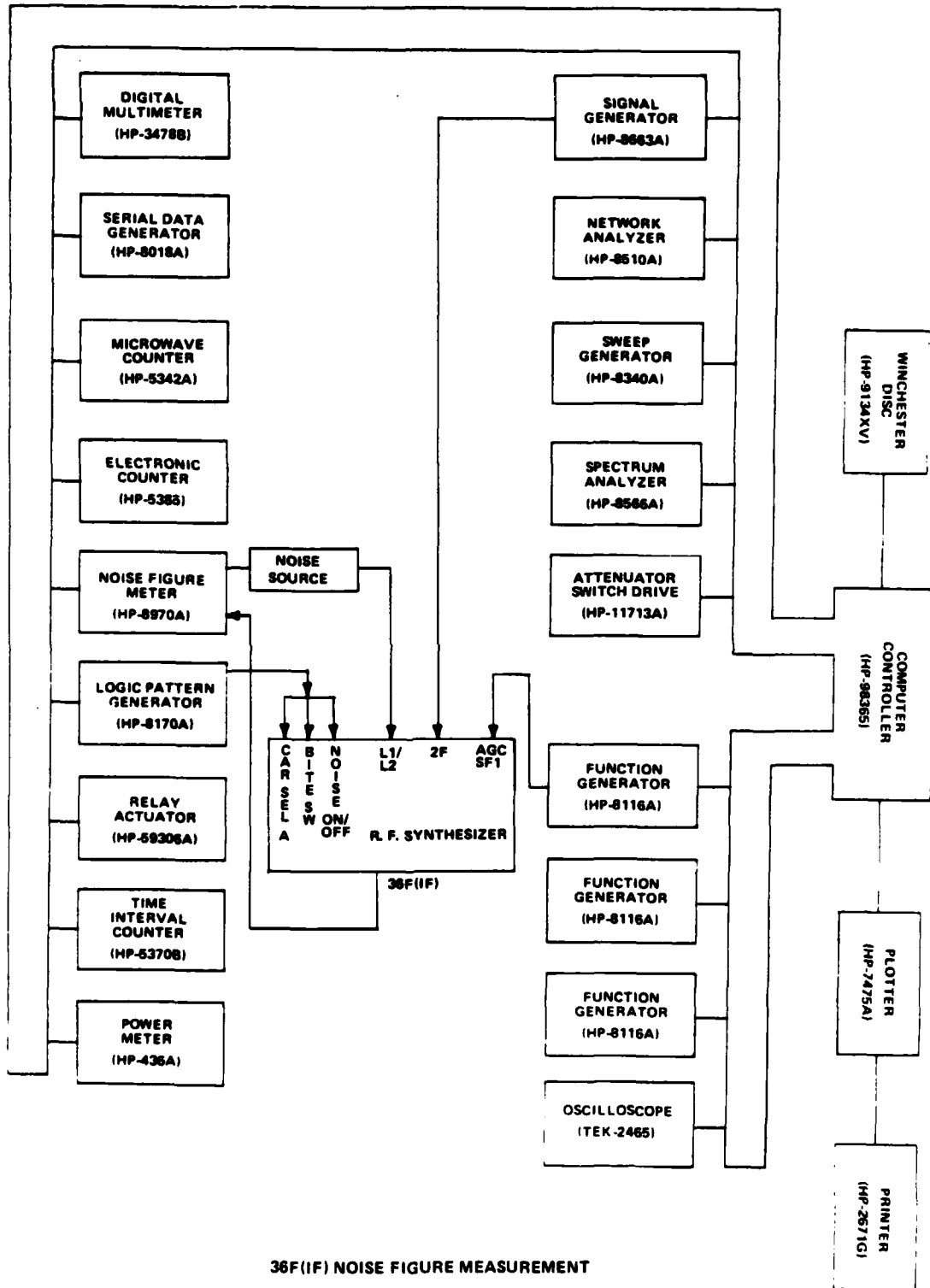
Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure noise figure of 36F(IF) output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Carrier Select A</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
2.	<u>BITE SW</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
3.	<u>Noise On/Off</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
4.	<u>Noise Source</u>	<u>TBD</u>	<u>Noise Source (HP-346B)</u>
5.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
6.	<u>AGC SF 1</u>	<u>TBD</u>	<u>Func. Gen. (HP-8116A)</u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>



36F(IF) NOISE FIGURE MEASUREMENT

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure frequency of 68F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
2.	<u></u>	<u></u>	<u></u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

Outputs

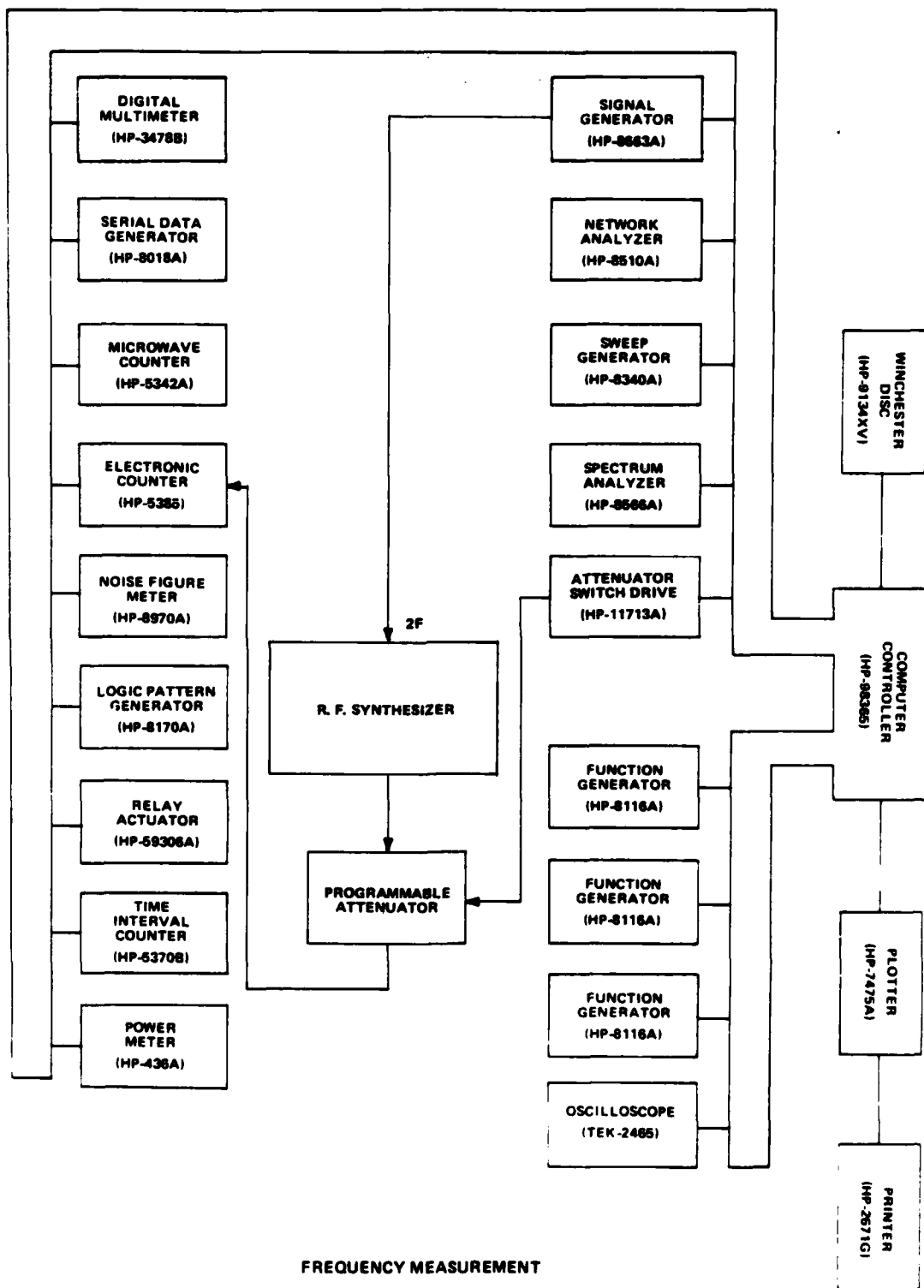
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>68F</u>	<u>347.82MHz</u>	<u>Electronic Ctr. (HP-5385)</u>
2.	<u> </u>	<u> </u>	<u>Att. Switch Drive (HP-11713A)</u>
3.	<u> </u>	<u> </u>	<u>Prog. Att. (HP-8494G)</u>
4.	<u> </u>	<u> </u>	<u> </u>
5.	<u> </u>	<u> </u>	<u> </u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply a 2F frequency to RF Synthesizer reference
oscillator input. Measure output frequency at 68F output with Electronic
Counter.

Data Reduction: Send input levels and output of Electronic Counter
to printer.

Equipment List:

- | | | |
|-----------------------------------|-------------------------------------|--|
| 1. <u>Signal Gen. (HP-8663A)</u> | 4. <u>Electronic Ctr. (HP-5385)</u> | 7. <u> </u> |
| 2. <u>Att. Sw Dr. (HP-11713A)</u> | 5. <u>Printer (HP-2671G)</u> | 8. <u> </u> |
| 3. <u>Prog. Att. (HP-8494G)</u> | 6. <u>Computer (HP-9836S)</u> | 9. <u> </u> |



Contractor: Magnavox
Board Tested: RF Synthesizer
Test Objective: Measure output power of 68F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
2.	<u></u>	<u></u>	<u></u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

Outputs

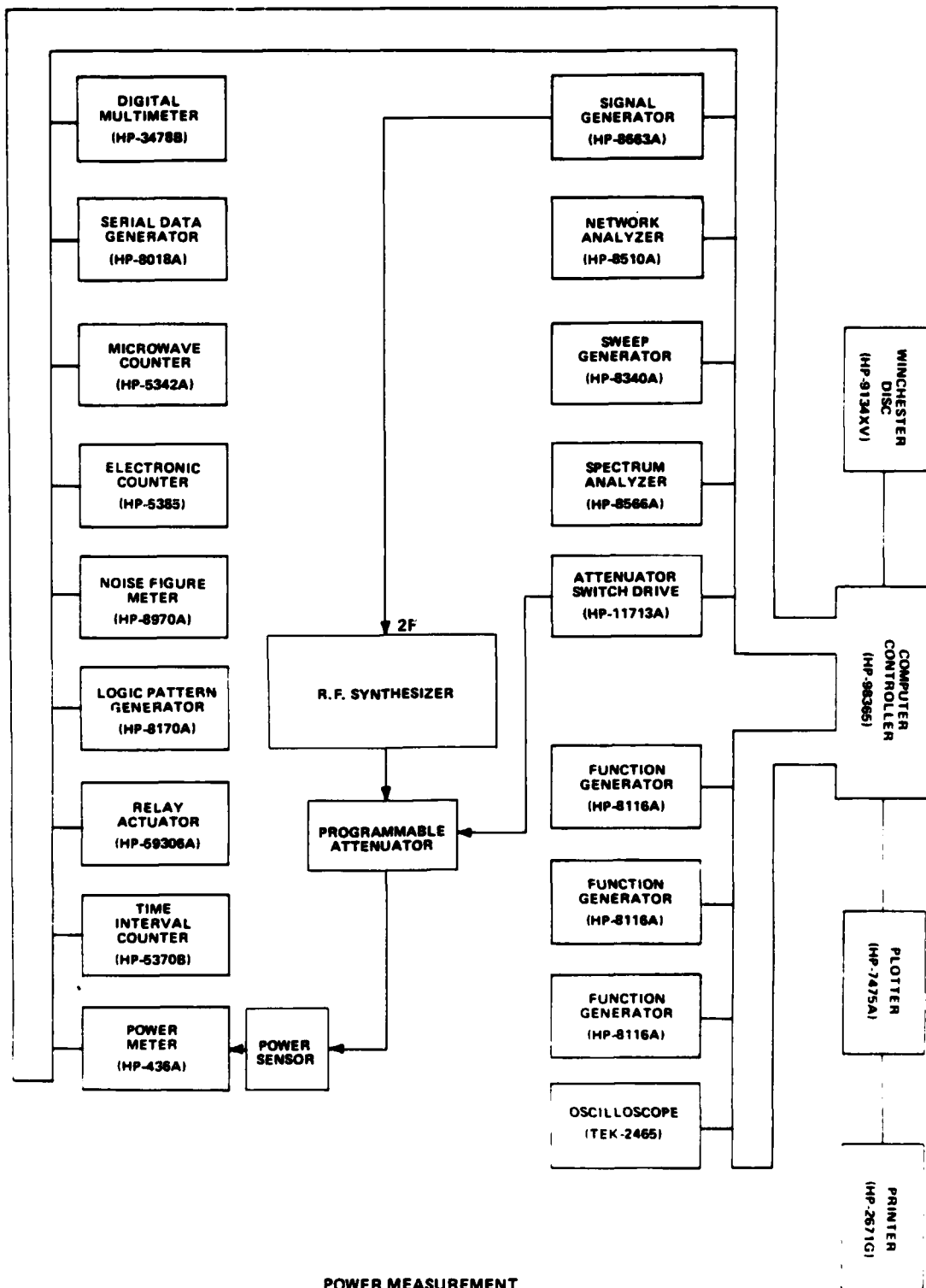
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>68F</u>	<u>-10dBm</u>	<u>Power Meter (HP-436A)</u>
2.	<u> </u>	<u> </u>	<u>Power Sensor (HP-8481A)</u>
3.	<u> </u>	<u> </u>	<u>Att. Sw Drive (HP-11713A)</u>
4.	<u> </u>	<u> </u>	<u>Prog. Att. (HP-8494G)</u>
5.	<u> </u>	<u> </u>	<u> </u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply a 2F signal to the RF Synthesizer's reference
oscillator input. Measure output power at 68F output with Power Meter.

Data Reduction: Send input levels and output of Power Meter to
printer.

Equipment List:

- | | | |
|-----------------------------------|-----------------------------------|--------------------------------|
| 1. <u>Sig. Gen. (HP-8663A)</u> | 4. <u>Att. Sw Dr. (HP-11713A)</u> | 7. <u>Computer (HP-9836S)</u> |
| 2. <u>Power Meter (HP-436A)</u> | 5. <u>Prog. Att. (HP-8494G)</u> | 8. <u> </u> |
| 3. <u>Power Sensor (HP-8481A)</u> | 6. <u>Printer (HP-2671G)</u> | 9. <u> </u> |



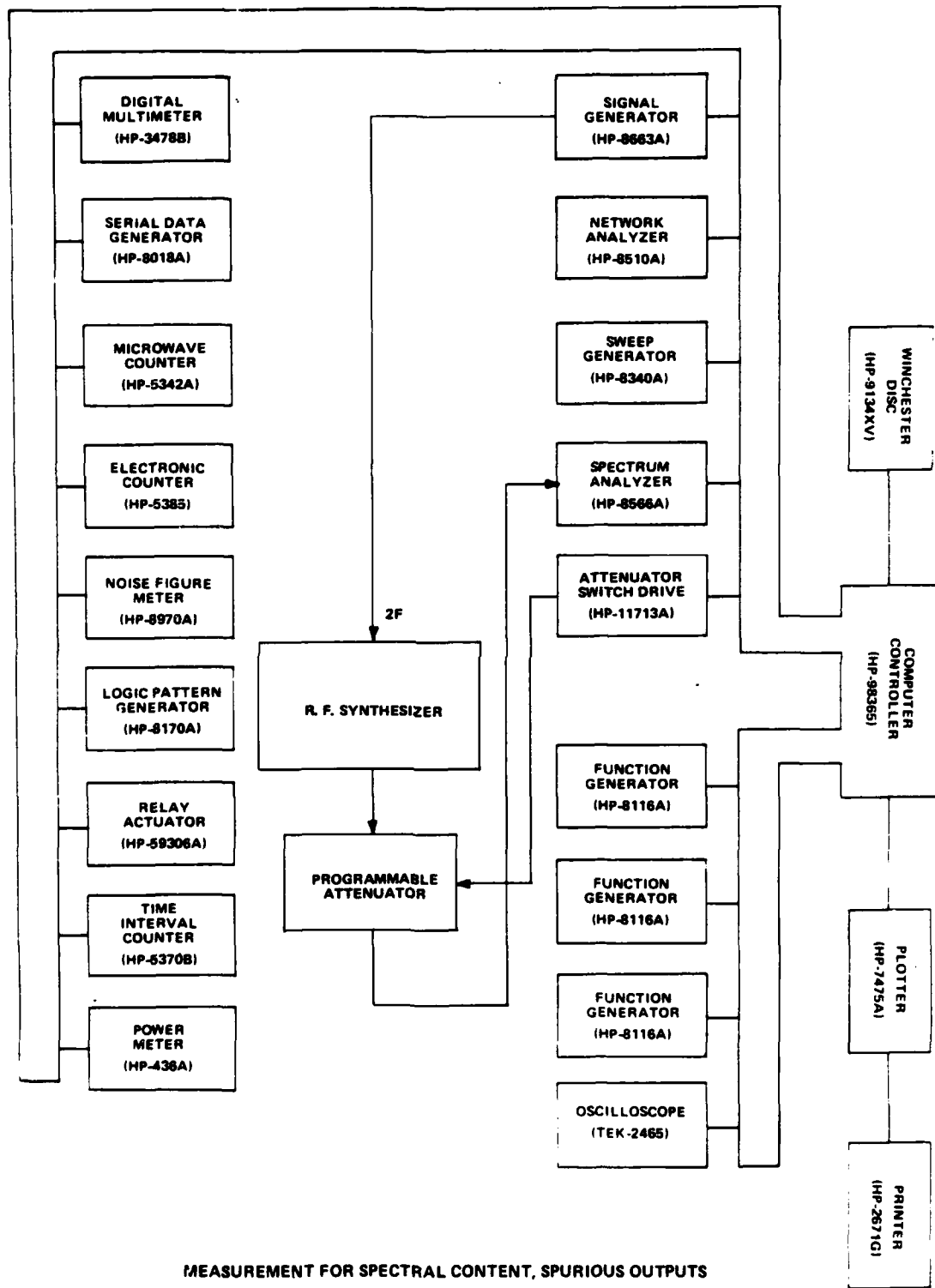
Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Check 68F output for spectral content.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Sig. Gen. (HP-8663A)</u>
2.	<u></u>	<u></u>	<u></u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>



MEASUREMENT FOR SPECTRAL CONTENT, SPURIOUS OUTPUTS

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure impedance and VSWR of 68F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>S-Par Test Set</u>	<u>TBD</u>	<u>Sweep Gen. (HP-8340A)</u>
2.	<u>_____</u>	<u>_____</u>	<u>_____</u>
3.	<u>_____</u>	<u>_____</u>	<u>_____</u>
4.	<u>_____</u>	<u>_____</u>	<u>_____</u>
5.	<u>_____</u>	<u>_____</u>	<u>_____</u>
6.	<u>_____</u>	<u>_____</u>	<u>_____</u>
7.	<u>_____</u>	<u>_____</u>	<u>_____</u>
8.	<u>_____</u>	<u>_____</u>	<u>_____</u>
9.	<u>_____</u>	<u>_____</u>	<u>_____</u>
10.	<u>_____</u>	<u>_____</u>	<u>_____</u>
11.	<u>_____</u>	<u>_____</u>	<u>_____</u>
12.	<u>_____</u>	<u>_____</u>	<u>_____</u>

Outputs

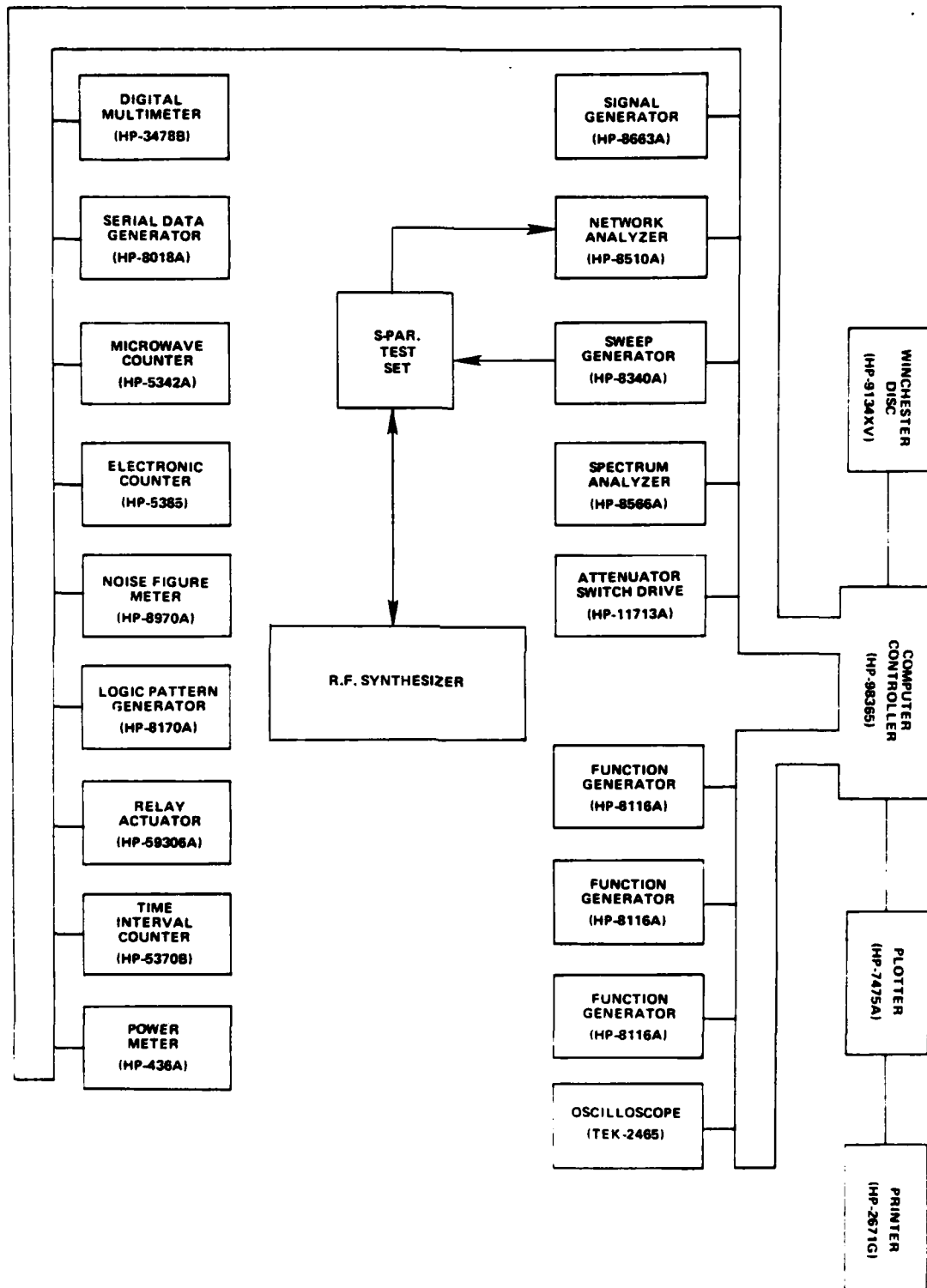
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>68F</u>	<u>TBD</u>	<u>Network Anal. (HP-8510A)</u>
2.	<u> </u>	<u> </u>	<u>S-Par Test Set (HP-8515A)</u>
3.	<u> </u>	<u> </u>	<u> </u>
4.	<u> </u>	<u> </u>	<u> </u>
5.	<u> </u>	<u> </u>	<u> </u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply input signal to S-Parameter Test Set from Sweep
Generator. Measure VSWR and impedance with Network Analyzer.

Data Reduction: Send input levels to printer. Output of Network
Analyzer to plotter.

Equipment List:

- | | | | | | |
|----|----------------------------|----|----------------------------------|----|---|
| 1. | <u>Computer (HP-9836S)</u> | 4. | <u>S-Par Test Set (HP-8515A)</u> | 7. | <u> </u> |
| 2. | <u>Printer (HP-2671G)</u> | 5. | <u>Network Anal. (HP-8510A)</u> | 8. | <u> </u> |
| 3. | <u>Plotter (HP-7475A)</u> | 6. | <u>Sweep Gen. (HP-8340A)</u> | 9. | <u> </u> |



VSWR AND IMPEDANCE MEASUREMENT

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure output frequency of 29⁷/8F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
2.	<u> </u>	<u> </u>	<u> </u>
3.	<u> </u>	<u> </u>	<u> </u>
4.	<u> </u>	<u> </u>	<u> </u>
5.	<u> </u>	<u> </u>	<u> </u>
6.	<u> </u>	<u> </u>	<u> </u>
7.	<u> </u>	<u> </u>	<u> </u>
8.	<u> </u>	<u> </u>	<u> </u>
9.	<u> </u>	<u> </u>	<u> </u>
10.	<u> </u>	<u> </u>	<u> </u>
11.	<u> </u>	<u> </u>	<u> </u>
12.	<u> </u>	<u> </u>	<u> </u>

Outputs

	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>29⁷/8F</u>	<u>152.81MHz</u>	<u>Electronic Ctr (HP-5385)</u>
2.	<u> </u>	<u> </u>	<u>Att. Sw Dr. (HP-11713A)</u>
3.	<u> </u>	<u> </u>	<u>Prog. Att. (HP-8494G)</u>
4.	<u> </u>	<u> </u>	<u> </u>
5.	<u> </u>	<u> </u>	<u> </u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply a 2F signal to the RF Synthesizer's reference
oscillator input. Measure output frequency at 29⁷/8F output with Elec-
tronic Counter.

Data Reduction: Send input levels and output of Electronic Counter
to printer.

Equipment List:

- | | | |
|------------------------------------|------------------------------------|--|
| 1. <u>Computer (HP-9836S)</u> | 4. <u>Att. Sw Dr. (HP -11713A)</u> | 7. <u> </u> |
| 2. <u>Sig. Gen. (HP-8663A)</u> | 5. <u>Prog. Att. (HP-8494G)</u> | 8. <u> </u> |
| 3. <u>Electronic Ctr (HP-5385)</u> | 6. <u>Printer (HP-2671G)</u> | 9. <u> </u> |

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure output power at 29⁷/8F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
2.	<u></u>	<u></u>	<u></u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

Outputs

	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>29⁷8F</u>	<u>-10dBm</u>	<u>Power Meter (HP-436A)</u>
2.	<u> </u>	<u> </u>	<u>Power Sensor (HP-8481A)</u>
3.	<u> </u>	<u> </u>	<u>Att. Sw Dr. (HP-11713A)</u>
4.	<u> </u>	<u> </u>	<u>Prog. Att. (HP-8494G)</u>
5.	<u> </u>	<u> </u>	<u> </u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply a 2F signal to the RF Synthesizer's reference
oscillator input. Measure output power at 29⁷8F output with Power Meter.

Data Reduction: Send input levels and output of Power Meter to printer.

Equipment List:

- | | | |
|---------------------------------|-----------------------------------|--------------------------------|
| 1. <u>Computer (HP-9836S)</u> | 4. <u>Power Sensor (HP-8481A)</u> | 7. <u>Printer (HP-2671G)</u> |
| 2. <u>Sig. Gen. (HP-8663A)</u> | 5. <u>Att. Sw Dr. (HP-11713A)</u> | 8. <u> </u> |
| 3. <u>Power Meter (HP-436A)</u> | 6. <u>Prog. Att. (HP-8494G)</u> | 9. <u> </u> |

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure spectral content of 29⁷/8F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
2.	<u></u>	<u></u>	<u></u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure output impedance and VSWR of 29⁷/8F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>S-Par Test Set</u>	<u>TBD</u>	<u>Sweep Gen. (HP-8340A)</u>
2.	<u>_____</u>	<u>_____</u>	<u>_____</u>
3.	<u>_____</u>	<u>_____</u>	<u>_____</u>
4.	<u>_____</u>	<u>_____</u>	<u>_____</u>
5.	<u>_____</u>	<u>_____</u>	<u>_____</u>
6.	<u>_____</u>	<u>_____</u>	<u>_____</u>
7.	<u>_____</u>	<u>_____</u>	<u>_____</u>
8.	<u>_____</u>	<u>_____</u>	<u>_____</u>
9.	<u>_____</u>	<u>_____</u>	<u>_____</u>
10.	<u>_____</u>	<u>_____</u>	<u>_____</u>
11.	<u>_____</u>	<u>_____</u>	<u>_____</u>
12.	<u>_____</u>	<u>_____</u>	<u>_____</u>

Outputs

	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>2978F</u>	<u>TBD</u>	<u>Network Analyzer (HP-8510A)</u>
2.	<u> </u>	<u> </u>	<u>S-Par Test Set (HP-8515A)</u>
3.	<u> </u>	<u> </u>	<u> </u>
4.	<u> </u>	<u> </u>	<u> </u>
5.	<u> </u>	<u> </u>	<u> </u>

Test Procedure: Turn on test equipment. Set measurement equipment to
proper ranges. Apply input signal to S-Parameter Test Set from Sweep
Generator. Measure VSWR and impedance with Network Analyzer.

Data Reduction: Send input levels to printer. Output of Network
Analyzer to plotter.

Equipment List:

- | | | |
|-------------------------------|-------------------------------------|--------------------------------|
| 1. <u>Computer (HP-9836S)</u> | 4. <u>S-Par Test Set (HP-8515A)</u> | 7. <u> </u> |
| 2. <u>Printer (HP-2671G)</u> | 5. <u>Network Anal. (HP-8510A)</u> | 8. <u> </u> |
| 3. <u>Plotter (HP-7475A)</u> | 6. <u>Sweep Gen. (HP-8340A)</u> | 9. <u> </u> |

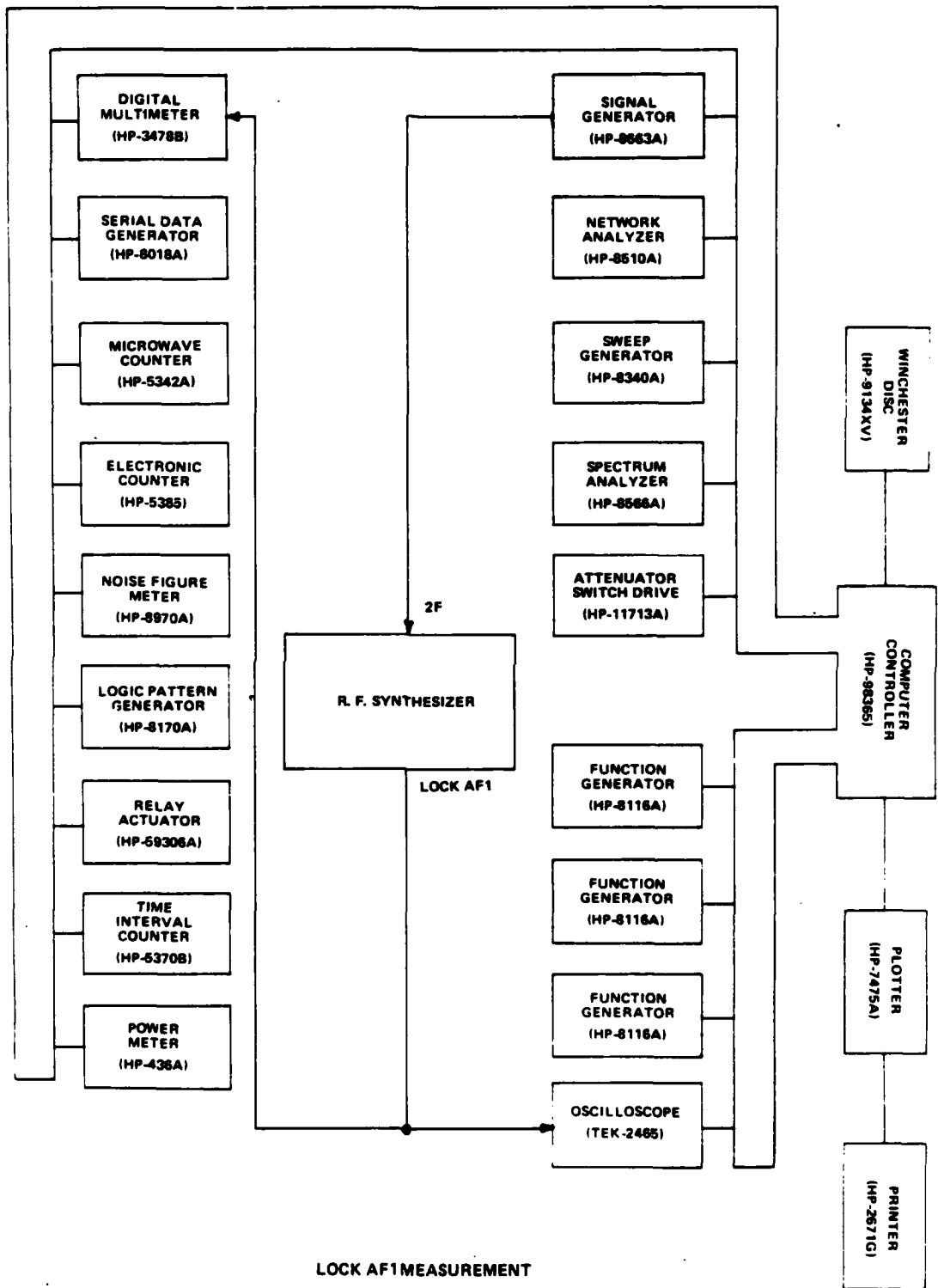
Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Verify AF1 LOCK operational.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
2.	<u></u>	<u></u>	<u></u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>



LOCK AF1 MEASUREMENT

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure output frequency at 6F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
2.	_____	_____	_____
3.	_____	_____	_____
4.	_____	_____	_____
5.	_____	_____	_____
6.	_____	_____	_____
7.	_____	_____	_____
8.	_____	_____	_____
9.	_____	_____	_____
10.	_____	_____	_____
11.	_____	_____	_____
12.	_____	_____	_____

Outputs

	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>6F</u>	<u>30.69MHz</u>	<u>Electronic Ctr (HP-5385)</u>
2.	<u> </u>	<u> </u>	<u>Att. Sw Drive (HP-11713A)</u>
3.	<u> </u>	<u> </u>	<u>Prog. Att. (HP-8494G)</u>
4.	<u> </u>	<u> </u>	<u> </u>
5.	<u> </u>	<u> </u>	<u> </u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply a 2F signal to the RF Synthesizer's reference
oscillator input. Measure output frequency at 6F output with Electronic
Counter.

Data Reduction: Send input levels and output of Electronic Counter
to printer.

Equipment List:

- | | | |
|--------------------------------|-----------------------------------|--|
| 1. <u>Computer (HP-9836S)</u> | 4. <u>Att. Sw Dr. (HP-11713A)</u> | 7. <u> </u> |
| 2. <u>Sig. Gen. (HP-8663A)</u> | 5. <u>Prog. Att. (HP-8494G)</u> | 8. <u> </u> |
| 3. <u>Elec. Ctr. (HP-5385)</u> | 6. <u>Printer (HP-2671G)</u> | 9. <u> </u> |

Contractor: Magnavox
Board Tested: RF Synthesizer
Test Objective: Measure output power at 6F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
2.	<u></u>	<u></u>	<u></u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure spectral content of 6F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
2.	<u></u>	<u></u>	<u></u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

Contractor: Magnavox
Board Tested: RF Synthesizer
Test Objective: Measure output frequency at 2F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
2.	_____	_____	_____
3.	_____	_____	_____
4.	_____	_____	_____
5.	_____	_____	_____
6.	_____	_____	_____
7.	_____	_____	_____
8.	_____	_____	_____
9.	_____	_____	_____
10.	_____	_____	_____
11.	_____	_____	_____
12.	_____	_____	_____

Contractor: Magnavox
Board Tested: RF Synthesizer
Test Objective: Measure output power at 2F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
2.	<u></u>	<u></u>	<u></u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

Outputs

	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>TBD</u>	<u>Power Meter (HP-436A)</u>
2.	<u> </u>	<u> </u>	<u>Power Sensor (HP-8481A)</u>
3.	<u> </u>	<u> </u>	<u>Att. Sw Dr. (HP-11713A)</u>
4.	<u> </u>	<u> </u>	<u>Prog. Att. (HP-8494G)</u>
5.	<u> </u>	<u> </u>	<u> </u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply a 2F signal to the RF Synthesizer's reference
oscillator input. Measure output power at 2F output with Power Meter.

Data Reduction: Send input levels and output of Power Meter to
printer.

Equipment List:

- | | | |
|---------------------------------|-----------------------------------|--------------------------------|
| 1. <u>Computer (HP-9836S)</u> | 4. <u>Power Sensor (HP-8481A)</u> | 7. <u>Printer (HP-2671G)</u> |
| 2. <u>Sig. Gen. (HP-8663A)</u> | 5. <u>Att. Sw Dr. (HP-11713A)</u> | 8. <u> </u> |
| 3. <u>Power Meter (HP-436A)</u> | 6. <u>Prog. Att. (HP-8494G)</u> | 9. <u> </u> |

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Measure spectral content of 2F output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
2.	<u></u>	<u></u>	<u></u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

Outputs

	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>30.69MHz @ TBD</u>	<u>Spectrum Anal. (HP-8566A)</u>
2.	<u> </u>	<u> </u>	<u>Prog. Att. (HP-8494G)</u>
3.	<u> </u>	<u> </u>	<u>Att. Sw Drive (HP-11713A)</u>
4.	<u> </u>	<u> </u>	<u> </u>
5.	<u> </u>	<u> </u>	<u> </u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply a 2F signal to the RF Synthesizer's reference
oscillator input. Measure spectral content at 2F output with Spectrum
Analyzer.

Data Reduction: Send input levels to printer. Output of Spectrum
Analyzer to plotter.

Equipment List:

- | | | |
|-------------------------------------|-----------------------------------|--------------------------------|
| 1. <u>Computer (HP-9836S)</u> | 4. <u>Att. Sw Dr. (HP-11713A)</u> | 7. <u>Printer (HP-2671G)</u> |
| 2. <u>Sig. Gen. (HP-8663A)</u> | 5. <u>Prog. Att. (HP-8494G)</u> | 8. <u> </u> |
| 3. <u>Spectrum Anal. (HP-8566A)</u> | 6. <u>Plotter (HP-7475A)</u> | 9. <u> </u> |

Contractor: Magnavox

Board Tested: RF Synthesizer

Test Objective: Verify 2F -F/154, 120 is operational.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
2.	<u>F/154, F/120</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

Outputs

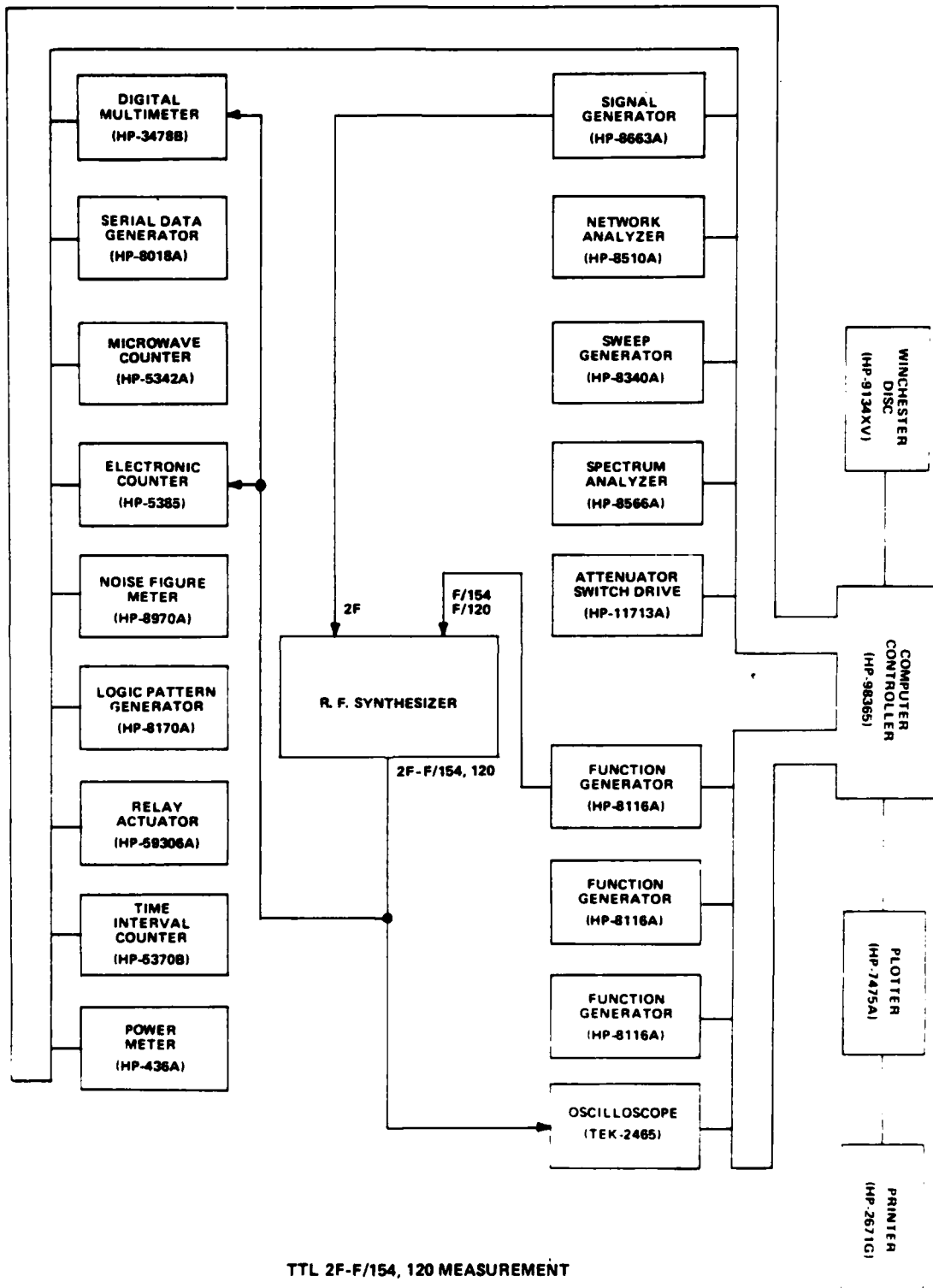
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>2F -F/154, 120</u>	<u>TBD</u>	<u>Digital Multi. (HP-3478B)</u>
2.	<u></u>	<u></u>	<u>Electronic Ctr (HP-5385)</u>
3.	<u></u>	<u></u>	<u>Oscilloscope (TEK-2465)</u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply proper signal levels to all inputs. Measure
output frequency at 2F -F/154, 120 output with Electronic Counter.
Measure signal strength with Digital Multimeter and an Oscilloscope will
be used to display waveform.

Data Reduction: Send input levels and outputs of Electronic Counter
and Digital Multimeter to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|-------------------------------------|------------------------------------|-------------------------------|
| 1. <u>Sig. Gen. (HP-8663A)</u> | 4. <u>Electronic Ctr (HP-5385)</u> | 7. <u>Computer (HP-9836S)</u> |
| 2. <u>Func. Gen. (HP-8116A)</u> | 5. <u>Printer (HP-2671G)</u> | 8. <u>Plotter (HP-7475A)</u> |
| 3. <u>Digital Multi. (HP-3478B)</u> | 6. <u>Oscilloscope (TEK-2465)</u> | 9. <u></u> |



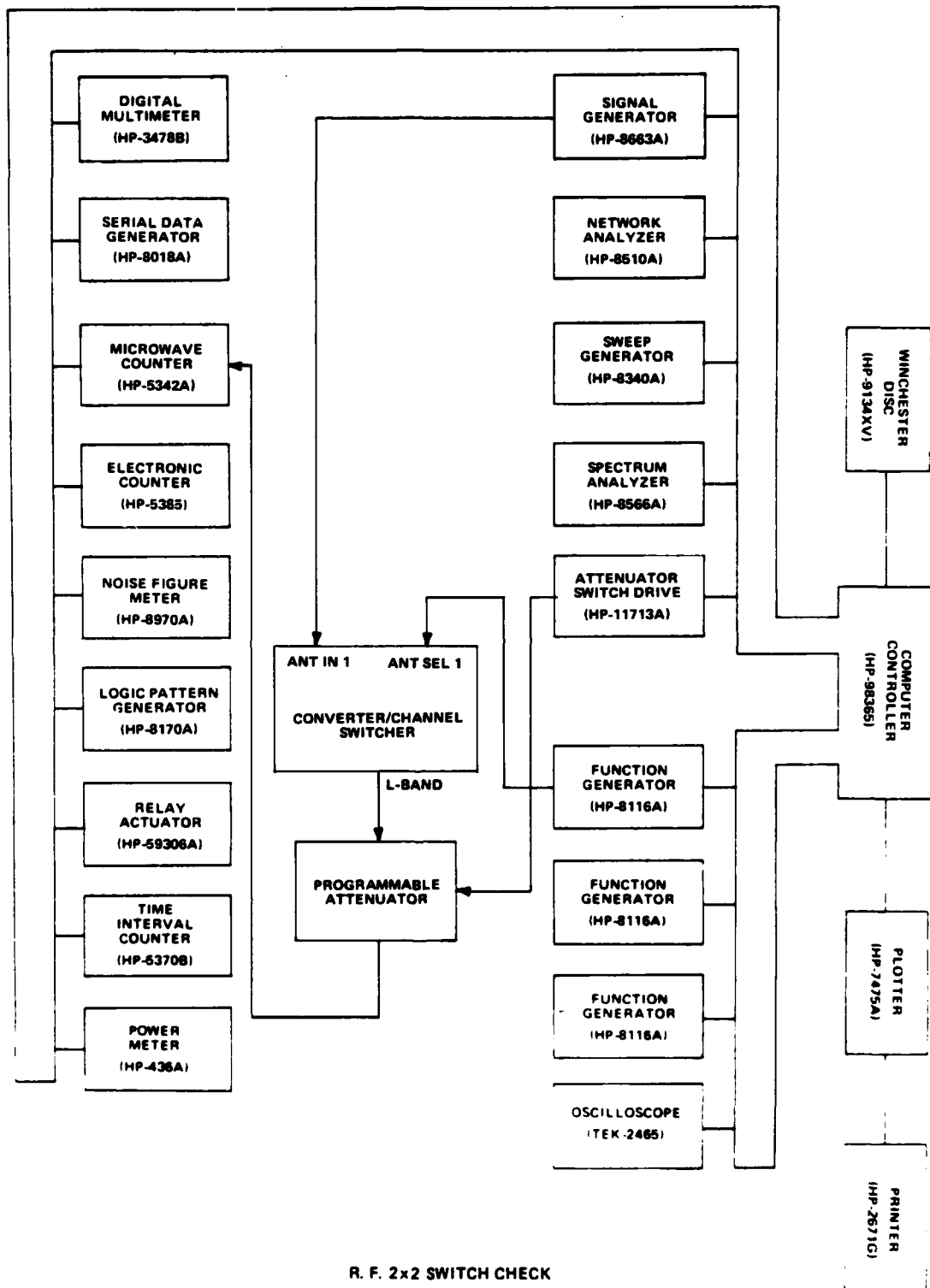
TTL 2F-F/154, 120 MEASUREMENT

3.1.1.2 CONVERTER/CHANNEL SELECTOR MODULE

Contractor: Magnavox
Board Tested: Converter/Channel Switch
Test Objective: Verify RF 2 x 2 switch operation.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Antenna Select 1</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
2.	<u>Antenna IN 1</u>	<u>-120dBm @ L1/L2</u>	<u>Signal Gen. (HP-8663A)</u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>



R. F. 2x2 SWITCH CHECK

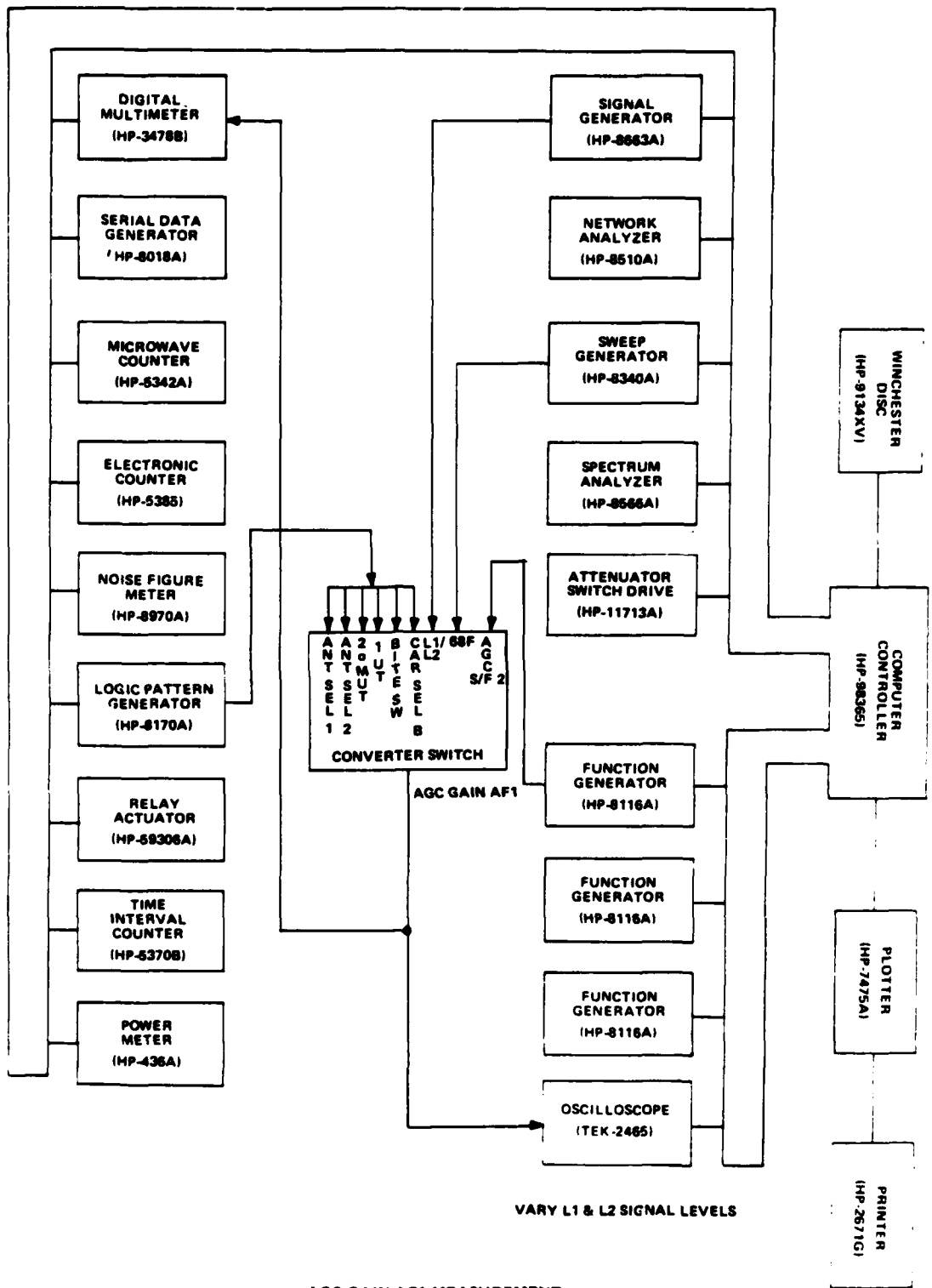
Contractor: Magnavox

Board Tested: Converter/Channel Switch

Test Objective: To verify AGC GAIN AF1 is operational.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Antenna Select 1</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
2.	<u>Antenna Select 2</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
3.	<u>20 MUT</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
4.	<u>1 UT</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
5.	<u>BITE SW. A, B, C</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
6.	<u>Carrier Select B</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
7.	<u>L1/L2</u>	<u>-120dBm @ L1/L2</u>	<u>Signal Generator (HP-8663A)</u>
8.	<u>68F</u>	<u>347.82MHz @ -10dBm</u>	<u>Sweep Generator (HP-8340A)</u>
9.	<u>AGC S/F 2</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>



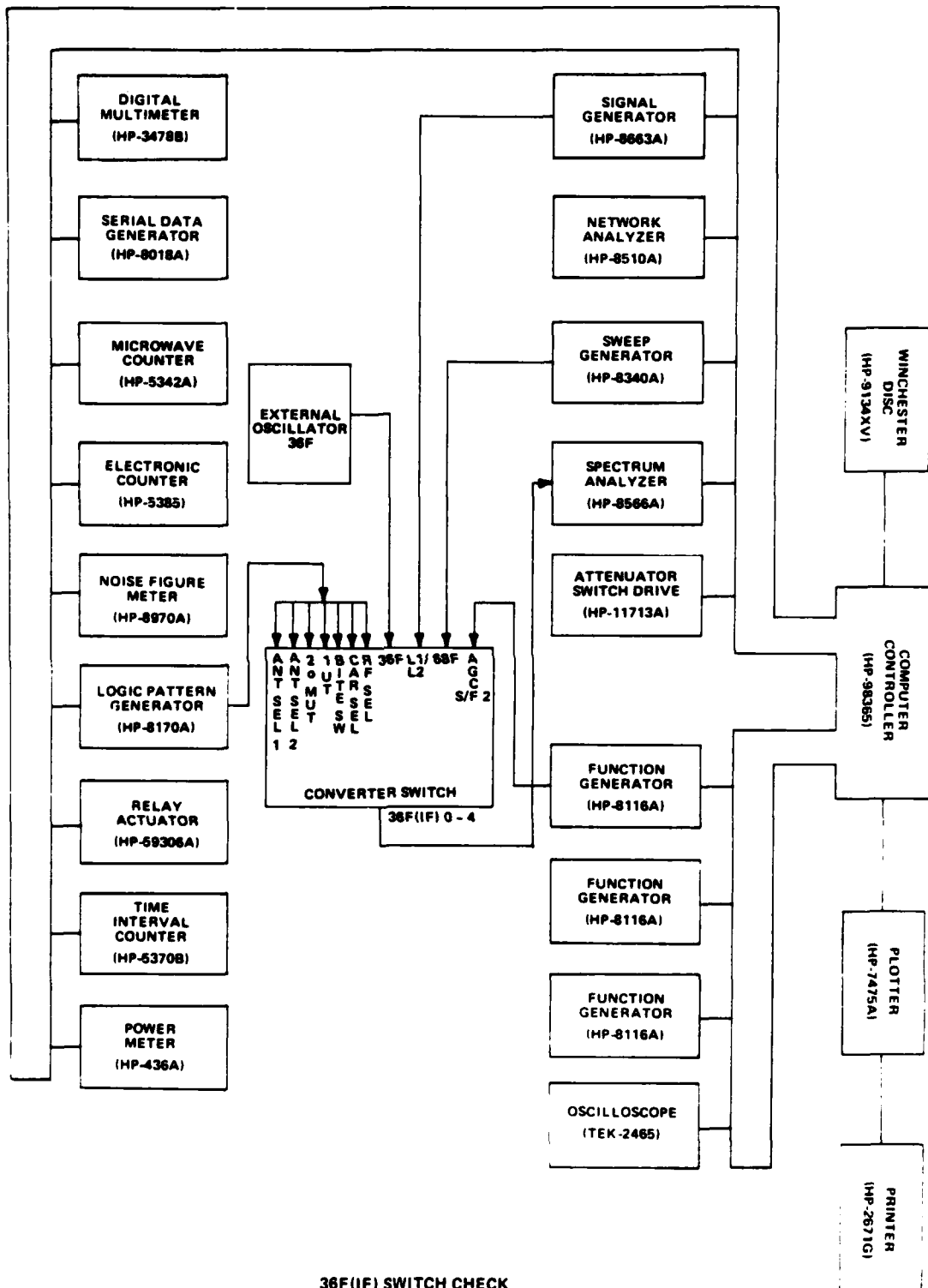
Contractor: Magnavox

Board Tested: Converter/Channel Switch

Test Objective: Verify 36F(IF) switch operation.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Antenna Select 1</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
2.	<u>Antenna Select 2</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
3.	<u>20 MUT</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
4.	<u>1 UT</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
5.	<u>BITE SW A, B, C</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
6.	<u>Carrier Select B</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
7.	<u>RF Select Ø</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
8.	<u>L1/L2</u>	<u>-120dBm @ L1/L2</u>	<u>Signal Generator (HP-8663A)</u>
9.	<u>68F</u>	<u>-10dBm @ 347.82MHz</u>	<u>Sweep Generator (HP-8340A)</u>
10.	<u>36F</u>	<u>-24dBm @ 184.14MHz</u>	<u>External Oscillator</u>
11.	<u>AGC S/F 2</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
12.	<u></u>	<u></u>	<u></u>



Contractor: Magnavox

Board Tested: Converter/Channel Switch

Test Objective: Verify operation of Carrier Select outputs.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>RF Select 0</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
2.	<u>RF Select 1</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
3.	<u>RF Select 2</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
4.	<u>RF Select 3</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
5.	<u>RF Select 4</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
6.	<u>Carrier Select A</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
7.	<u>Carrier Select B</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

Outputs

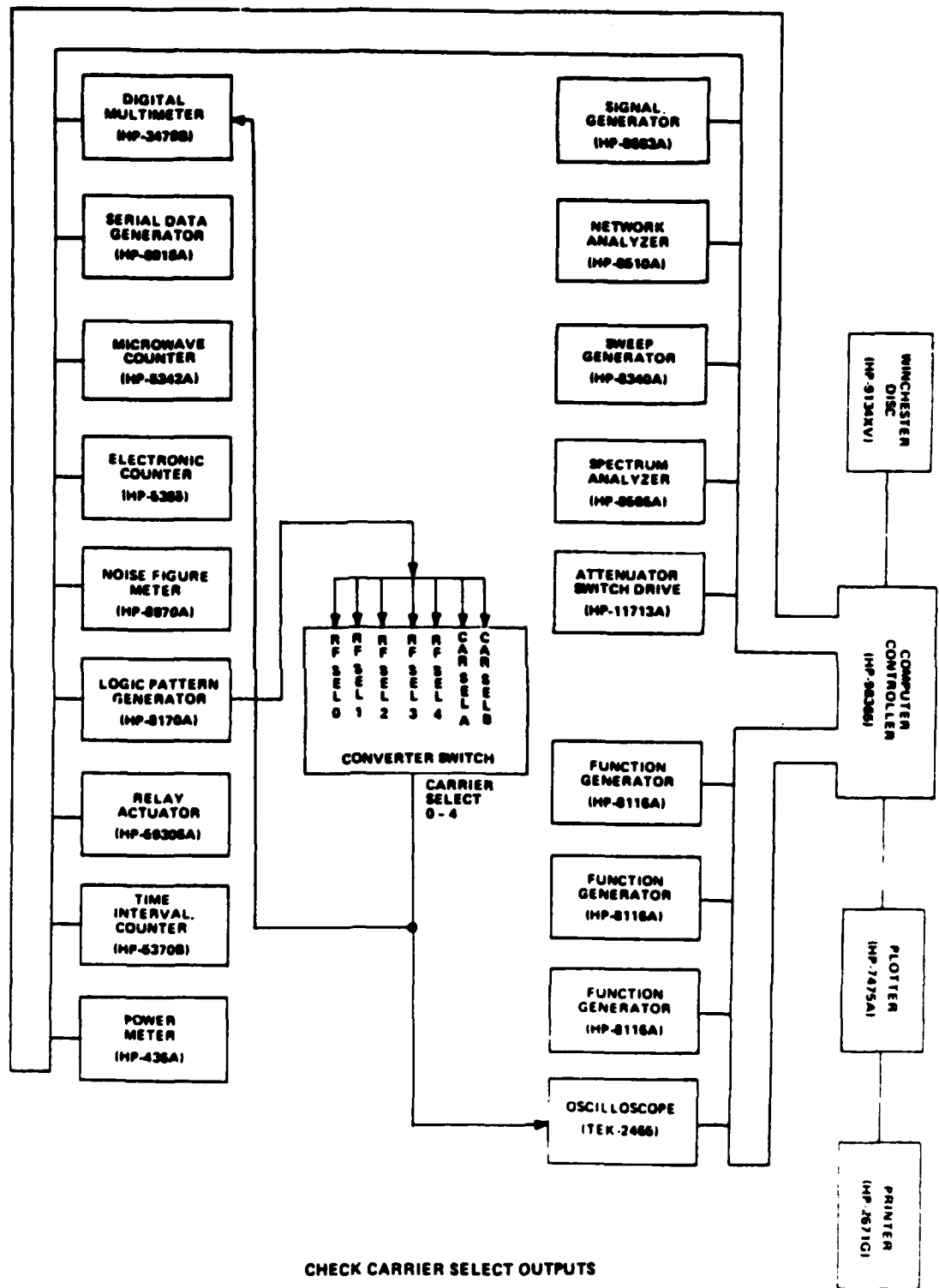
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>Carrier Select 0-4</u>	<u>TBD</u>	<u>Digital Multi. (HP-3478B)</u>
2.	<u></u>	<u></u>	<u>Oscilloscope (TEK-2465)</u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply proper signal levels to inputs. Measure Carrier
Select output with Digital Multimeter. An Oscilloscope will be used to
display waveform. (Repeat for all Carrier Select outputs)

Data Reduction: Send input levels and output of Digital Multimeter to
printer. Output of Spectrum Analyzer to plotter.

Equipment List:

- | | | | | | |
|----|------------------------------------|----|--------------------------------|----|---------|
| 1. | <u>Logic Patt. Gen. (HP-8170A)</u> | 4. | <u>Computer (HP-9836S)</u> | 7. | <u></u> |
| 2. | <u>Digital Multi. (HP-3478B)</u> | 5. | <u>Oscilloscope (TEK-2465)</u> | 8. | <u></u> |
| 3. | <u>Printer (HP-2671G)</u> | 6. | <u>Plotter (HP-7475A)</u> | 9. | <u></u> |



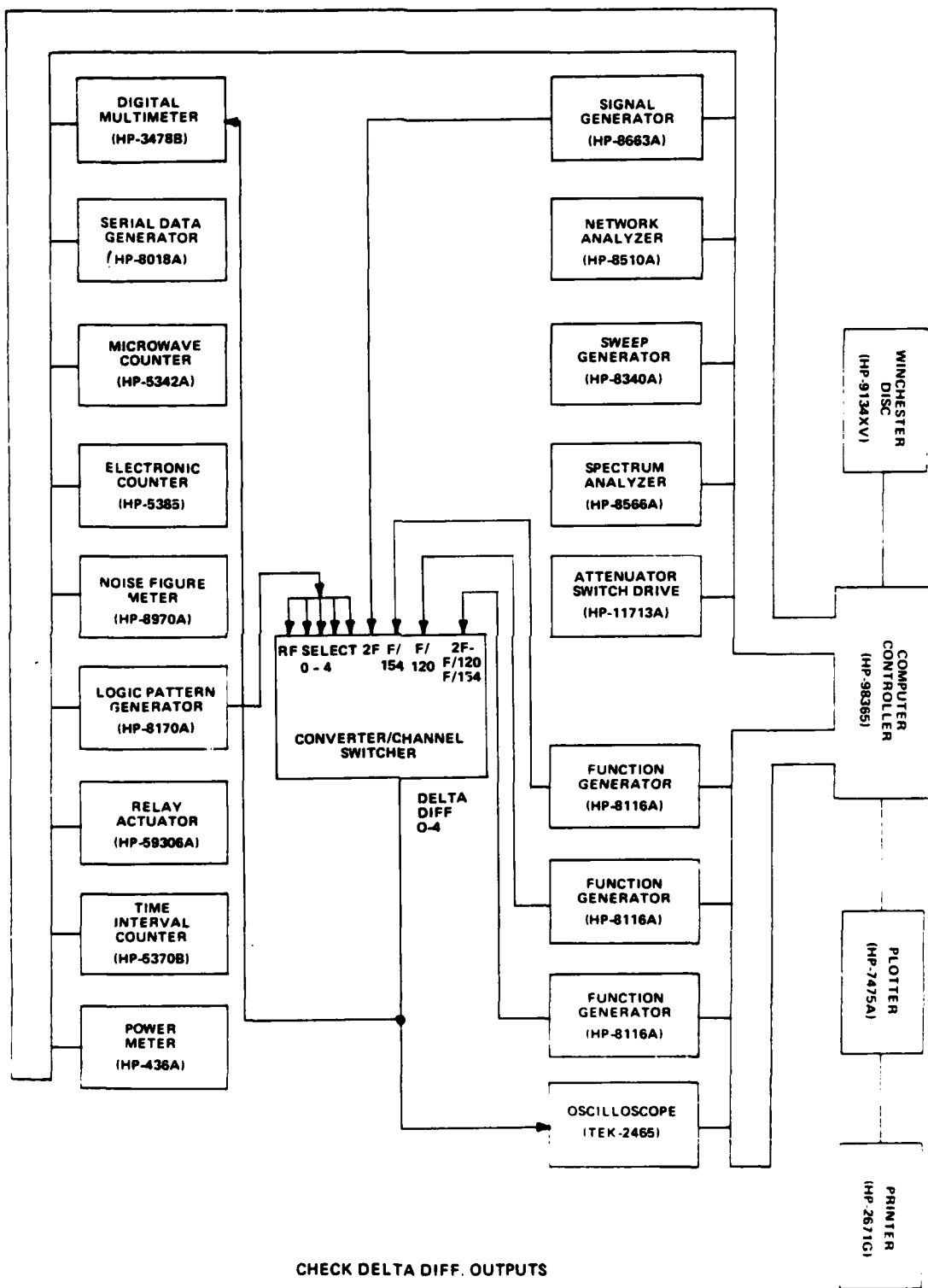
Contractor: Magnavox

Board Tested: Converter/Channel Switch

Test Objective: Verify Delta Diff outputs operational.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>RF Select 0</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
2.	<u>RF Select 1</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
3.	<u>RF Select 2</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
4.	<u>RF Select 3</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
5.	<u>RF Select 4</u>	<u>TBD</u>	<u>Logic Patt. Gen. (HP-8170A)</u>
6.	<u>2F</u>	<u>10.23MHz @ TBD</u>	<u>Signal Generator (HP-8663A)</u>
7.	<u>F/120 UT</u>	<u>42625kHz @ TBD</u>	<u>Func. Generator (HP-8116A)</u>
8.	<u>F/154 UT</u>	<u>33214.3kHz @ TBD</u>	<u>Func. Generator (HP-8116A)</u>
9.	<u>2F - F/120, 154</u>	<u>10.23MHz - 42625kHz/ 33214.3kHz @ TBD</u>	<u>Func. Generator (HP-8116A)</u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>



CHECK DELTA DIFF. OUTPUTS

3.1.1.3 IF/BB/CODER MODULE

Contractor: Magnavox

Board Tested: IF/BB/Coder

Test Objective: Verify presence and proper operation of SLEW
DNE Signal.

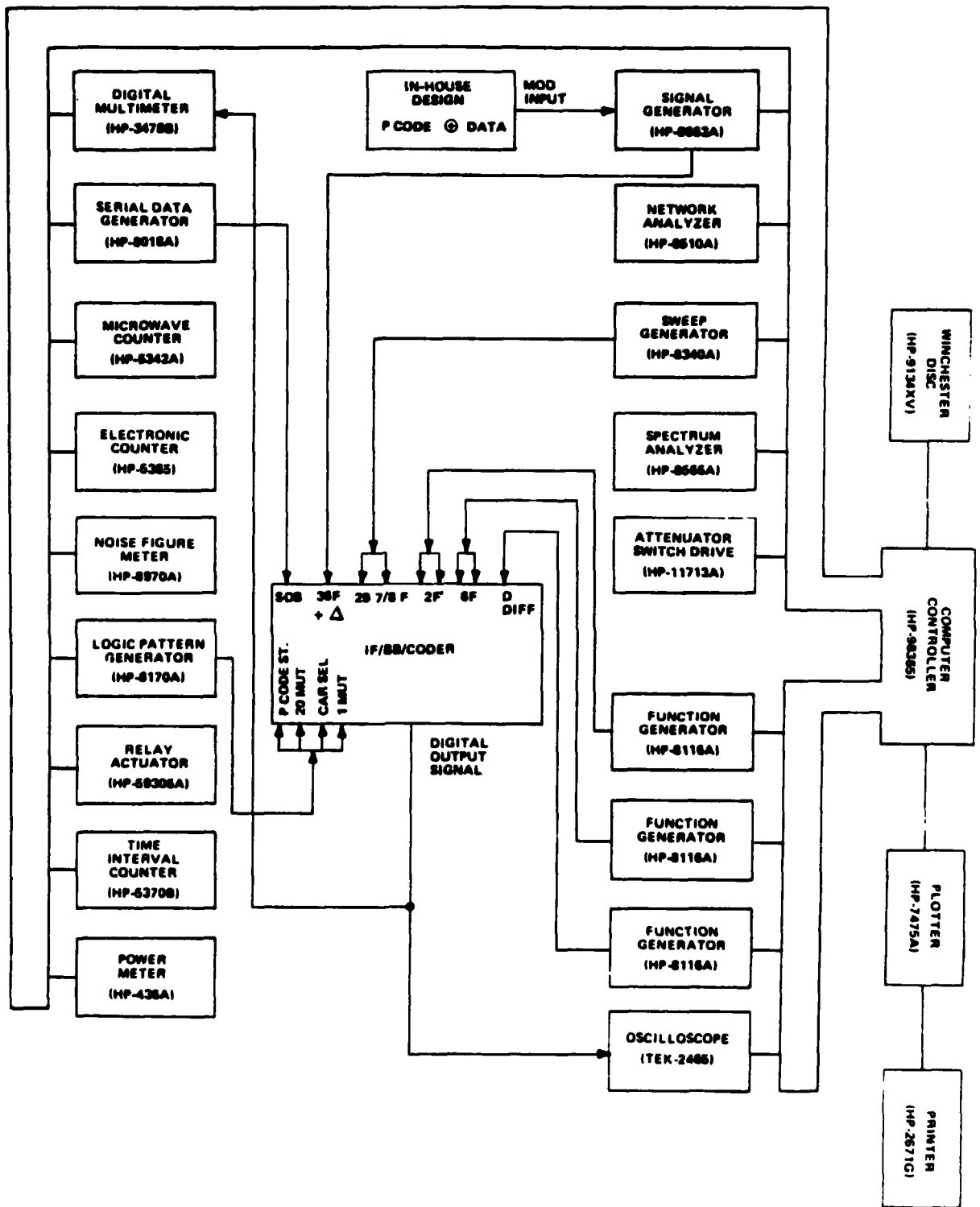
Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
2.	<u>* 36F + Δ</u>	<u>-24dBm</u>	<u>Signal Gen. (HP-8663A)</u>
3.	<u>** 29⁷/8F (2 inputs)</u>	<u>-10dBm</u>	<u>Sweep Gen. (HP-8340A)</u>
4.	<u>** 2F (2 inputs)</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
5.	<u>** 6F (2 inputs)</u>	<u>-30dBm</u>	<u>Function Gen. (HP-8116A)</u>
6.	<u>Serial Data Bus</u>	<u>TBD</u>	<u>Serial Data Gen. (HP-8018A)</u>
7.	<u>X P Code St</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
8.	<u>X 20 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
9.	<u>X CARR SEL</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
10.	<u>X 1 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
11.	<u>_____</u>	<u>_____</u>	<u>_____</u>
12.	<u>_____</u>	<u>_____</u>	<u>_____</u>

* Signal Generator output must be modulated by an in house designed code and data generator.

** Power splitters needed for these six inputs.

X Breakout board needed to provide various levels for theses inputs.



Contractor: Magnavox

Board Tested: IF/BB/Coder

Test Objective: Verify presence and proper level of Course Range
Epoch (CRE) output signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
2.	<u>* 36F + Δ</u>	<u>-24dBm</u>	<u>Signal Gen. (HP-8663A)</u>
3.	<u>** 29⁷/8F (2 inputs)</u>	<u>-10dBm</u>	<u>Sweep Gen. (HP-8340A)</u>
4.	<u>** 2F (2 inputs)</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
5.	<u>** 6F (2 inputs)</u>	<u>-30dBm</u>	<u>Function Gen. (HP-8116A)</u>
6.	<u>Serial Data Bus</u>	<u>TBD</u>	<u>Serial Data Gen. (HP-8018A)</u>
7.	<u>X P Code St</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
8.	<u>X 20 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
9.	<u>CARR SEL</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
10.	<u>1 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

* Signal Generator output must be modulated by an in house designed Code and data generator.

** Power splitters needed for these six inputs.

X Breakout board needed to provide various levels for these inputs.

Contractor: Magnavox

Board Tested: IF/BB/Coder

Test Objective: Verify presence and proper level of Fine Range Epoch (FRE) output signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
2.	<u>* 36F + Δ</u>	<u>-24dBm</u>	<u>Signal Gen. (HP-8663A)</u>
3.	<u>** 29⁷/8F (2 inputs)</u>	<u>-10dBm</u>	<u>Sweep Gen. (HP-8340A)</u>
4.	<u>** 2F (2 inputs)</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
5.	<u>** 6F (2 inputs)</u>	<u>-30dBm</u>	<u>Function Gen. (HP-8116A)</u>
6.	<u>Serial Data Bus</u>	<u>TBD</u>	<u>Serial Data Gen. (HP-8018A)</u>
7.	<u>X P Code St</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
8.	<u>X 20 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
9.	<u>X CARR SEL</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
10.	<u>X 1 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
11.	<u>_____</u>	<u>_____</u>	<u>_____</u>
12.	<u>_____</u>	<u>_____</u>	<u>_____</u>

* Signal Generator output must be modulated by an in house designed Code and data generator.

** Power splitters needed for these six inputs.

X Breakout board needed to provide various levels for these inputs.

Outputs

	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>FRE</u>	<u>TBD</u>	<u>DMM (HP-3478B)</u>
2.	<u></u>	<u></u>	<u>Scope (TEK-2465)</u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply proper signal levels to all inputs of IF/BB/
Coder module. Measure output of FRE with DMM. Observe signal presence
and level on scope.

Data Reduction: Send input levels and output of Digital Multimeter
to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|----------------------------------|-------------------------------------|------------------------------------|
| 1. <u>Func. Gen. (HP-8116A)</u> | 4. <u>Ser. Data Gen. (HP-8018A)</u> | 7. <u>Oscilloscope (TEK-2465)</u> |
| 2. <u>Signal Gen. (HP-8663A)</u> | 5. <u>Log Patt. Gen. (HP-8170A)</u> | 8. <u>Special In house</u> |
| 3. <u>Sweep Gen. (HP-8340A)</u> | 6. <u>DMM (HP-3478B)</u> | 9. <u>designed equipment (See</u> |
| 10. <u>Computer (HP-9836S)</u> | 11. <u>Printer (HP-2671G)</u> | 12. <u>notes on previous page)</u> |
| | | <u>Plotter (HP-7475A)</u> |

Contractor: Magnavox

Board Tested: IF/BB/Coder

Test Objective: Verify presence and proper operation of AGC
SF/1 signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
2.	<u>* 36F + Δ</u>	<u>-24dBm</u>	<u>Signal Gen. (HP-8663A)</u>
3.	<u>** 29⁷/8F (2 inputs)</u>	<u>-10dBm</u>	<u>Sweep Gen. (HP-8340A)</u>
4.	<u>** 2F (2 inputs)</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
5.	<u>** 6F (2 inputs)</u>	<u>-30dBm</u>	<u>Function Gen. (HP-8116A)</u>
6.	<u>Serial Data Bus</u>	<u>TBD</u>	<u>Serial Data bus (HP-8018A)</u>
7.	<u>X P Code St</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
8.	<u>X 20 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
9.	<u>X CARR SEL</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
10.	<u>X 1 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
11.	<u>_____</u>	<u>_____</u>	<u>_____</u>
12.	<u>_____</u>	<u>_____</u>	<u>_____</u>

* Signal Generator output must be modulated by an in house designed Code and data generator.

** Power splitters needed for these six inputs.

X Breakout board needed to provide various levels for these inputs.

Outputs

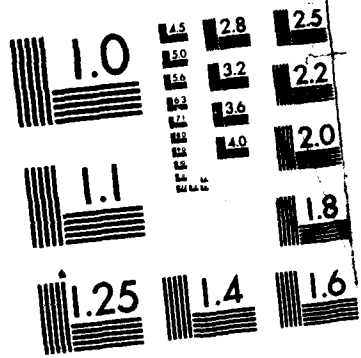
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>AGC SF/1</u>	<u>TBD</u>	<u>DMM (HP-3478B)</u>
2.	<u></u>	<u></u>	<u>Scope (TEK-2465)</u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply proper signal levels to all inputs of IF/BB/
Coder module. Measure output level of AGC SF/1 signal with DMM. Observe
signal presence and level on scope.

Data Reduction: Send input levels and output of Digital Multimeter
to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|---------------------------------|-------------------------------------|-----------------------------------|
| 1. <u>Func. Gen. (HP-8116A)</u> | 4. <u>Ser Data Gen. (HP-8018A)</u> | 7. <u>Oscilloscope (TEK-2465)</u> |
| 2. <u>Sig. Gen. (HP-8663A)</u> | 5. <u>Log Patt. Gen. (HP-8170A)</u> | 8. <u>Special In house</u> |
| 3. <u>Sweep Gen. (HP-8340A)</u> | 6. <u>DMM (HP-3478B)</u> | 9. <u>notes on previous page)</u> |
| 10. <u>Computer (HP-9836S)</u> | 11. <u>Printer (HP-2671G)</u> | 12. <u>Plotter (HP-7475A)</u> |



MICROCOPY RESOLUTION TEST CHART
 NATIONAL BUREAU OF STANDARDS-1963-A

Contractor: Magnavox

Board Tested: IF/BB/Coder

Test Objective: Verify presence and proper operation of RF
SEL signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	<u>TBD</u>	<u>Func. Gen. (HP-8116A)</u>
2.	<u>* 36F + Δ</u>	<u>-24dBm</u>	<u>Signal Gen. (HP-8663A)</u>
3.	<u>** 29⁷/8F (2 inputs)</u>	<u>-10dBm</u>	<u>Sweep Gen. (HP-8340A)</u>
4.	<u>** 2F (2 inputs)</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
5.	<u>** 6F (2 inputs)</u>	<u>-30dBm</u>	<u>Function Gen. (HP-8116A)</u>
6.	<u>Serial Data Bus</u>	<u>TBD</u>	<u>Ser. Data Gen. (HP-8018A)</u>
7.	<u>X P Code St</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
8.	<u>X 20 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
9.	<u>X CARR SEL</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
10.	<u>X 1 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
11.	<u>_____</u>	<u>_____</u>	<u>_____</u>
12.	<u>_____</u>	<u>_____</u>	<u>_____</u>

* Signal Generator output must be modulated by an in house designed Code and data generator.

** Power splitters needed for these six inputs.

X Breakout board needed to provide various levels for these inputs.

Outputs

	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>RF SEL</u>	<u>TBD</u>	<u>DMM (HP-3478B)</u>
2.	<u> </u>	<u> </u>	<u>Scope (TEK-2465)</u>
3.	<u> </u>	<u> </u>	<u> </u>
4.	<u> </u>	<u> </u>	<u> </u>
5.	<u> </u>	<u> </u>	<u> </u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply proper signal levels to all inputs of IF/BB/
Coder module. Measure output level of RF SEL with DMM. Observe signal
presence and level on scope.

Data Reduction: Send input levels and output of Digital Multimeter
to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|---------------------------------|--------------------------------------|------------------------------------|
| 1. <u>Func. Gen. (HP-8116A)</u> | 4. <u>Ser Data Gen. (HP-8018A)</u> | 7. <u>Oscilloscope (TEK-2465)</u> |
| 2. <u>Sig. Gen. (HP-8663A)</u> | 5. <u>Log Patt. Gen. (HP-8170f.)</u> | 8. <u>Special In house</u> |
| 3. <u>Sweep Gen. (HP-8340A)</u> | 6. <u>DMM (HP-3478B)</u> | 9. <u>designed equipment (See</u> |
| 10. <u>Computer (HP-9836S)</u> | 11. <u>Printer (HP-2671G)</u> | 12. <u>notes on previous page)</u> |
| | | <u>Plotter (HP-7475A)</u> |

Contractor: Magnavox

Board Tested: IF/BB/Coder

Test Objective: Verify presence and proper operation of Noise
On/Off Signal.

<u>Inputs</u>		
<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1. <u>Δ Difference</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
2. <u>* 36F + Δ</u>	<u>-24dBm</u>	<u>Signal Gen. (HP-8663A)</u>
3. <u>** 29⁷/8F (2 inputs)</u>	<u>-10dBm</u>	<u>Sweep Gen. (HP-8340A)</u>
4. <u>** 2F (2 inputs)</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
5. <u>** 6F (2 inputs)</u>	<u>-30dBm</u>	<u>Function Gen. (HP-8116A)</u>
6. <u>Serial Data Bus</u>	<u>TBD</u>	<u>Ser. Data Gen. (HP-8116A)</u>
7. <u>X P Code St</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
8. <u>X 20 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
9. <u>X CARR SEL</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
10. <u>X 1 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
11. <u>_____</u>	<u>_____</u>	<u>_____</u>
12. <u>_____</u>	<u>_____</u>	<u>_____</u>

* Signal Generator output must be modulated and spread by in house designed P or C/A Code and data generator.

** Power splitters needed for these 6 inputs.

X Some sort of breakout board needed to provide different levels for these inputs.

Outputs

	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>Noise On/Off</u>	<u>TBD</u>	<u>DMM (HP-3478B)</u>
2.	<u></u>	<u></u>	<u>Scope (TEK-2465)</u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply proper signal levels to all inputs of IF/BB/Coder
module. Measure output level of Noise On/Off signal with DMM. Observe
signal presence and level on scope.

Data Reduction: Send input levels and output of Digital Multimeter
to printer. Output of Oscilloscope to plotter.

Equipment List:

- | | | |
|----------------------------------|-------------------------------------|-----------------------------------|
| 1. <u>Func. Gen. (HP-8116A)</u> | 4. <u>Ser. Data Gen. (HP-8018A)</u> | 7. <u>Oscilloscope (TEK-2465)</u> |
| 2. <u>Signal Gen. (HP-8663A)</u> | 5. <u>Log Patt. Gen. (HP-8170A)</u> | 8. <u>Special In house</u> |
| 3. <u>Sweep Gen. (HP-8340A)</u> | 6. <u>DMM (HP-3478B)</u> | 9. <u>designed equipment (See</u> |
| 10. <u>Computer (HP-9836S)</u> | 11. <u>Printer (HP-2671G)</u> | 12. <u>first page notes)</u> |
| | | <u>Flotter (HP-7475A)</u> |

Contractor: Magnavox
Board Tested: IF/BB/Coder
Test Objective: Verify presence and proper level of ANT SEL signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
2.	<u>* 36F + Δ</u>	<u>-24dBm</u>	<u>Signal Gen. (HP-8663A)</u>
3.	<u>** 29⁷/8F (2 inputs)</u>	<u>-10dBm</u>	<u>Sweep Gen. (HP-8340A)</u>
4.	<u>** 2F (2 inputs)</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
5.	<u>** 6F (2 inputs)</u>	<u>-30dBm</u>	<u>Function Gen. (HP-8116A)</u>
6.	<u>Serial Data Bus</u>	<u>TBD</u>	<u>Serial Data Gen. (HP-8018A)</u>
7.	<u>X P Code St</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
8.	<u>X 20 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
9.	<u>X CARR SEL</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
10.	<u>X 1 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
11.	<u>_____</u>	<u>_____</u>	<u>_____</u>
12.	<u>_____</u>	<u>_____</u>	<u>_____</u>

* Signal Generator output must be modulated by an in house designed Code and data generator.

** Power splitters needed for these six inputs.

X Breakout board needed to provide various levels for these inputs.

Contractor: Magnavox

Board Tested: IF/BB/Coder

Test Objective: Verify presence and proper level of Bite SW output signal.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Δ Difference</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
2.	<u>* 36F + Δ</u>	<u>-24dBm</u>	<u>Signal Gen. (HP-8663A)</u>
3.	<u>** 29⁷/8F (2 inputs)</u>	<u>-10dBm</u>	<u>Sweep Gen. (HP-8340A)</u>
4.	<u>** 2F (2 inputs)</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
5.	<u>** 6F (2 inputs)</u>	<u>-30dBm</u>	<u>Function Gen. (HP-8116A)</u>
6.	<u>Serial Data Bus</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8018A)</u>
7.	<u>X P Code St</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
8.	<u>X 20 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
9.	<u>X CARR SEL</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
10.	<u>X 1 MUT</u>	<u>TBD</u>	<u>Log Patt. Gen. (HP-8170A)</u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

* Signal Generator output must be modulated by an in house designed Code and data generator.

** Power splitters needed for these six inputs.

X Breakout board needed to provide various levels for these inputs.

3.1.1.4 PRE AMPLIFIER UNIT MODULE

Contractor: Magnavox

Board Tested: Pre Amp Unit

Test Objective: Measure PAU noise figure

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>Noise Input</u>	<u>TBD</u>	<u>Noise Source (HP-346B)</u>
2.	_____	_____	_____
3.	_____	_____	_____
4.	_____	_____	_____
5.	_____	_____	_____
6.	_____	_____	_____
7.	_____	_____	_____
8.	_____	_____	_____
9.	_____	_____	_____
10.	_____	_____	_____
11.	_____	_____	_____
12.	_____	_____	_____

Outputs

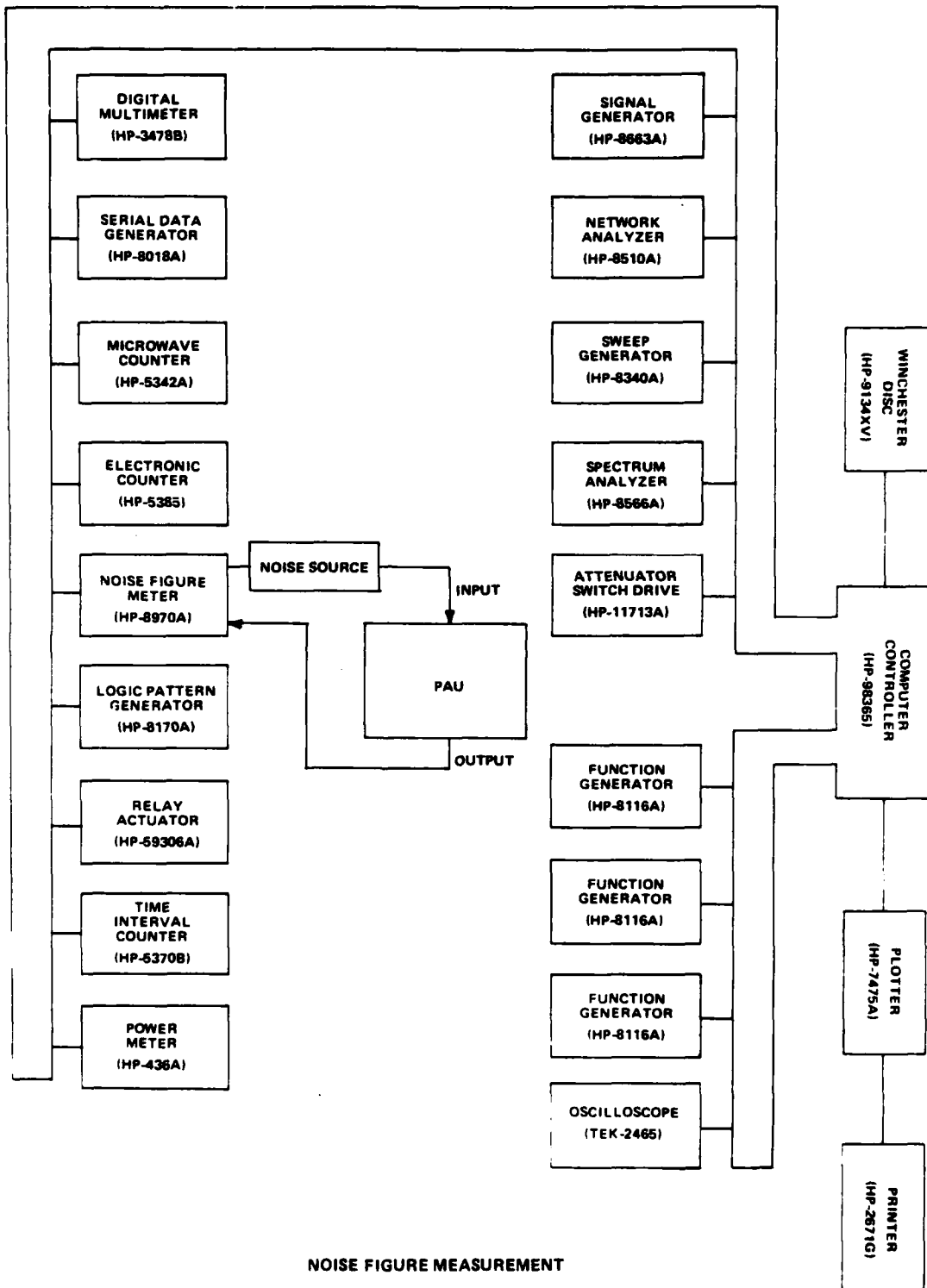
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	NF	5dB Max	Noise Meter (HP-8970A)
2.	_____	_____	_____
3.	_____	_____	_____
4.	_____	_____	_____
5.	_____	_____	_____

Test Procedure: Hook up unit under test as shown in diagram. Turn
on test equipment. Set measurement equipment to proper ranges. Apply
proper input level to PAU. Measure NF with meter.

Data Reduction: Record Noise Figure on printer.

Equipment List:

- | | | |
|----------------------------------|------------------------------|----------|
| 1. <u>Noise Source (HP-346B)</u> | 4. <u>Printer (HP-2671G)</u> | 7. _____ |
| 2. <u>NF Meter (HP-8970A)</u> | 5. _____ | 8. _____ |
| 3. <u>Computer (HP-9836S)</u> | 6. _____ | 9. _____ |



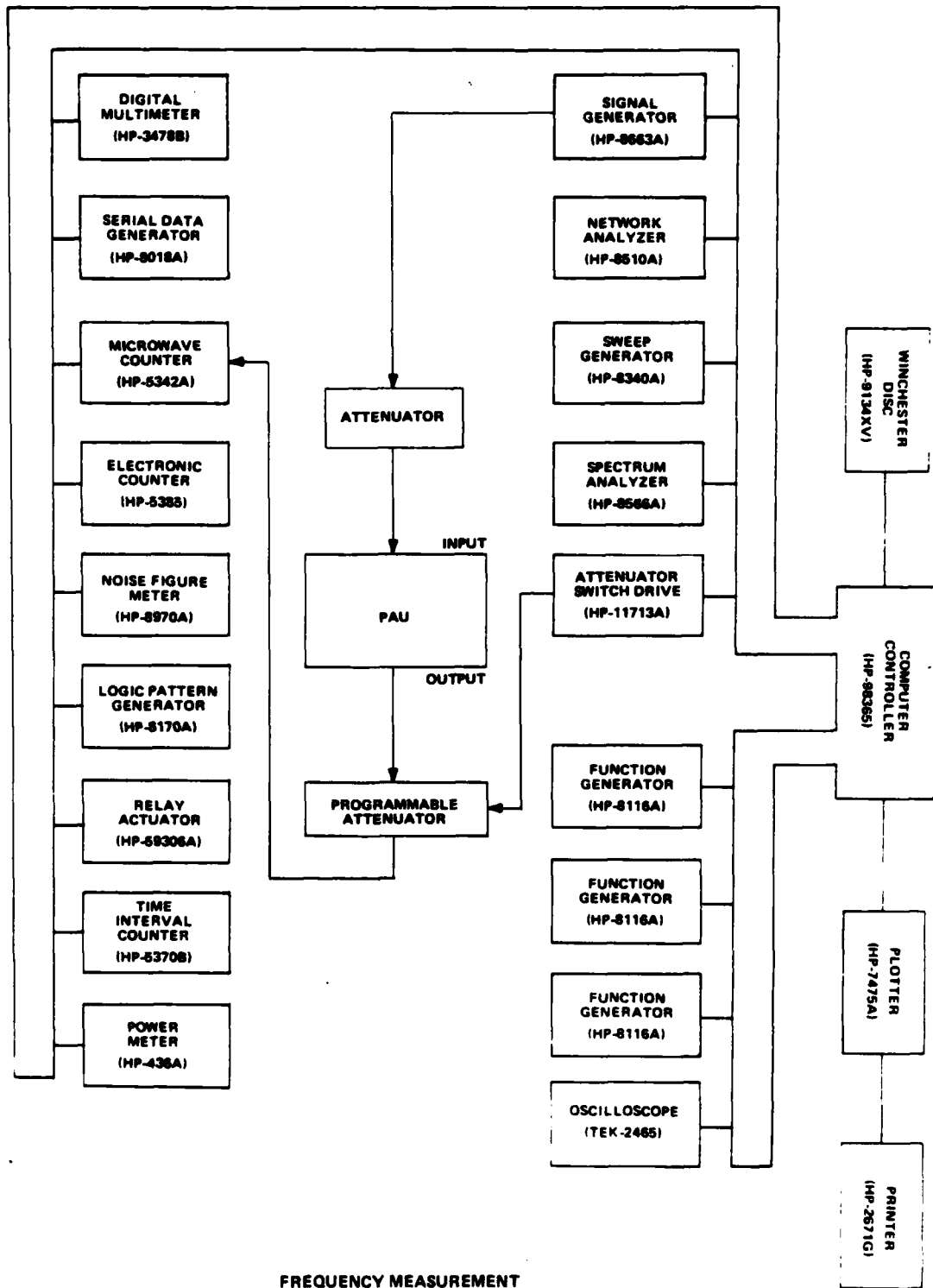
Contractor: Magnavox

Board Tested: PAU

Test Objective: Verify proper frequency output from Preamp unit

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>L1/L2</u>	<u>1227.60/1575.42MHz</u>	<u>Signal Generator (HP-8663A)</u>
2.	<u></u>	<u></u>	<u>Attenuator</u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>



Contractor: Magnavox

Board Tested: Pre-Amp Unit

Test Objective: Verify proper power output and gain for PAU

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>L1/L2</u>	<u>-163dbW</u>	<u>Signal Generator (HP-8663A)</u>
2.	_____	_____	<u>Attenuator</u>
3.	_____	_____	_____
4.	_____	_____	_____
5.	_____	_____	_____
6.	_____	_____	_____
7.	_____	_____	_____
8.	_____	_____	_____
9.	_____	_____	_____
10.	_____	_____	_____
11.	_____	_____	_____
12.	_____	_____	_____

Outputs

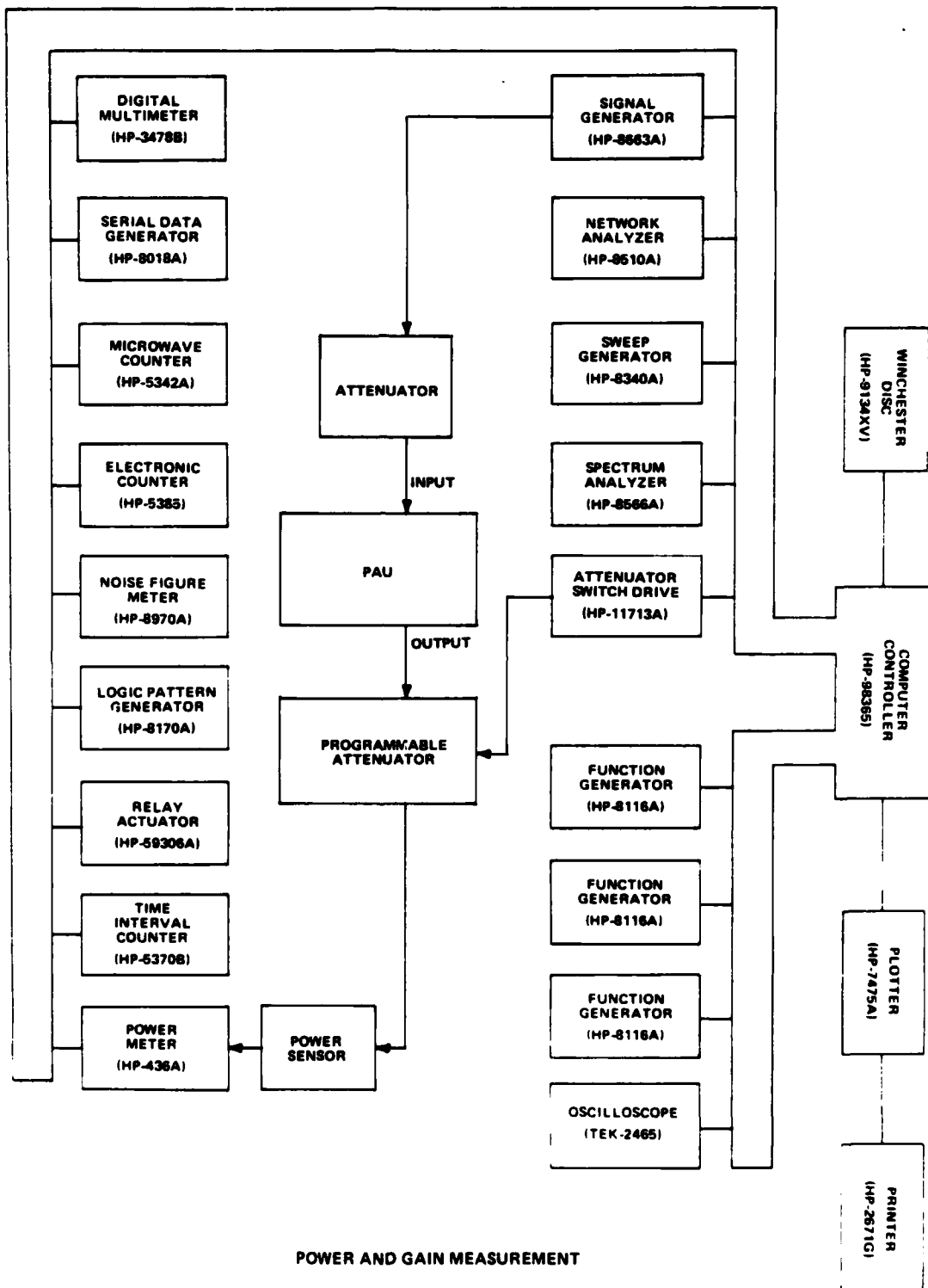
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>L1/L2</u>	<u>-120dbw</u>	<u>Power Meter (HP-436A)</u>
2.	<u>GAIN</u>	<u>43db</u>	<u>Power Sensor (HP-8481A)</u>
3.	<u> </u>	<u> </u>	<u>Prog Atten (HP-8494G)</u>
4.	<u> </u>	<u> </u>	<u>Att Sw Drive (HP-11713A)</u>
5.	<u> </u>	<u> </u>	<u> </u>

Test Procedure: Hook up unit under test as shown in diagram. Turn
on test equipment. Set measurement equipment to proper ranges. Apply
proper input level to PAU. Measure PAU output power with Power Meter.

Data Reduction: Record input levels and output power measurement on
printer. GAIN can be computed.

Equipment List:

- | | | |
|-------------------------------|-----------------------------------|--|
| 1. <u>Sig Gen (HP-8663A)</u> | 4. <u>Printer (HP-2671G)</u> | 7. <u>Prog. Atten. (HP-8494G)</u> |
| 2. <u>Attenuator</u> | 5. <u>Power Meter (HP-436A)</u> | 8. <u>Att. Sw Drive (HP-11713A)</u> |
| 3. <u>Computer (HP-9836S)</u> | 6. <u>Power Sensor (HP-8481A)</u> | 9. <u> </u> |



Contractor: Magnavox

Board Tested: Pre-Amp Unit

Test Objective: Measure input/output VSWR

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>S-Par Test Set</u>	<u>(1565.42 to 1585.42MHz) @ TED</u>	<u>Sweep Gen. (HP-8340A)</u>
2.	<u>S-Par Test Set</u>	<u>(1217.6 to 1237.6MHz) @ TED</u>	<u>Sweep Gen. (HP-8340A)</u>
3.	<u>S-Par Test Set</u>	<u>(1565.42 to 1585.42MHz) @ TED</u>	<u>Sweep Gen. (HP-8340A)</u>
4.	<u>S-Par Test Set</u>	<u>(1217.6 to 1237.5MHz) @ TED</u>	<u>Sweep Gen. (HP-8340A)</u>
5.	_____	_____	_____
6.	_____	_____	_____
7.	_____	_____	_____
8.	_____	_____	_____
9.	_____	_____	_____
10.	_____	_____	_____
11.	_____	_____	_____
12.	_____	_____	_____

Outputs

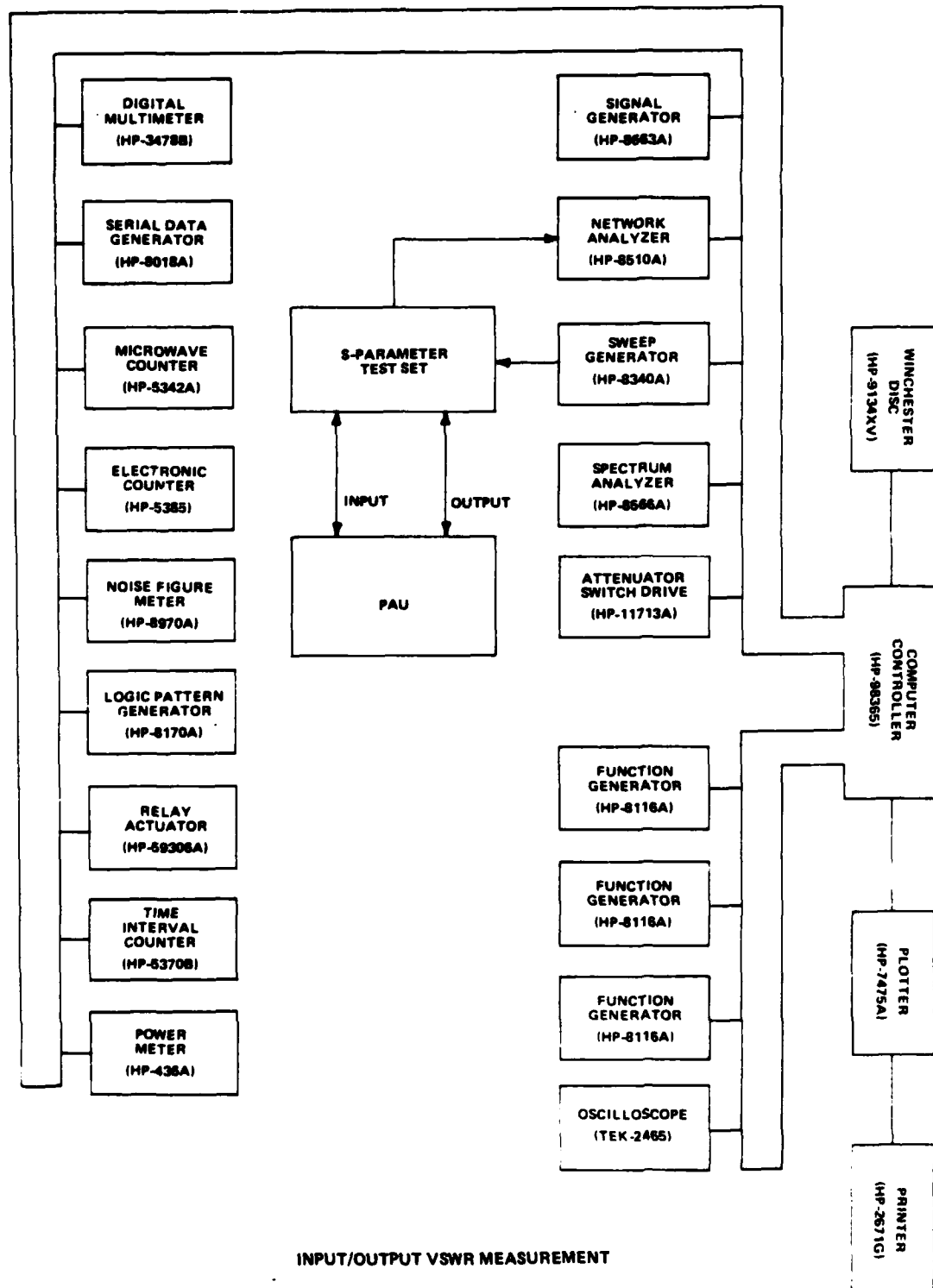
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>L1</u>	<u>1.5:1 (max)</u>	<u>S-Par Test Set (HP-8515A)</u>
2.	<u>L2</u>	<u>1.5:1 (max)</u>	<u>Network Anal. (HP-8510A)</u>
3.	<u>RF Out</u>	<u>1.5:1 (max)</u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Connect the input of the PAU to one port of the
S-parameter test set and the output of the PAU to the other test port.
Set Sweep Generator to sweep through L1 and measure S11 (input reflec-
tion coefficient) and L1 S22 (output reflection coefficient). Set Sweep
Generator to sweep through L2 and repeat measurements.

Data Reduction: Output data from Network Analyzer to plotter and
send input levels to printer.

Equipment List:

- | | | | | | |
|----|------------------------------|----|----------------------------------|----|---------|
| 1. | <u>Computer (HP-9836S)</u> | 4. | <u>S-Par Test Set (HP-8515A)</u> | 7. | <u></u> |
| 2. | <u>Printer (HP-2671G)</u> | 5. | <u>Network Anal. (HP-8510A)</u> | 8. | <u></u> |
| 3. | <u>Sweep Gen. (HP-8340A)</u> | 6. | <u>Plotter (HP-7475A)</u> | 9. | <u></u> |



Contractor: Magnavox

Board Tested: Pre-Amp Unit

Test Objective: Check spectral content, and selectivity
(3db and 60dB Bandwidth) of PAU output

<u>Inputs</u>		
<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1. <u>L1/L2</u>	<u>1227.6/1575.42MHz</u>	<u>Sig. Gen. (HP-8663A)</u>
2. _____	<u>@ -163dbw</u>	<u>Attenuator</u>
3. _____	_____	_____
4. _____	_____	_____
5. _____	_____	_____
6. _____	_____	_____
7. _____	_____	_____
8. _____	_____	_____
9. _____	_____	_____
10. _____	_____	_____
11. _____	_____	_____
12. _____	_____	_____

Outputs

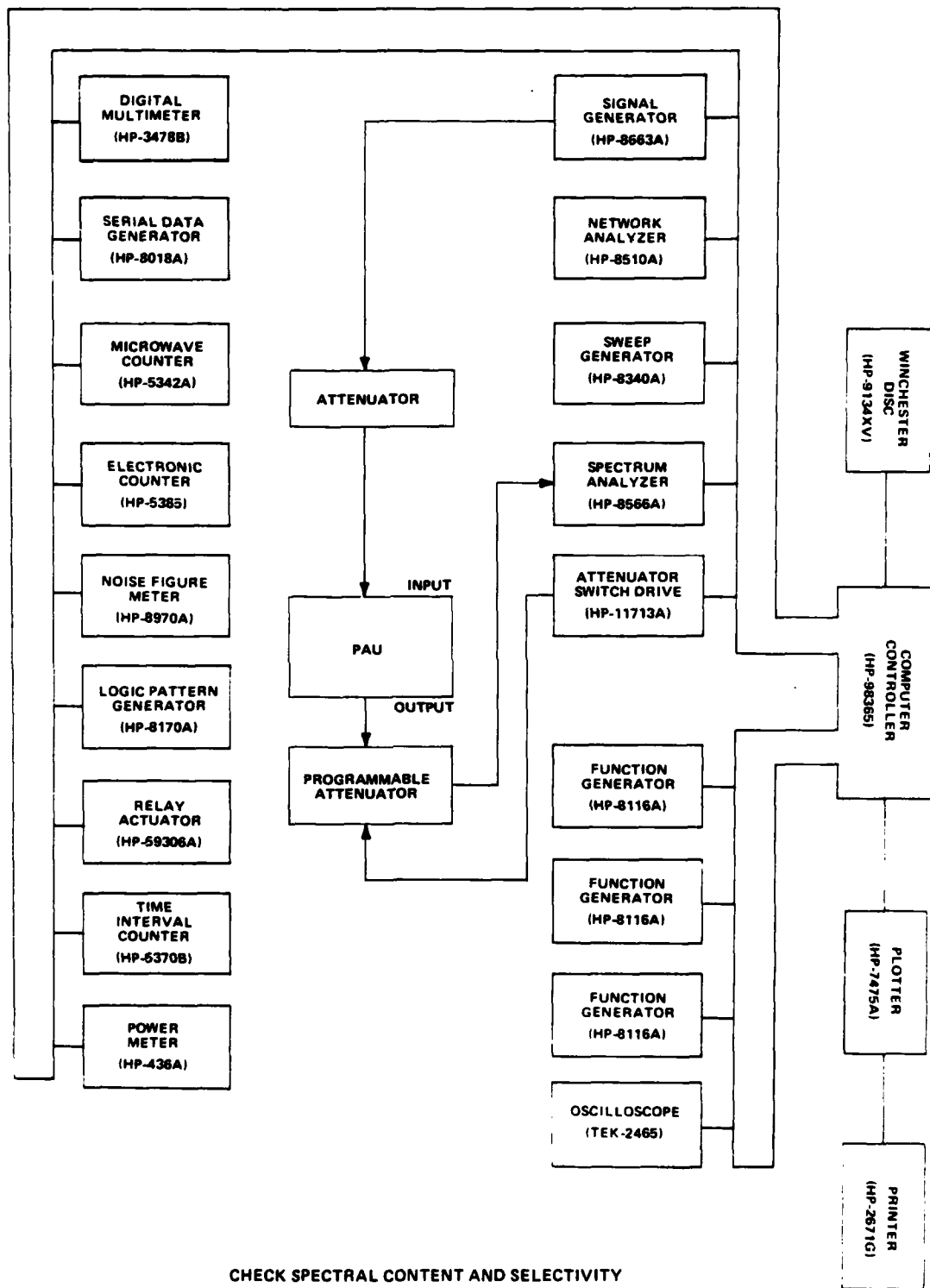
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>L1/L2</u>	<u>1227.6/1575.42 @ -120dB</u>	<u>Spec Analyzer (HP-8566A)</u>
2.	<u>3dB BW</u>	<u>14MHz</u>	<u>Prog Atten (HP-8494G)</u>
3.	<u>60dB BW</u>	<u>200MHz</u>	<u>Att Sw Drive (HP-11713A)</u>
4.	<u> </u>	<u> </u>	<u> </u>
5.	<u> </u>	<u> </u>	<u> </u>

Test Procedure: Hook up unit under test as shown in diagram. Turn
on test equipment. Set measurement equipment to proper ranges. Apply
proper input level to PAU. Measure output quantities using Spectrum
Analyzer.

Data Reduction: Record input levels with printer. Output Spectrum
Analyzer data to plotter.

Equipment List:

- | | | |
|-------------------------------|------------------------------------|--------------------------------|
| 1. <u>Sig Gen (HP-8663A)</u> | 4. <u>Spec Anal (HP-8566A)</u> | 7. <u>Printer (HP-2671G)</u> |
| 2. <u>Attenuator</u> | 5. <u>Prog Atten (HP-8494G)</u> | 8. <u>Plotter (HP-7475A)</u> |
| 3. <u>Computer (HP-9836S)</u> | 6. <u>Att SW Drive (HP-11713A)</u> | 9. <u> </u> |



Contractor: Magnavox
Board Tested: Pre-Amp Unit
Test Objective: Verify burnout protection level

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>In Band Signal</u>	<u>up to +30dbm</u>	<u>Sig Gen. (HP-8663A)</u>
2.	_____	_____	<u>Amplifier</u>
3.	_____	_____	<u>Prog. Atten. (HP-8494G)</u>
4.	_____	_____	<u>Att Sw Drive (HP-11713A)</u>
5.	_____	_____	<u>Power Sensor (HP-8481A)</u>
6.	_____	_____	<u>Power Meter (HP-436A)</u>
7.	_____	_____	_____
8.	_____	_____	_____
9.	_____	_____	_____
10.	_____	_____	_____
11.	_____	_____	_____
12.	_____	_____	_____

Outputs

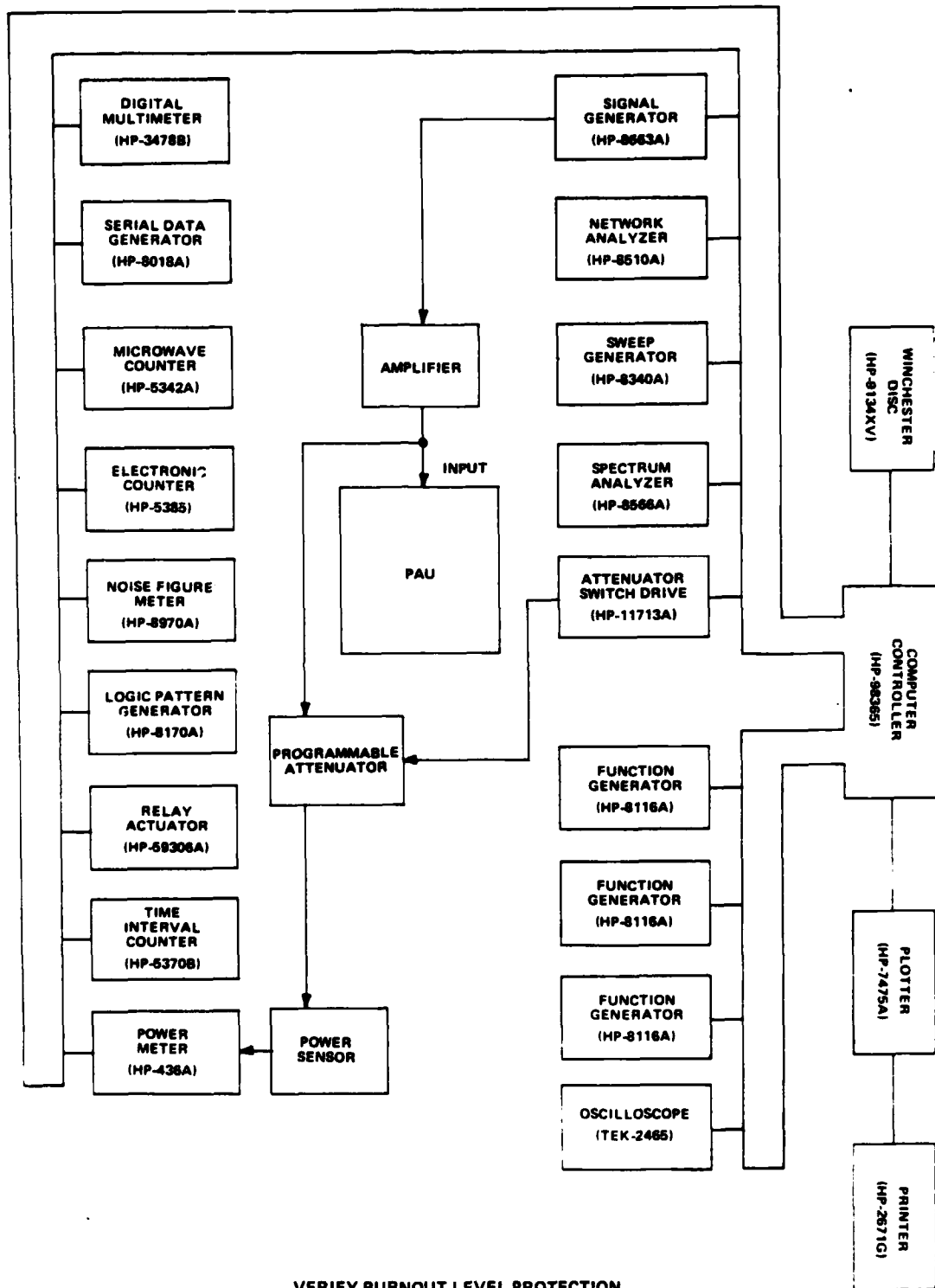
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	_____	_____	_____
2.	_____	_____	_____
3.	_____	_____	_____
4.	_____	_____	_____
5.	_____	_____	_____

Test Procedure: Hook up unit under test as shown in diagram. Turn
on test equipment. Set measurement equipment to proper ranges. Apply
input to PAU increasing to max of +30dbm. Repeat Gain and Noise Figure
Tests to insure no permanent damage to PAU.

Data Reduction: Record input levels with printer.

Equipment List:

- | | | |
|-------------------------------|-----------------------------------|-------------------------------------|
| 1. <u>Sig Gen (HP-8663A)</u> | 4. <u>Power Meter (HP-436A)</u> | 7. <u>Program Atten. (HP-8494G)</u> |
| 2. <u>Computer (HP-9836S)</u> | 5. <u>Power Sensor (HP-8481A)</u> | 8. <u>Amplifier</u> |
| 3. <u>Printer (HP-2671G)</u> | 6. <u>Att. Sw Dr. (HP-11713A)</u> | 9. _____ |



VERIFY BURNOUT LEVEL PROTECTION

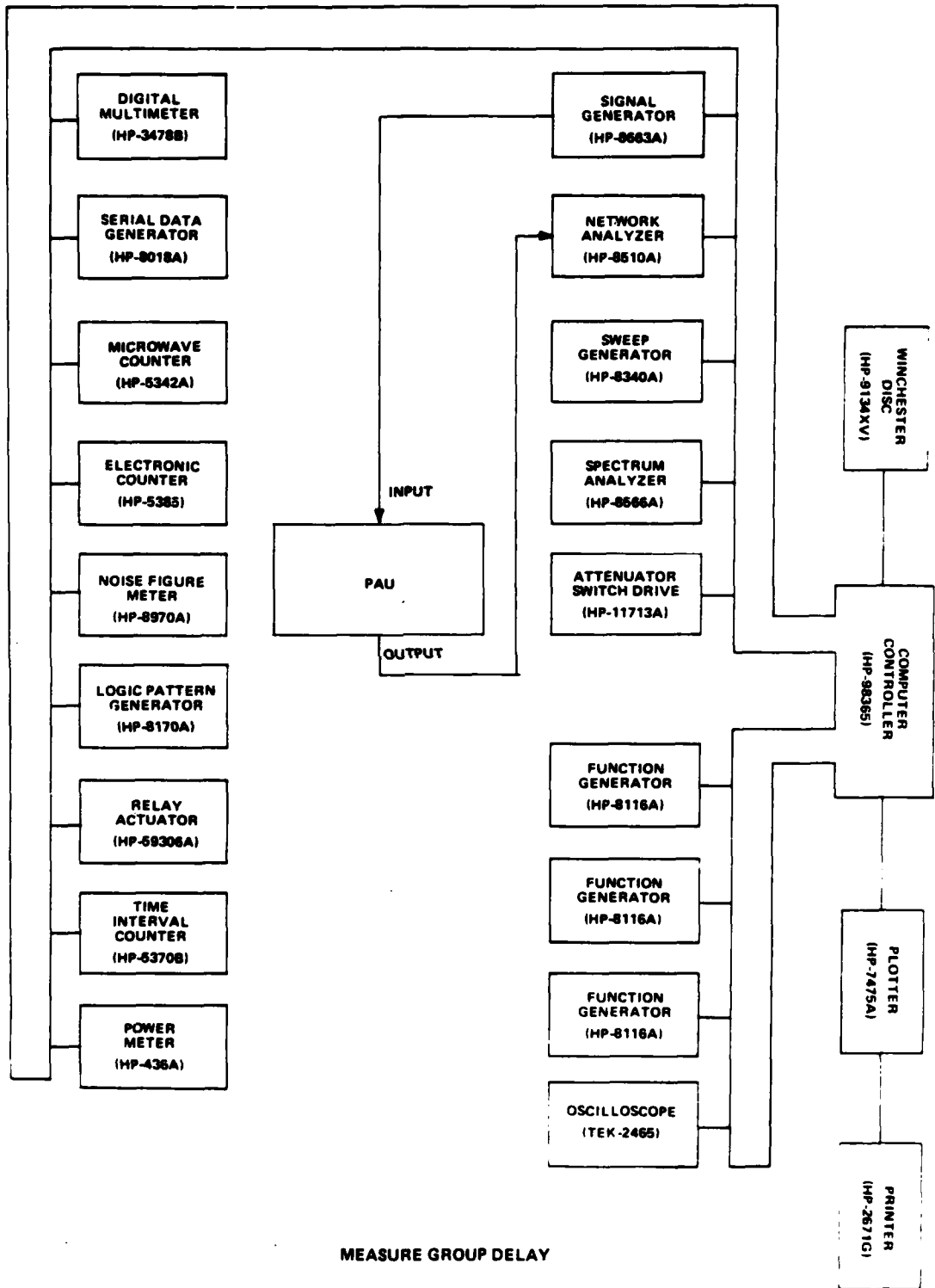
Contractor: Magnavox

Board Tested: Pre-Amp Unit

Test Objective: Measure group delay

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>L1</u>	<u>1575.42MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
2.	<u>L2</u>	<u>1227.6MHz @ TBD</u>	<u>Signal Gen. (HP-8663A)</u>
3.	_____	_____	_____
4.	_____	_____	_____
5.	_____	_____	_____
6.	_____	_____	_____
7.	_____	_____	_____
8.	_____	_____	_____
9.	_____	_____	_____
10.	_____	_____	_____
11.	_____	_____	_____
12.	_____	_____	_____



3.1.1.5 REFERENCE OSCILLATOR MODULE

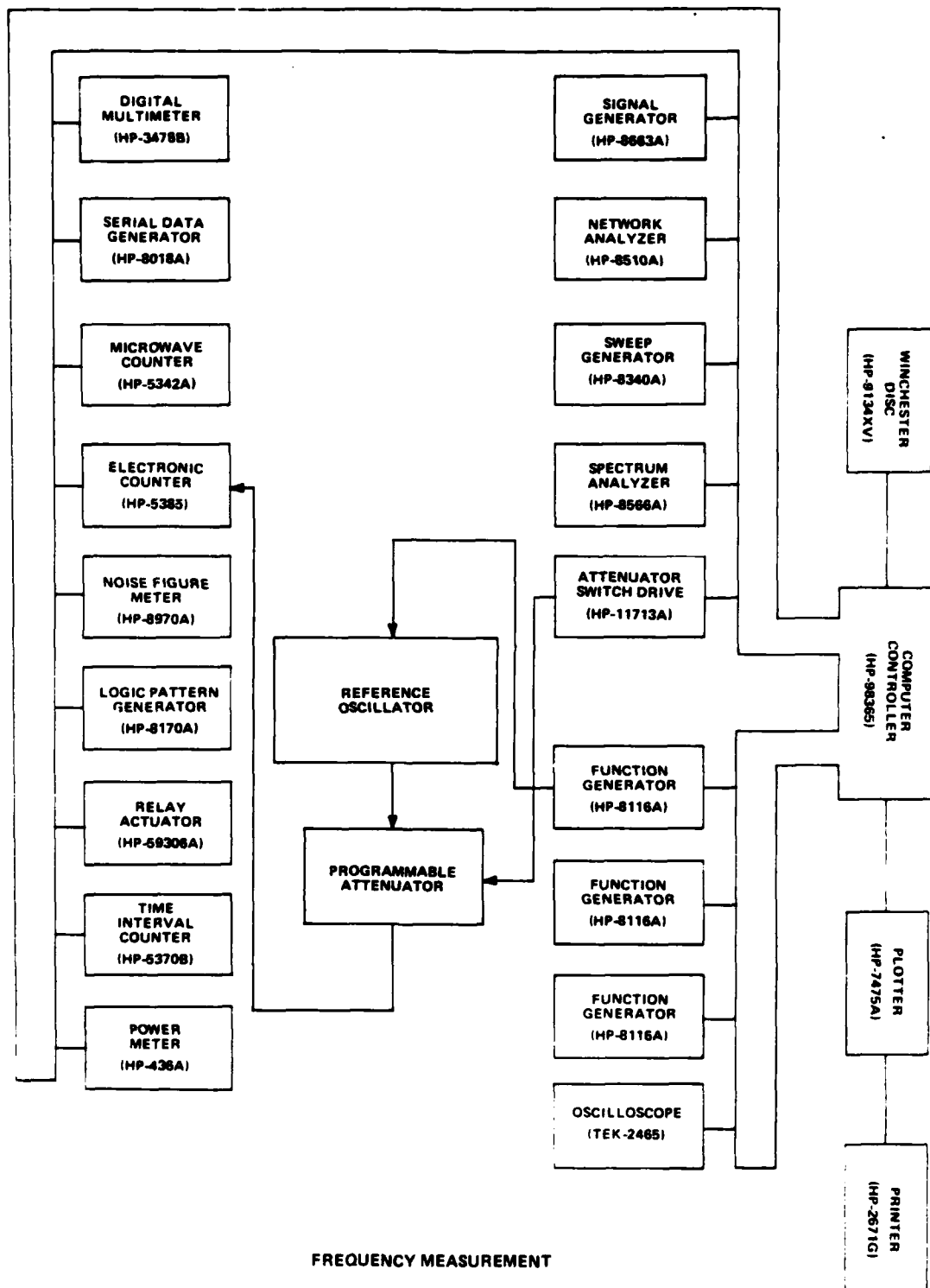
Contractor: Magnavox

Board Tested: Reference Oscillator

Test Objective: Verify proper frequency output.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>DC Voltage</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
2.	_____	_____	_____
3.	_____	_____	_____
4.	_____	_____	_____
5.	_____	_____	_____
6.	_____	_____	_____
7.	_____	_____	_____
8.	_____	_____	_____
9.	_____	_____	_____
10.	_____	_____	_____
11.	_____	_____	_____
12.	_____	_____	_____



FREQUENCY MEASUREMENT

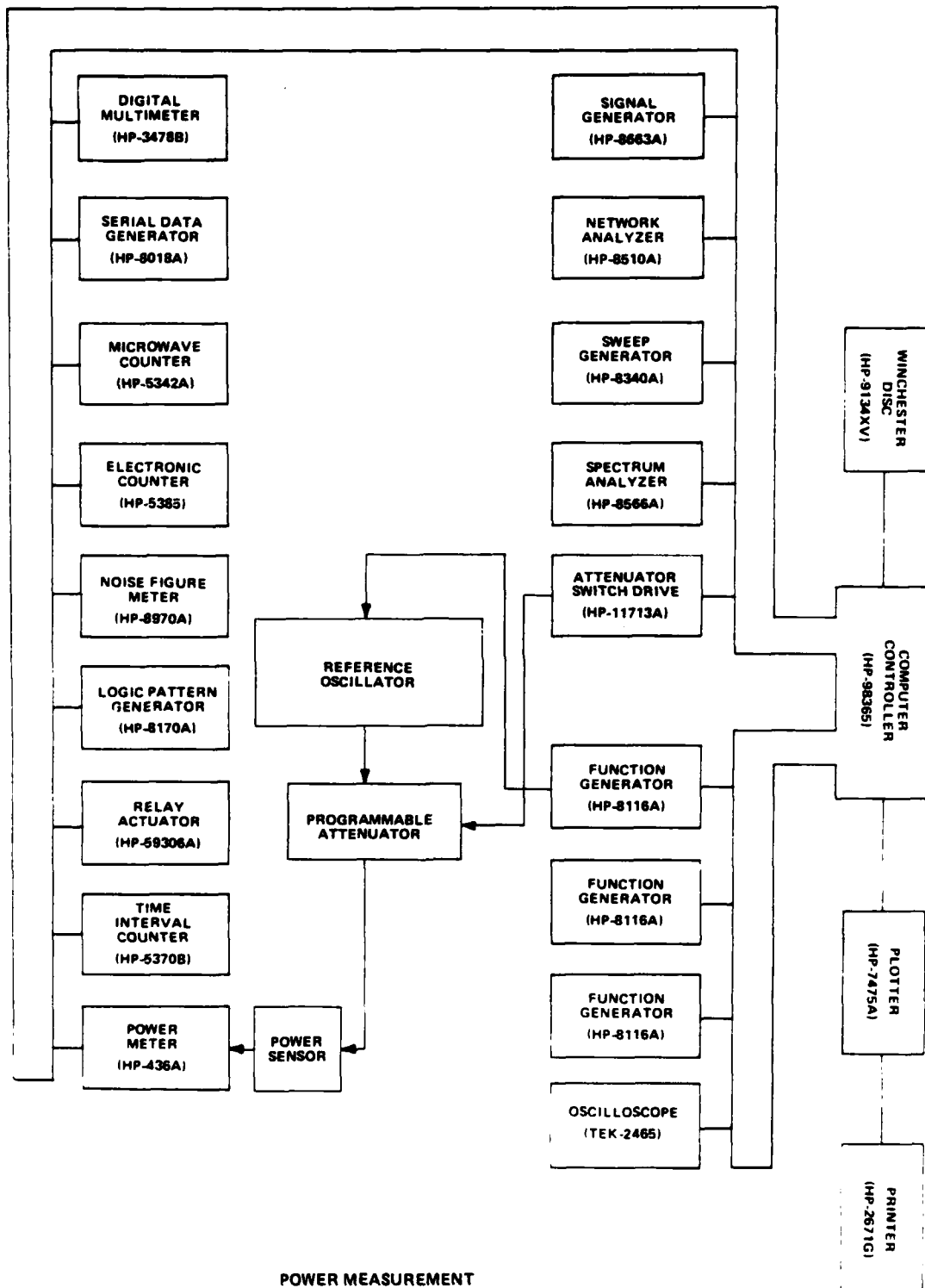
Contractor: Magnavox

Board Tested: Reference Oscillator

Test Objective: Verify proper oscillator output power level.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>DC Voltage</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
2.	<u></u>	<u></u>	<u></u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>



POWER MEASUREMENT

Contractor: Magnavox

Board Tested: Reference Oscillator

Test Objective: Check oscillator output for spectral content.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>DC Voltage</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
2.	<u></u>	<u></u>	<u></u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

Outputs

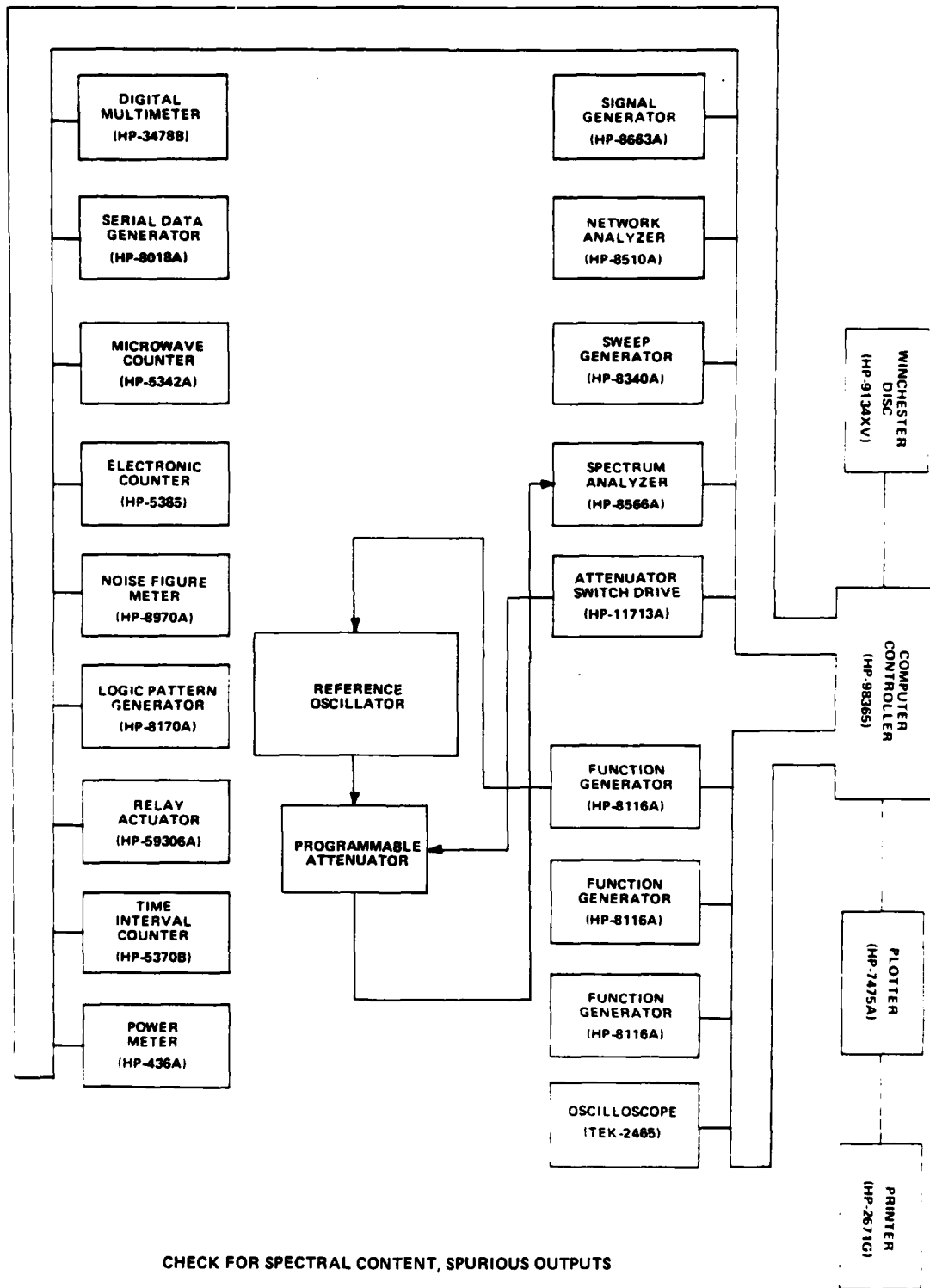
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>Oscillator output</u>	<u>+7dBm at 10.23MHz</u>	<u>Spect. Analyzer (HP-8566A)</u>
2. _____	_____	<u>Atten. Sw Dr. (HP-11713A)</u>
3. _____	_____	<u>Prog. Atten. (HP-8494G)</u>
4. _____	_____	_____
5. _____	_____	_____

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply DC power to oscillator board. Check spectral
content of oscillator output for spurious outputs, harmonic levels etc.

Data Reduction: Send input levels to printer. Output of Spectral
Analyzer to plotter.

Equipment List:

- | | |
|--------------------------------------|-----------------------------------|
| 1. <u>Func. Gen. (HP-8116A)</u> | 4. <u>Prog. Atten. (HP-8494G)</u> |
| 2. <u>Spect. Analyzer (HP-8566A)</u> | 5. <u>Plotter (HP-7475A)</u> |
| 3. <u>Att. Sw Dr. (HP-11713A)</u> | 6. <u>Computer (HP-9830C)</u> |



CHECK FOR SPECTRAL CONTENT, SPURIOUS OUTPUTS

Contractor: Magnavox

Board Tested: Reference Oscillator

Test Objective: Check oscillator output for stability, residual FM, noise sidebands, and phase noise.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>DC Voltage</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
2.	<u>_____</u>	<u>_____</u>	<u>_____</u>
3.	<u>_____</u>	<u>_____</u>	<u>_____</u>
4.	<u>_____</u>	<u>_____</u>	<u>_____</u>
5.	<u>_____</u>	<u>_____</u>	<u>_____</u>
6.	<u>_____</u>	<u>_____</u>	<u>_____</u>
7.	<u>_____</u>	<u>_____</u>	<u>_____</u>
8.	<u>_____</u>	<u>_____</u>	<u>_____</u>
9.	<u>_____</u>	<u>_____</u>	<u>_____</u>
10.	<u>_____</u>	<u>_____</u>	<u>_____</u>
11.	<u>_____</u>	<u>_____</u>	<u>_____</u>
12.	<u>_____</u>	<u>_____</u>	<u>_____</u>

Outputs

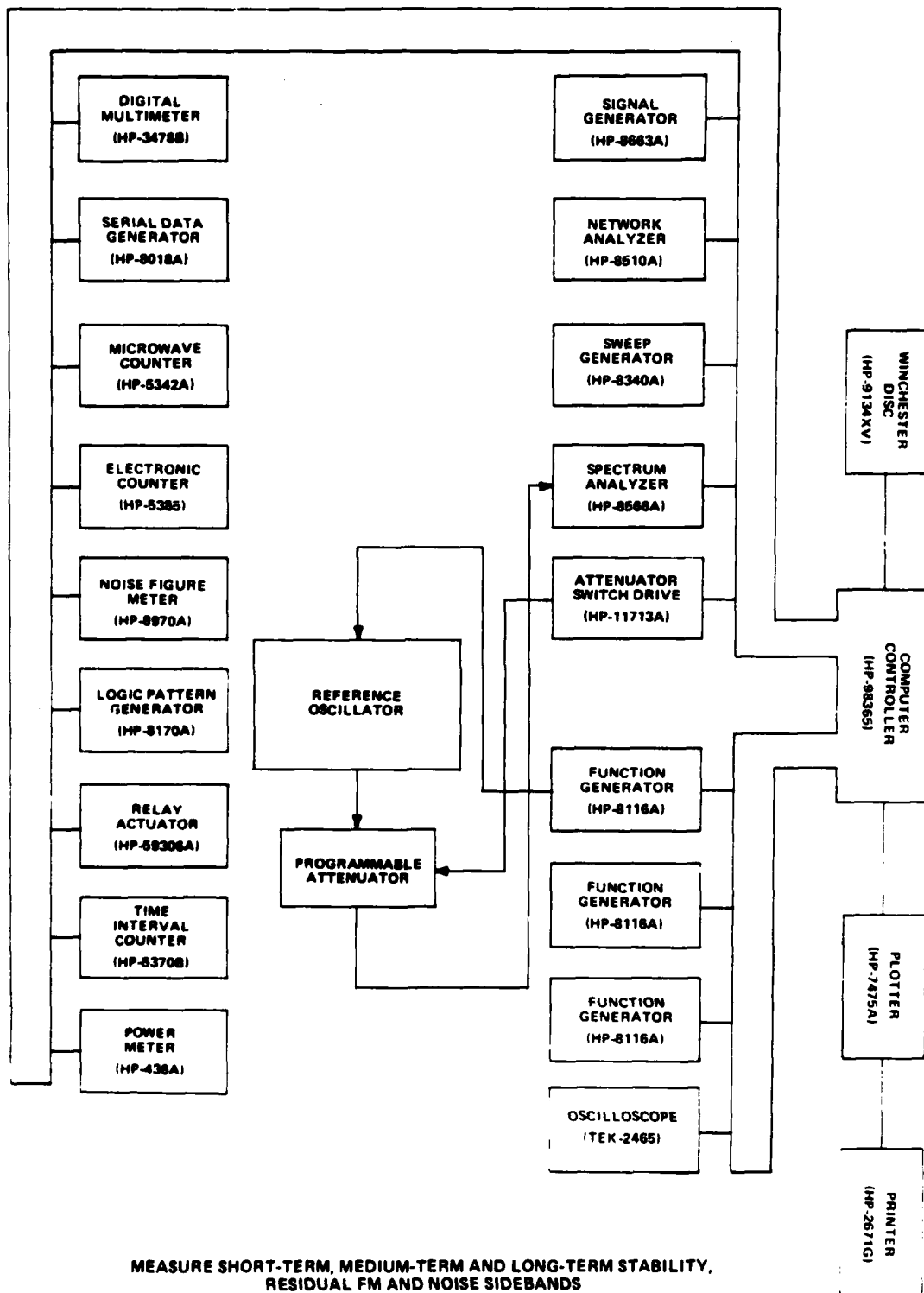
<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1. <u>Oscillator Output</u>	Short term Stab. <u>1×10^{-12} (1-100 sec)</u>	<u>Spect. Analyzer (HP-8566A)</u>
2. _____	<u>1×10^{-11} (.1-1000 sec)</u>	<u>Prog. Atten. (HP-8494G)</u>
3. _____	<u>Phase Noise -115dBc (2Hz)</u>	<u>Atten. Sw Drive (HP-11713A)</u>
4. _____	<u>Long term Stab. - TBD</u>	_____
5. _____	<u>Noise Sidebands - TBD</u>	_____
	<u>Residual FM - TBD</u>	_____

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply DC power to oscillator board. Measure short and
long term stability, phase noise, noise sidebands, and residual FM of
oscillator output.

Data Reduction: Send input levels to printer. Output of Spectrum
Analyzer to plotter.

Equipment List:

- | | | |
|--------------------------------------|-----------------------------------|------------------------------|
| 1. <u>Func. Gen. (HP-8116A)</u> | 4. <u>Prog. Atten. (HP-8494G)</u> | 7. <u>Printer (HP-2671G)</u> |
| 2. <u>Spect. Analyzer (HP-8566A)</u> | 5. <u>Plotter (HP-7475A)</u> | 8. _____ |
| 3. <u>Atten. Sw Dr. (HP-11713A)</u> | 6. <u>Computer (HP-9836S)</u> | 9. _____ |



Contractor: Magnavox
Board Tested: Reference Oscillator
Test Objective: Verify within specification operation of
oscillator after 5 minute warm up.

Inputs

	<u>Input Name</u>	<u>Input Level</u>	<u>Equipment Used</u>
1.	<u>DC Voltage</u>	<u>TBD</u>	<u>Function Gen. (HP-8116A)</u>
2.	<u></u>	<u></u>	<u></u>
3.	<u></u>	<u></u>	<u></u>
4.	<u></u>	<u></u>	<u></u>
5.	<u></u>	<u></u>	<u></u>
6.	<u></u>	<u></u>	<u></u>
7.	<u></u>	<u></u>	<u></u>
8.	<u></u>	<u></u>	<u></u>
9.	<u></u>	<u></u>	<u></u>
10.	<u></u>	<u></u>	<u></u>
11.	<u></u>	<u></u>	<u></u>
12.	<u></u>	<u></u>	<u></u>

Outputs

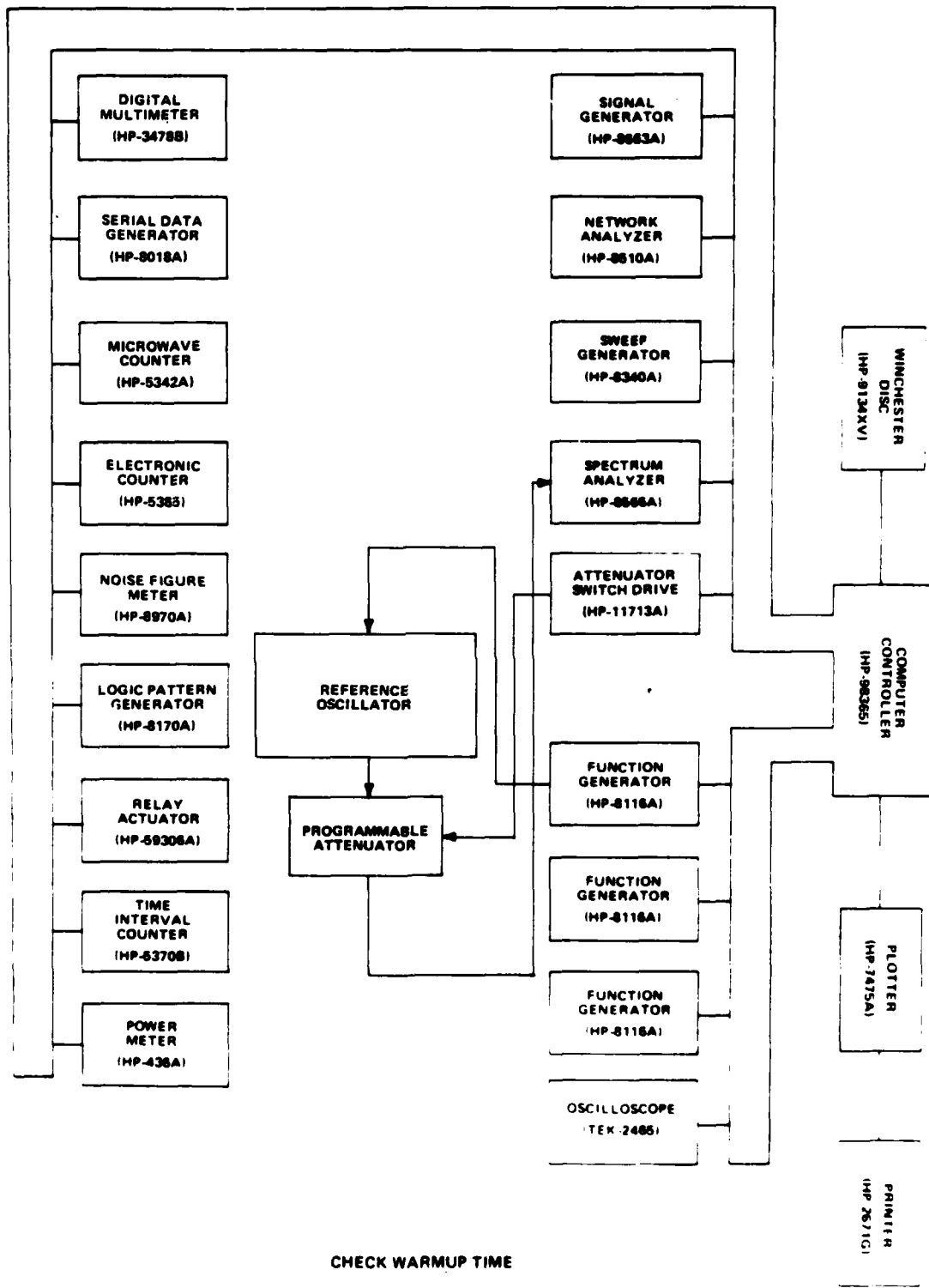
	<u>Output Name</u>	<u>Output Level</u>	<u>Equipment Used</u>
1.	<u>Oscillator Output</u>	<u>---</u>	<u>Spect. Analyzer (HP-8566A)</u>
2.	<u>_____</u>	<u>_____</u>	<u>Atten. Sw Dr. (HP-11713A)</u>
3.	<u>_____</u>	<u>_____</u>	<u>Prog. Atten. (HP-8494G)</u>
4.	<u>_____</u>	<u>_____</u>	<u>_____</u>
5.	<u>_____</u>	<u>_____</u>	<u>_____</u>

Test Procedure: Turn on test equipment. Set measurement equipment
to proper ranges. Apply DC power to oscillator board. After 5 minute
specificaiton period take stability reading with Spectrum Analyzer to
verify proper operation after the warm up period.

Data Reduction: Send input levels to printer. Output of Spectrum
Analyzer to plotter.

Equipment List:

- | | | | | | |
|----|----------------------------------|----|--------------------------------|----|---------------------------|
| 1. | <u>Func. Gen. (HP-8116A)</u> | 4. | <u>Prog. Atten. (HP-8494G)</u> | 7. | <u>Printer (HP-2671G)</u> |
| 2. | <u>Spect. Anal. (HP-8566A)</u> | 5. | <u>Plotter (HP-7475A)</u> | 8. | <u>_____</u> |
| 3. | <u>Atten. Sw Dr. (HP-11713A)</u> | 6. | <u>Computer (HP-9836S)</u> | 9. | <u>_____</u> |



CHECK WARMUP TIME

4.0 SOFTWARE REQUIREMENTS

This section contains the software requirements for the bench test procedures defined in Section 3. These software requirements are defined in terms of flowcharts.

The software requirements contained herein are written at the functional level. There is a flowchart for each test procedure that illustrates the sequence of events that will be required in order to perform these tests by computer. In addition to these requirements, all tests will include the following capabilities:

- o menu driven testing will prompt operator through:
 1. test set-ups
 2. testing
 3. data reduction/storage/hardcopy
- o Each test will have a program module number and will be contained in a testing menu. All tests with the same set-ups can be grouped together to be run one after the other.
- o Each test will incorporate a full error-checking capability during operator inputs so that no manual error can be entered into the system.
- o Data reduction based on baseline data and/or range limits. The specified range values and/or tolerances will be stored and compared to the results obtained from the actual test. The values will be displayed and any differences can quickly be seen.
- o There will be real-time monitoring for any bus available information during testing.
- o There will be off-line storage or transfer of data for interfacing with the DPSSF/NSL system capabilities. This capability will be used for any information that is needed for the DPSSF, NSL or for RFL data reduction requirements.

- o All software will be stored including back-up disks and hardcopy listings in a library which will contain as a minimum the following information in its catalog:
 - o program module name and number (disk #)
 - o back-up copy # (disk #)
 - o date written, by whom
 - o date last revised, by whom

There are two floppy disk drives which have a memory of 170k each built into the computer. All information will be stored on either of these floppy disks. There is also a Winchester hard disk provided. This can be used as a temporary storage for data and information obtained while testing, if needed. For example, if a series of tests are being run and many data points are needed to be stored and all of the memory is being used in the computer, this information is stored temporarily on the Winchester disk until processing is complete and then archived onto the floppy disks.

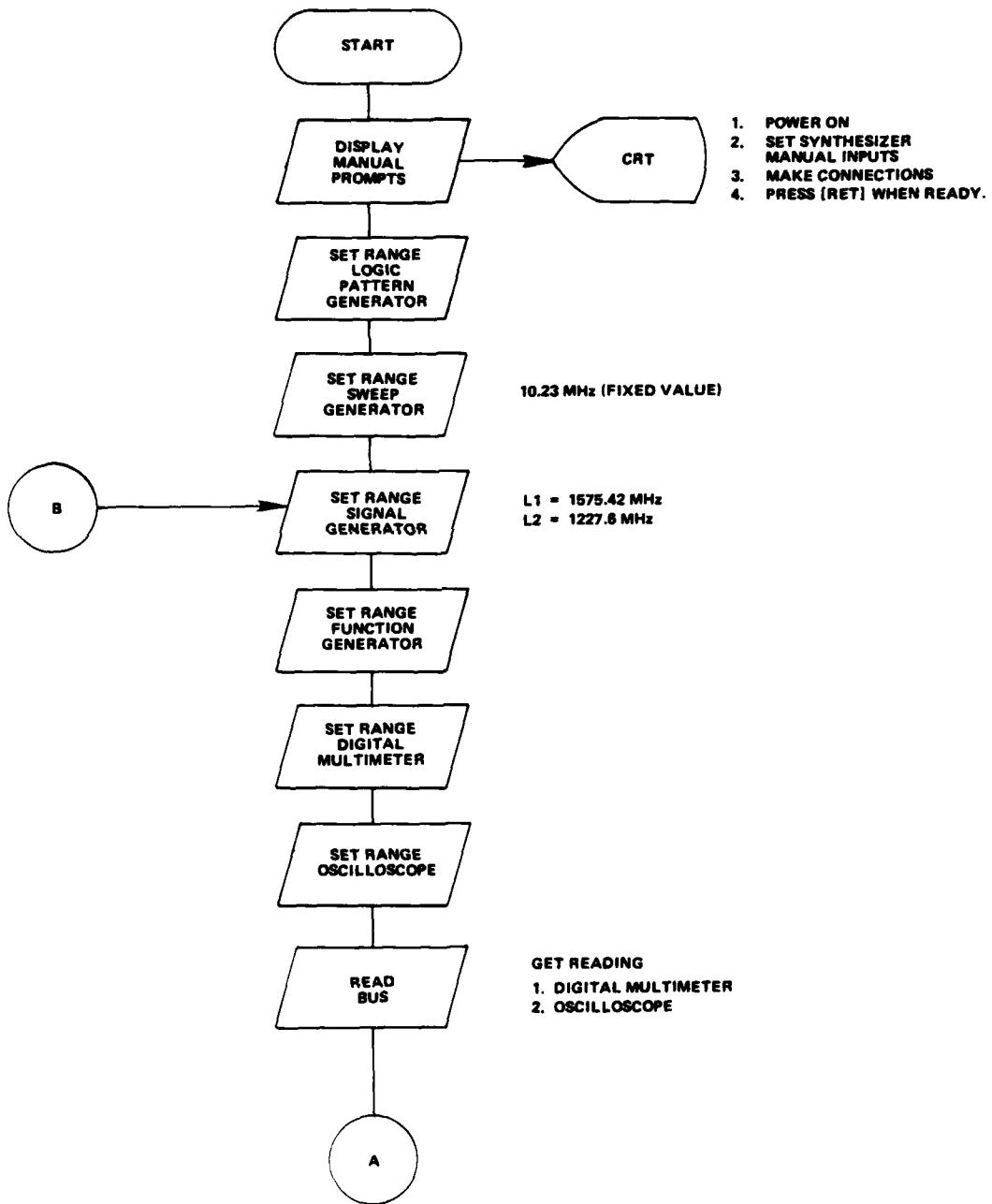
4.1 MAGNAVOX SOFTWARE REQUIREMENTS

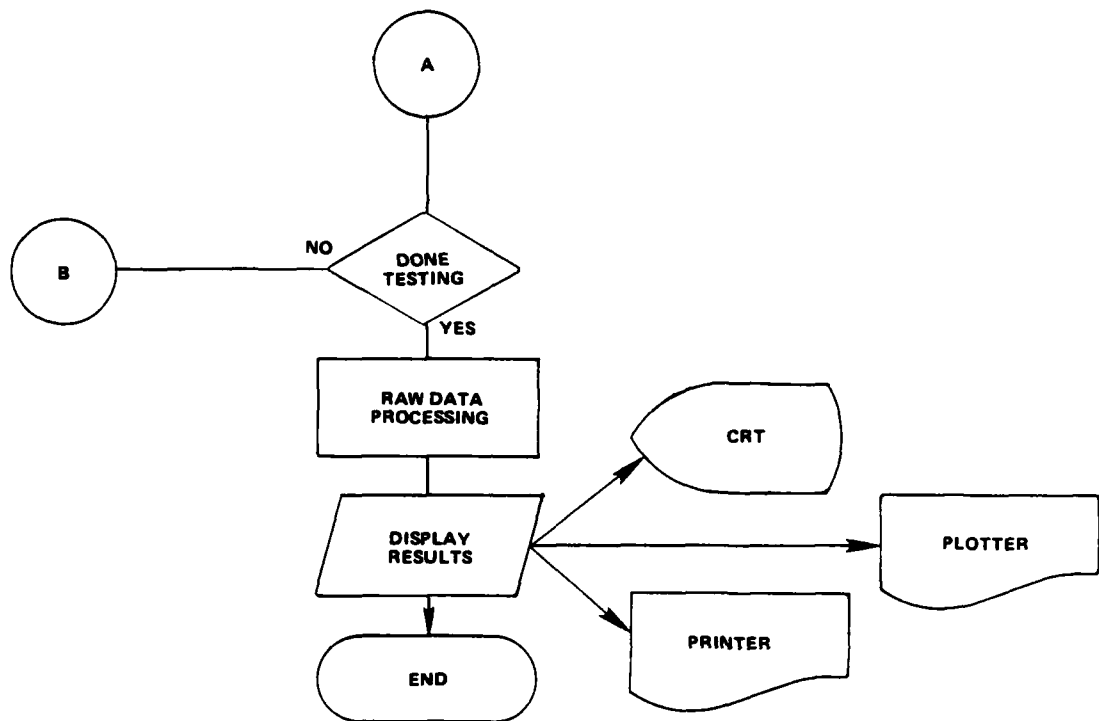
The following sheets contain the software requirements for the Bench test procedures for each of the boards identified in Section 3.1. The software requirements will also be refined and expanded as more detailed information becomes available for the bench tests.

4.1.1 RF SYNTHESIZER MODULE

GAIN AF1 MEASUREMENT

PROGRAM MODULE NAME: _____

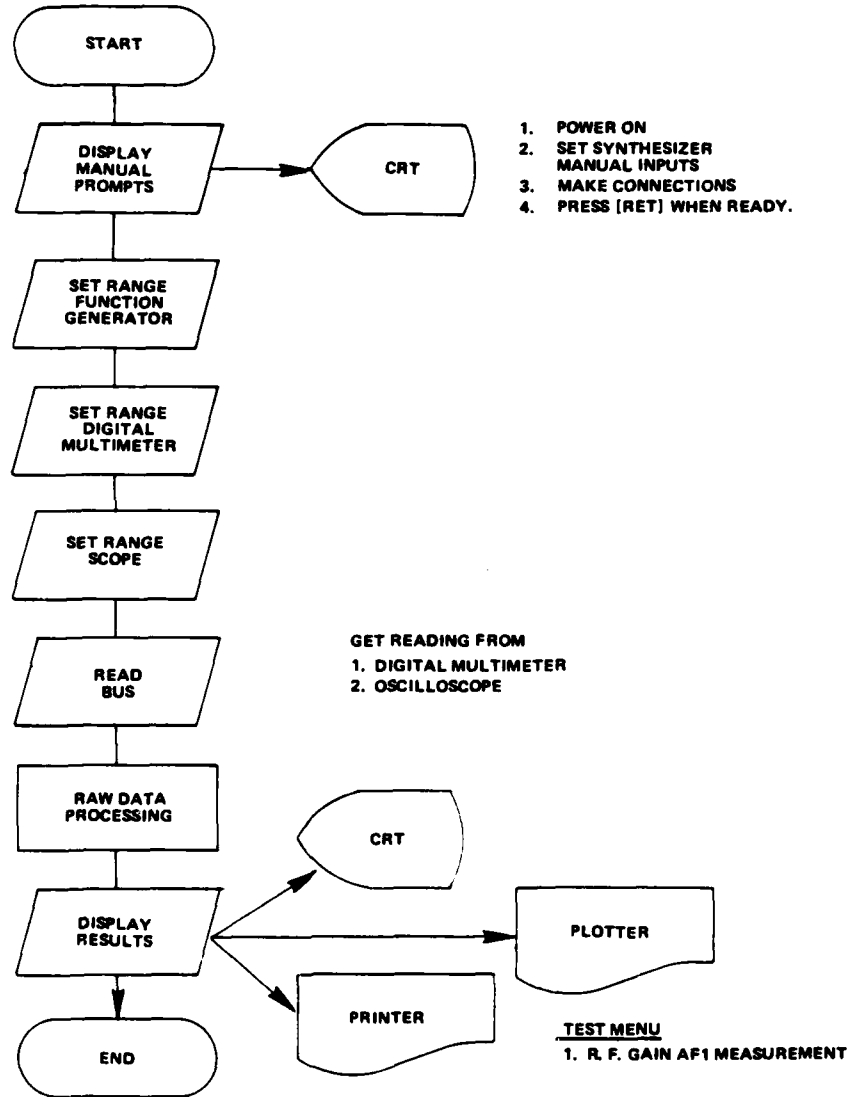




TEST MENU
1. GAIN AF1 MEASUREMENT

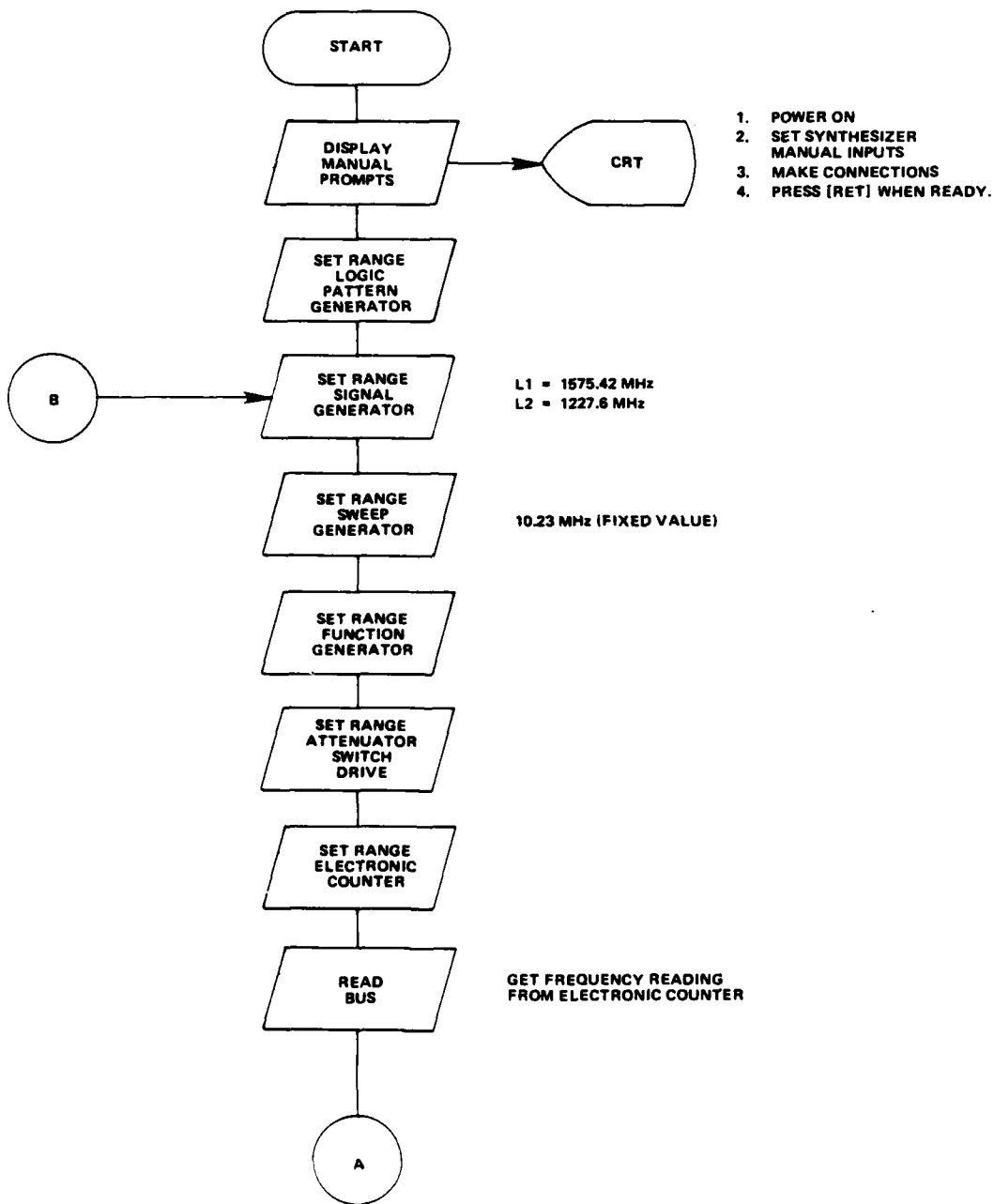
R. F. GAIN AF1 MEASUREMENT

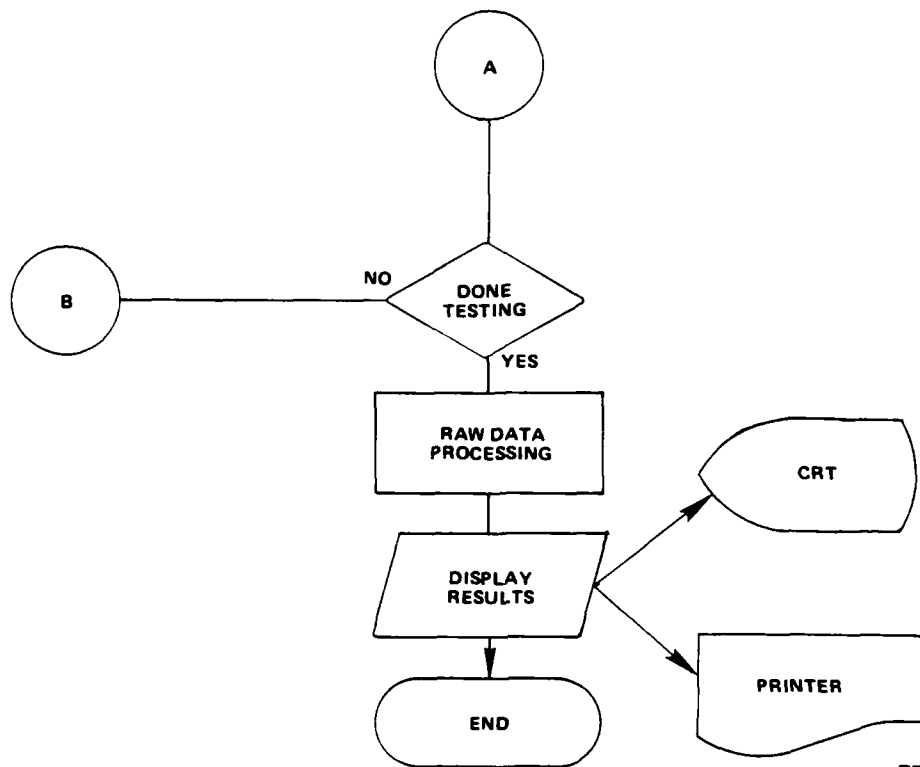
PROGRAM MODULE NAME: _____



38F(IF) OUTPUT FREQUENCY

PROGRAM MODULE NAME: _____

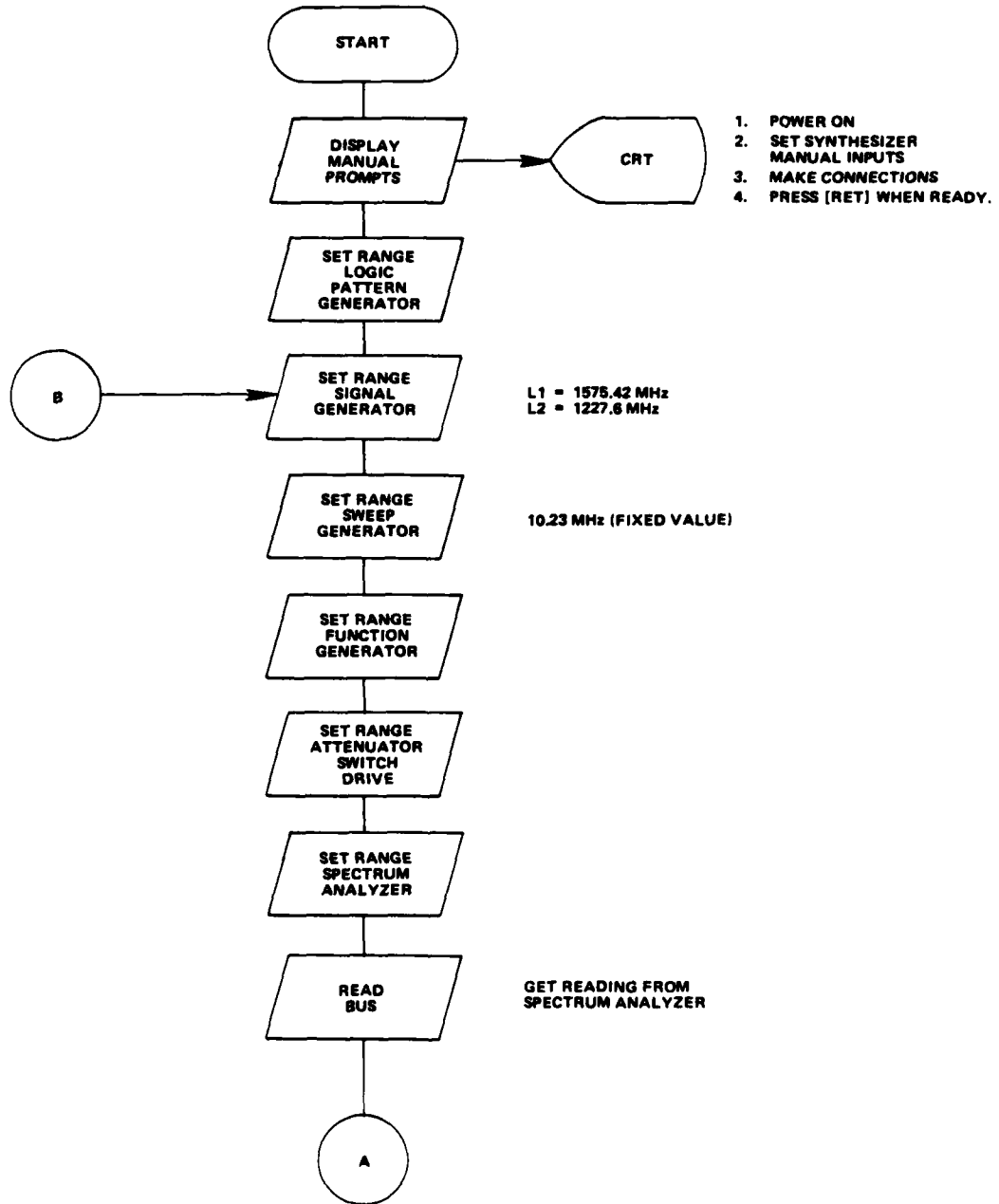


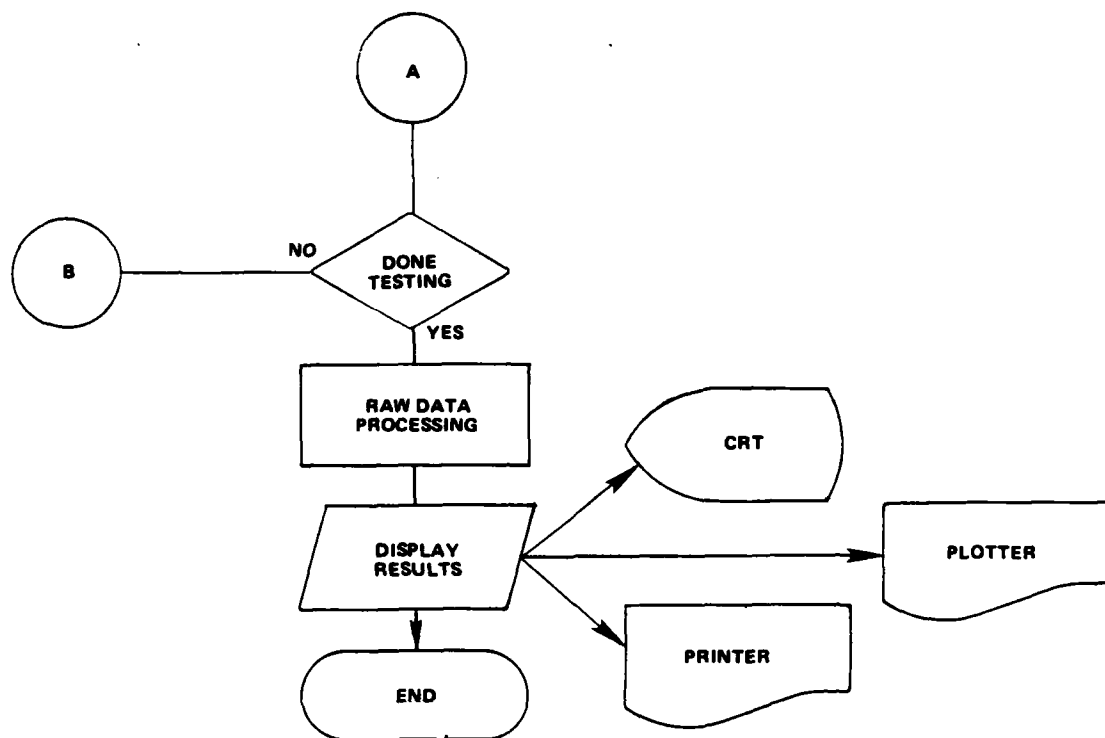


TEST MENU
1. 36F(IF) OUTPUT FREQUENCY

36F(IF) SPECTRAL CONTENT

PROGRAM MODULE NAME: _____

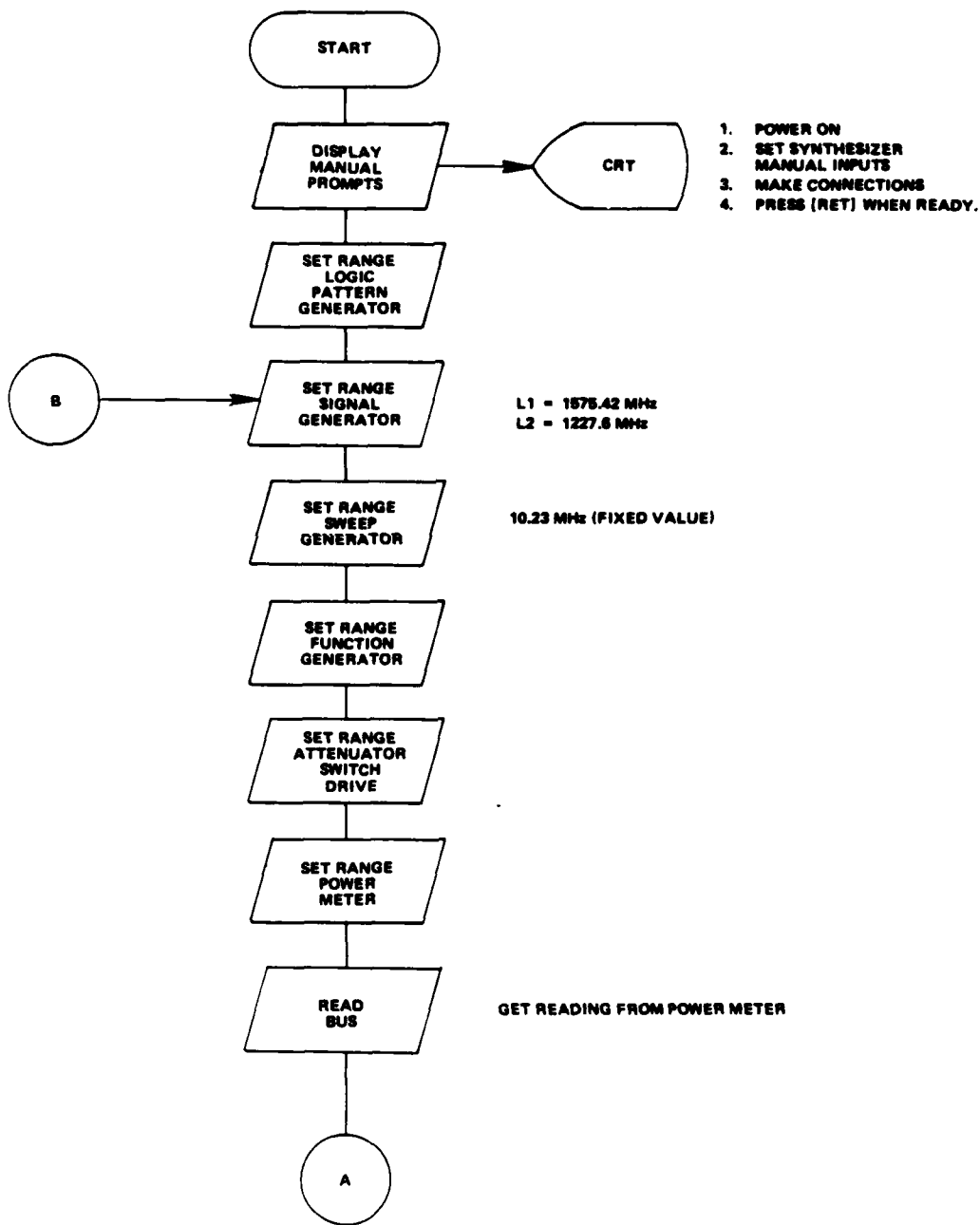


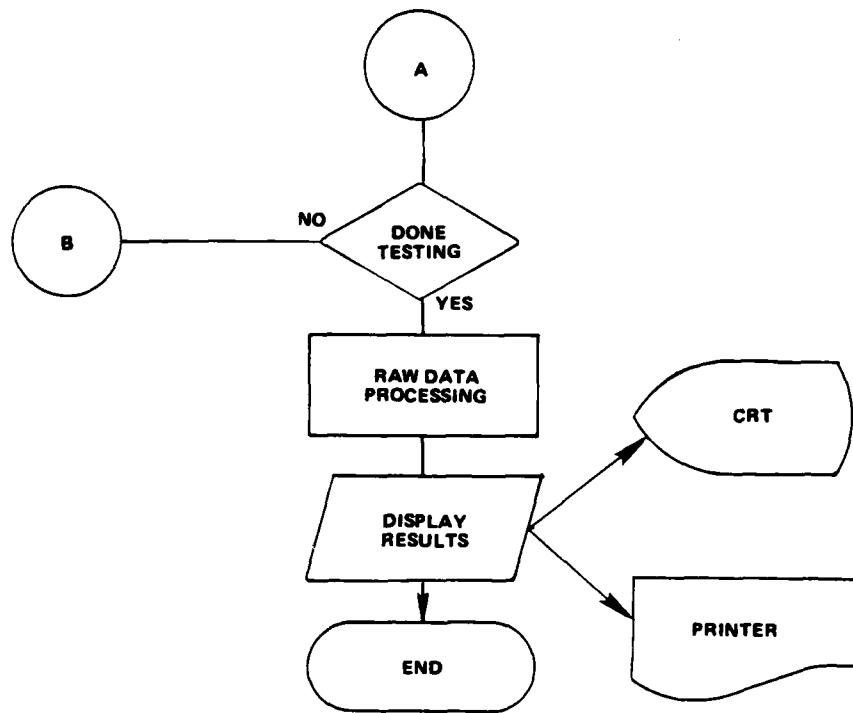


TEST MENU
1. 36F(IF) SPECTRAL CONTENT

38F(IF) OUTPUT POWER MEASUREMENT

PROGRAM MODULE NAME: _____

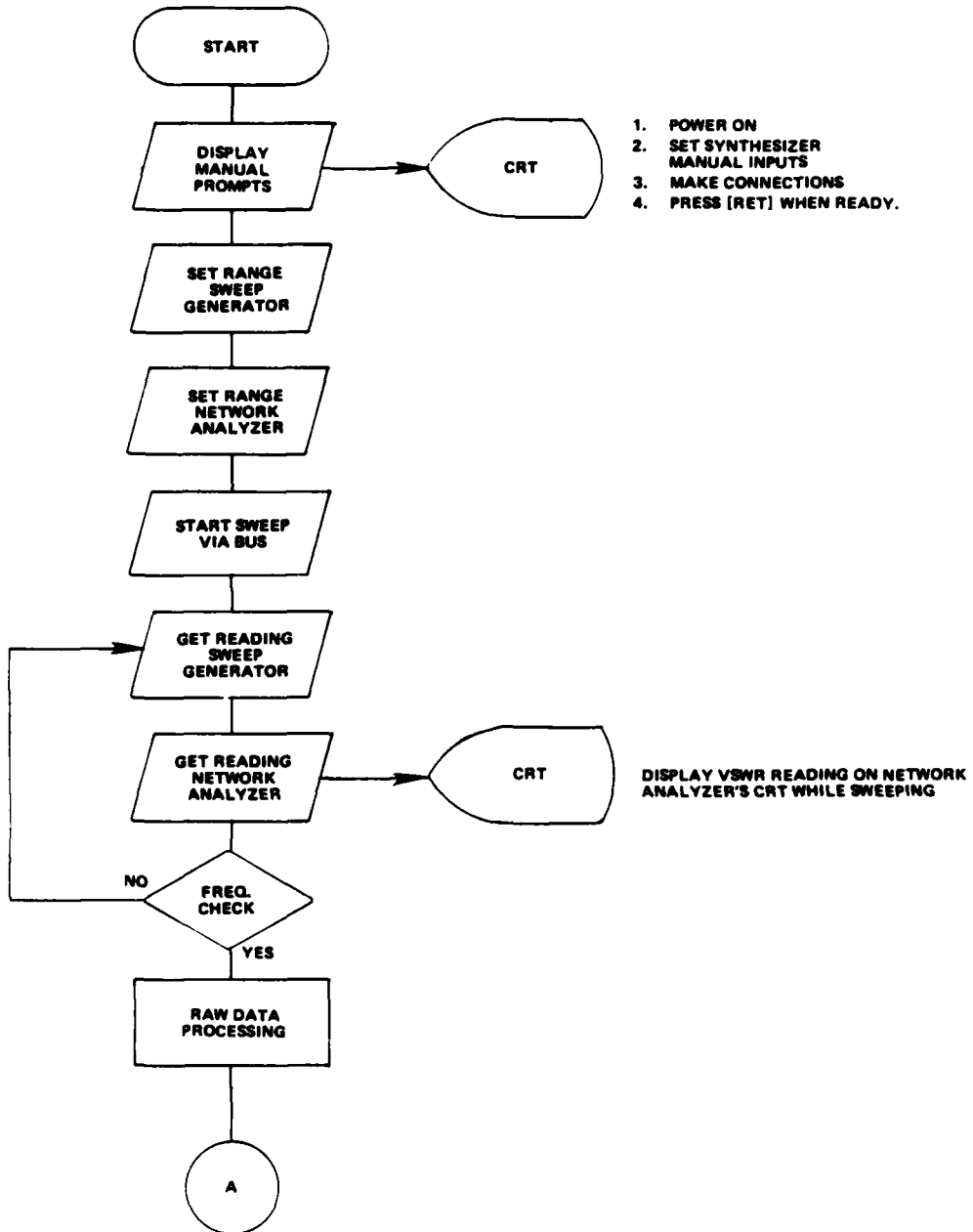


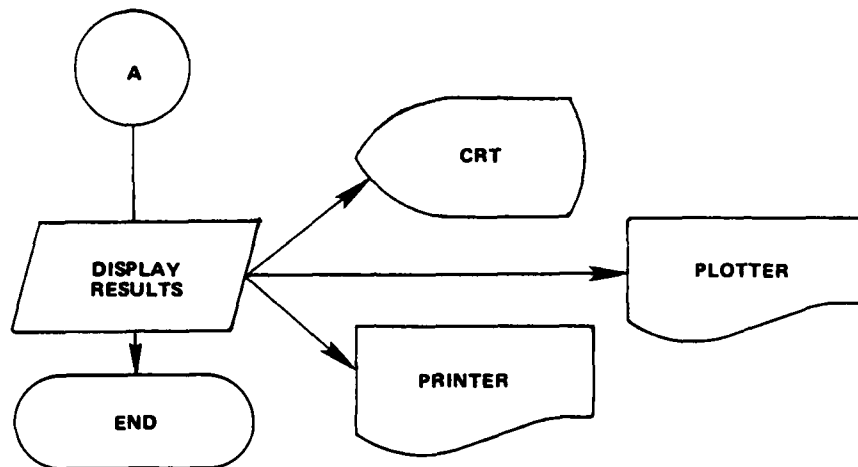


TEST MENU
1. 36F(IF) OUTPUT POWER
MEASUREMENT

38F(I) VSWR AND IMPEDANCE MEASUREMENT

PROGRAM MODULE NAME: _____

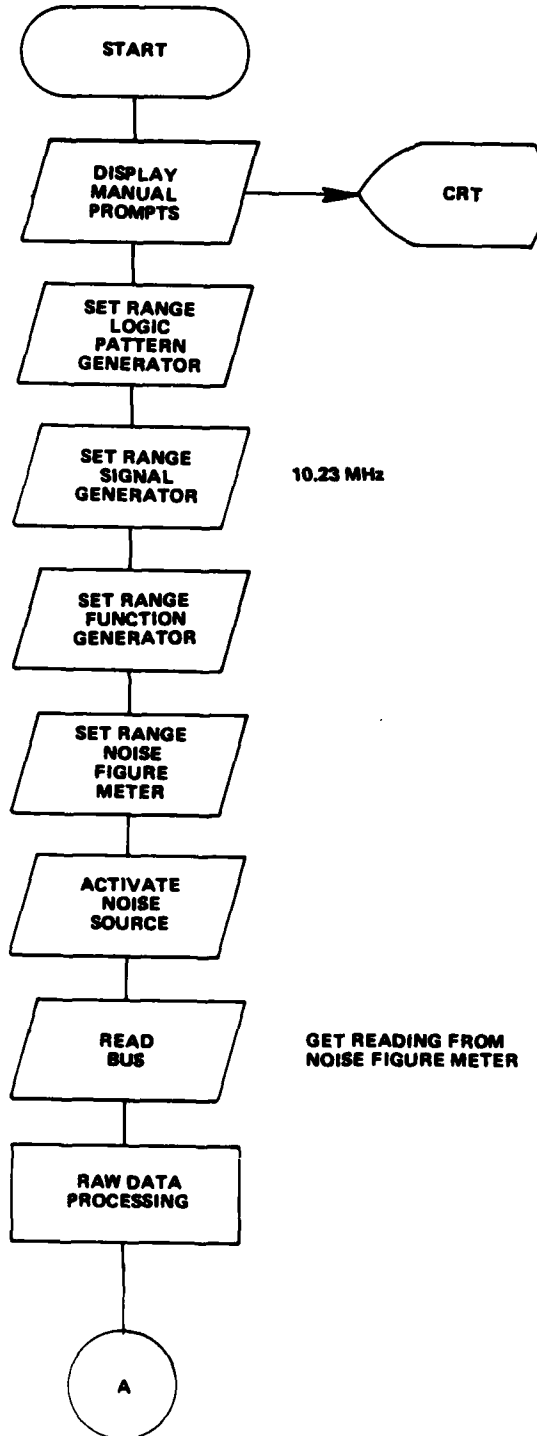




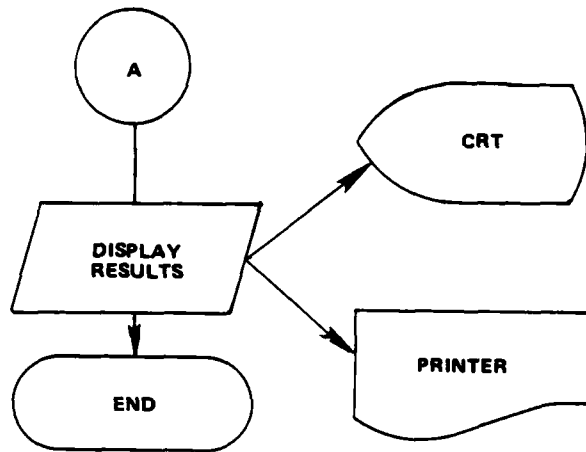
TEST MENU
1. 38F(1F) VSWR AND
IMPEDANCE MEASUREMENT

36F(IF) NOISE FIGURE MEASUREMENT

PROGRAM MODULE NAME: _____



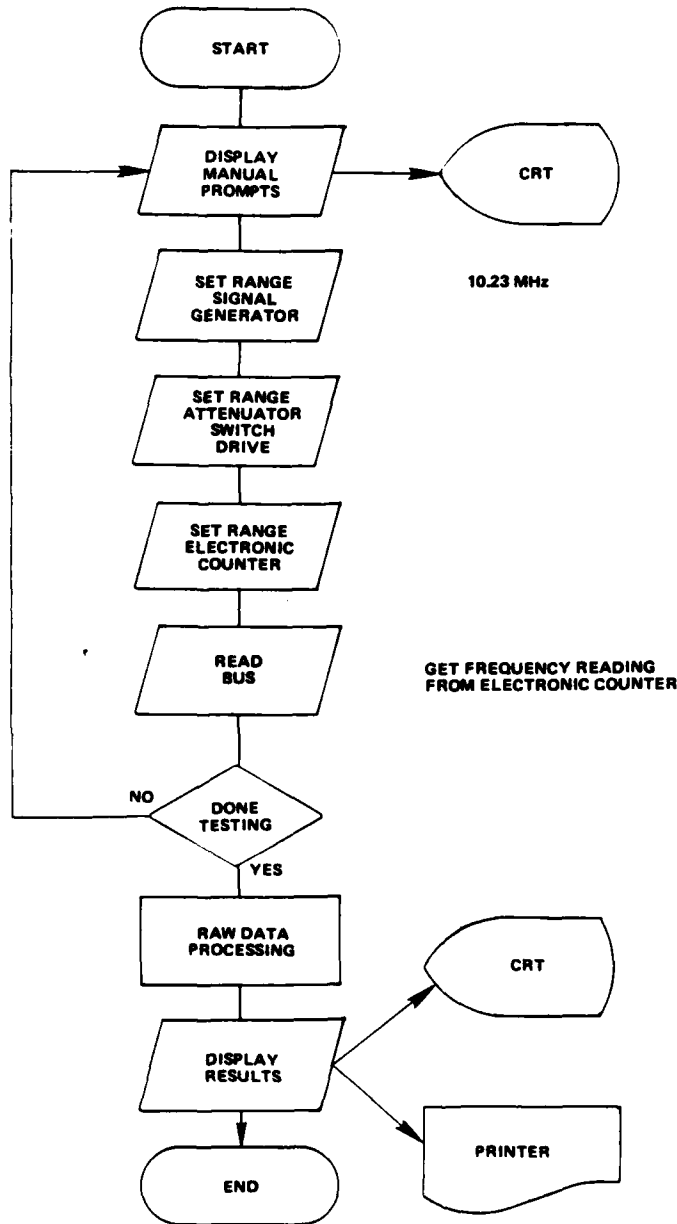
1. POWER ON
2. SET SYNTHESIZER MANUAL INPUTS
3. MAKE CONNECTIONS
4. PRESS [RET] WHEN READY.



TEST MENU
1. 36F(IF) NOISE FIGURE
MEASUREMENT

OUTPUT FREQUENCY MEASUREMENTS

PROGRAM MODULE NAME: _____



1. POWER ON
2. SET SYNTHESIZER MANUAL INPUTS
3. MAKE CONNECTIONS
4. PRESS [RET] WHEN READY.

10.23 MHz

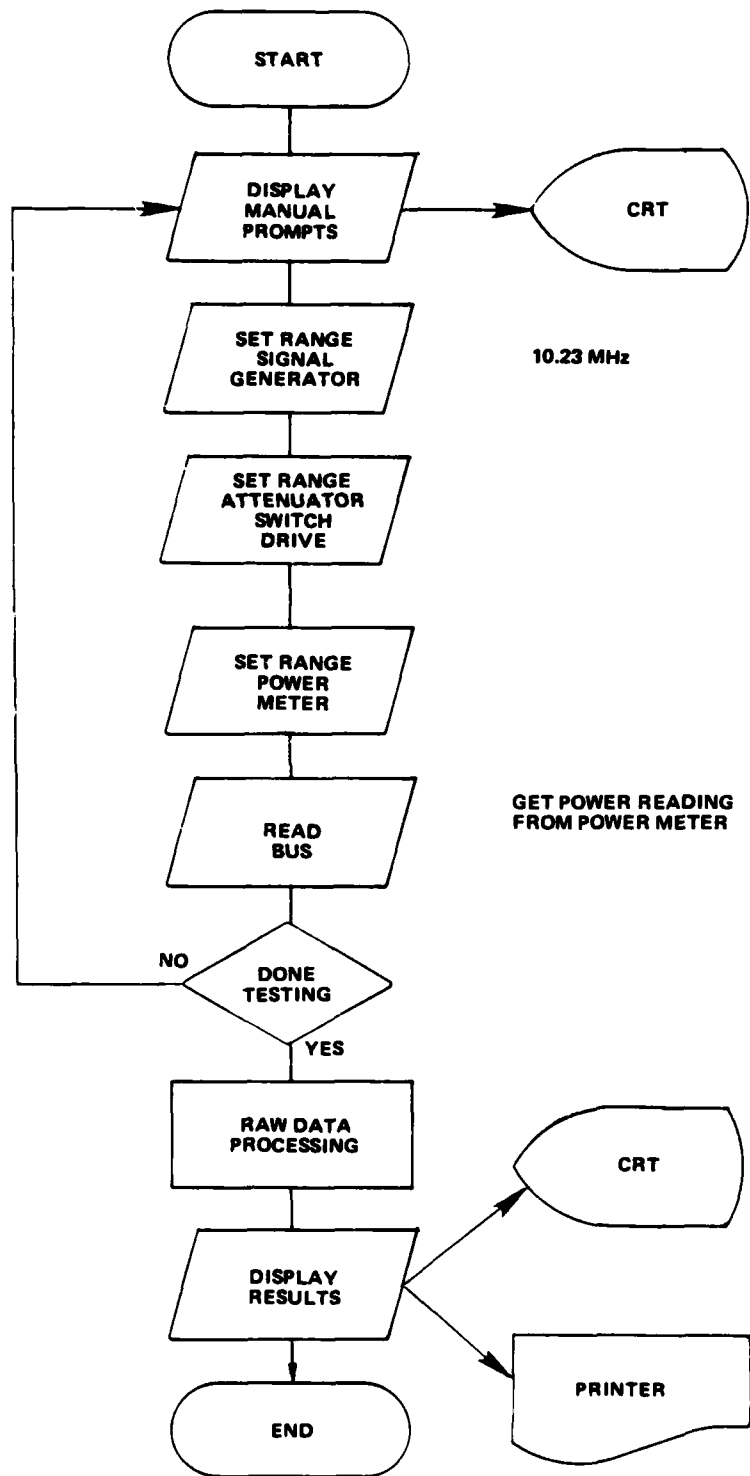
GET FREQUENCY READING FROM ELECTRONIC COUNTER

TEST MENU

1. 68F
2. 28-7/8F
3. 6F
4. 2F

OUTPUT POWER MEASUREMENT

PROGRAM MODULE NAME: _____



1. POWER ON
2. SET SYNTHESIZER MANUAL INPUTS
3. MAKE CONNECTIONS
4. PRESS [RET] WHEN READY.

10.23 MHz

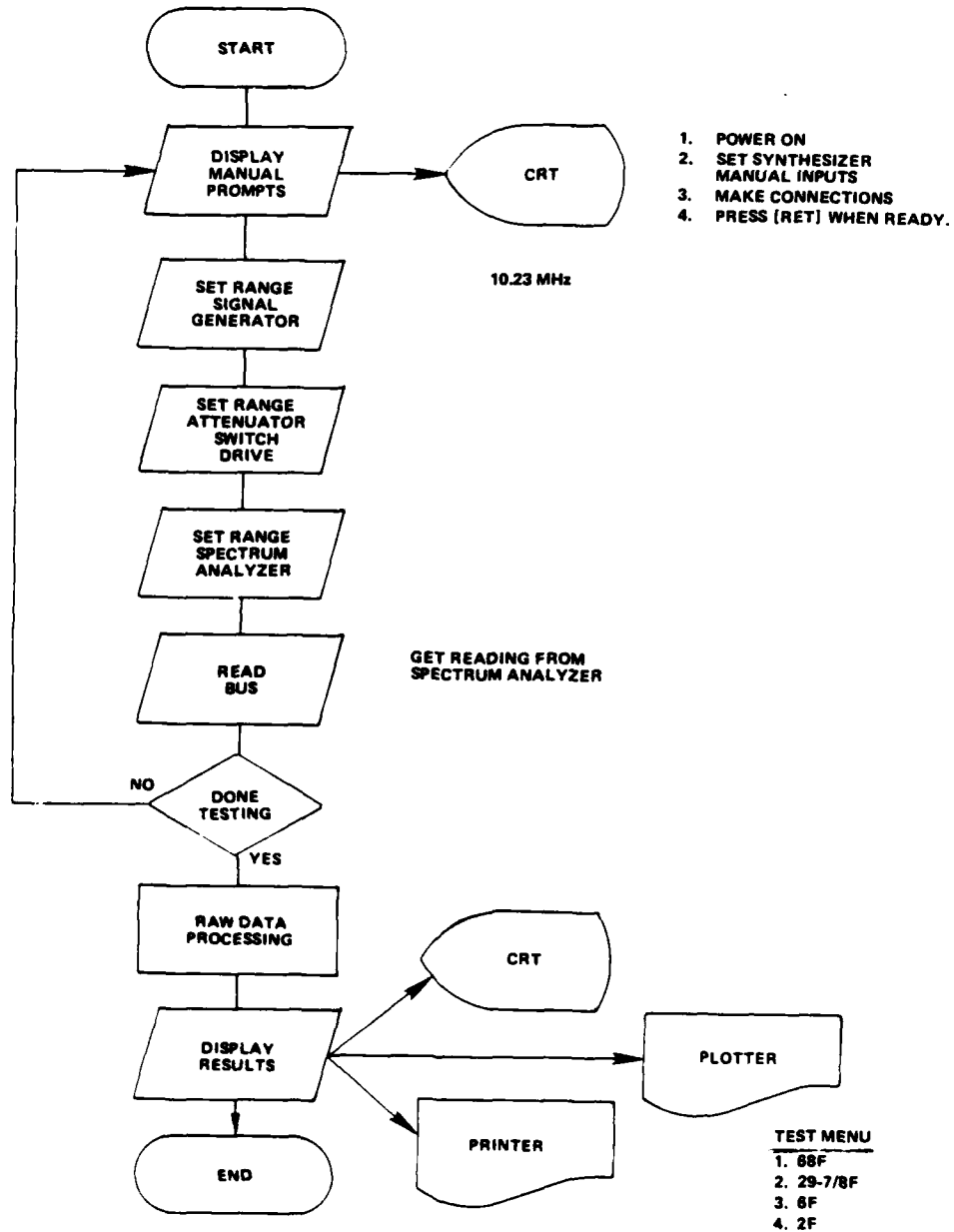
GET POWER READING FROM POWER METER

TEST MENU

1. 68F
2. 29-7/8F
3. 6F
4. 2F

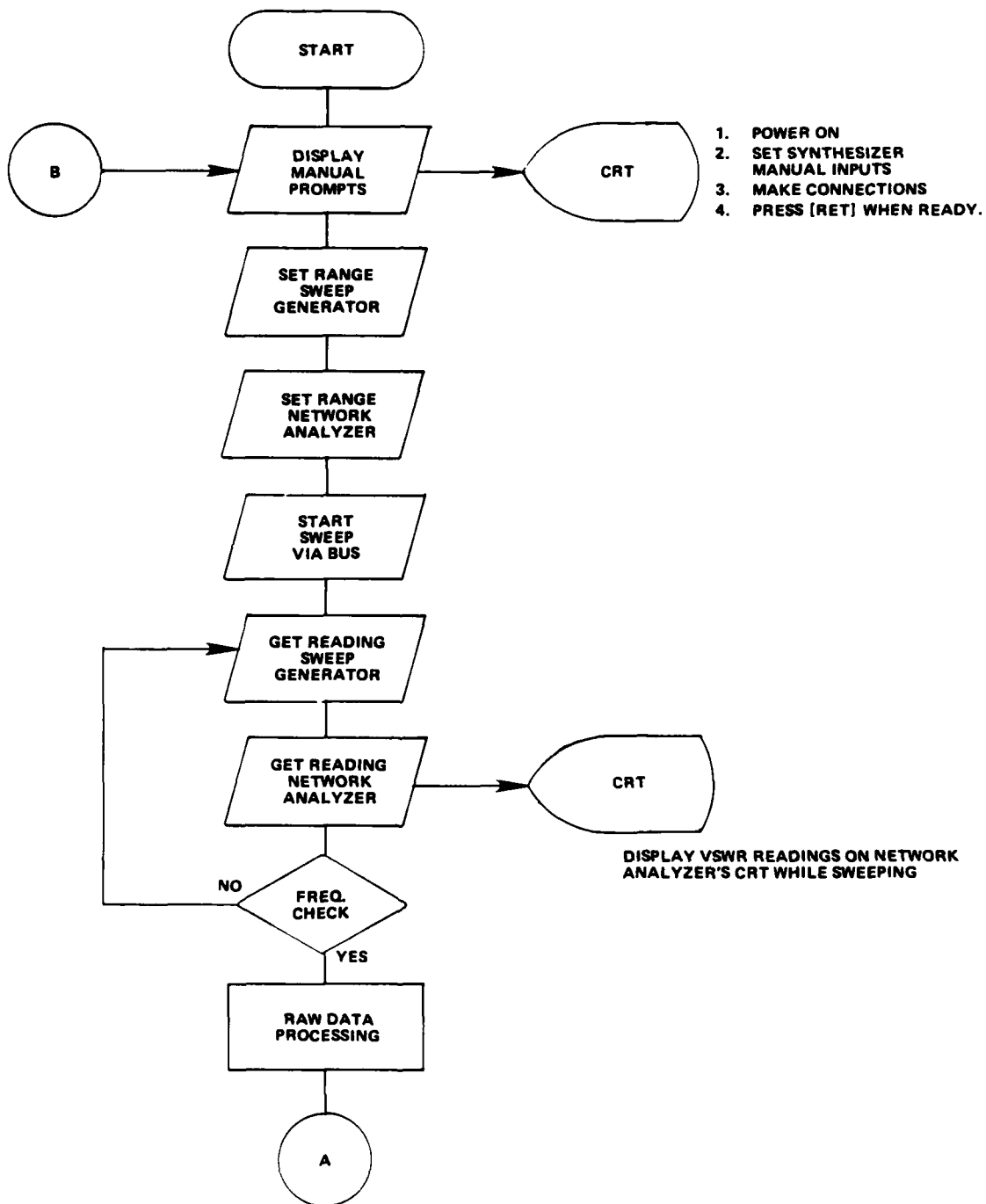
MEASURE SPECTRAL CONTENT

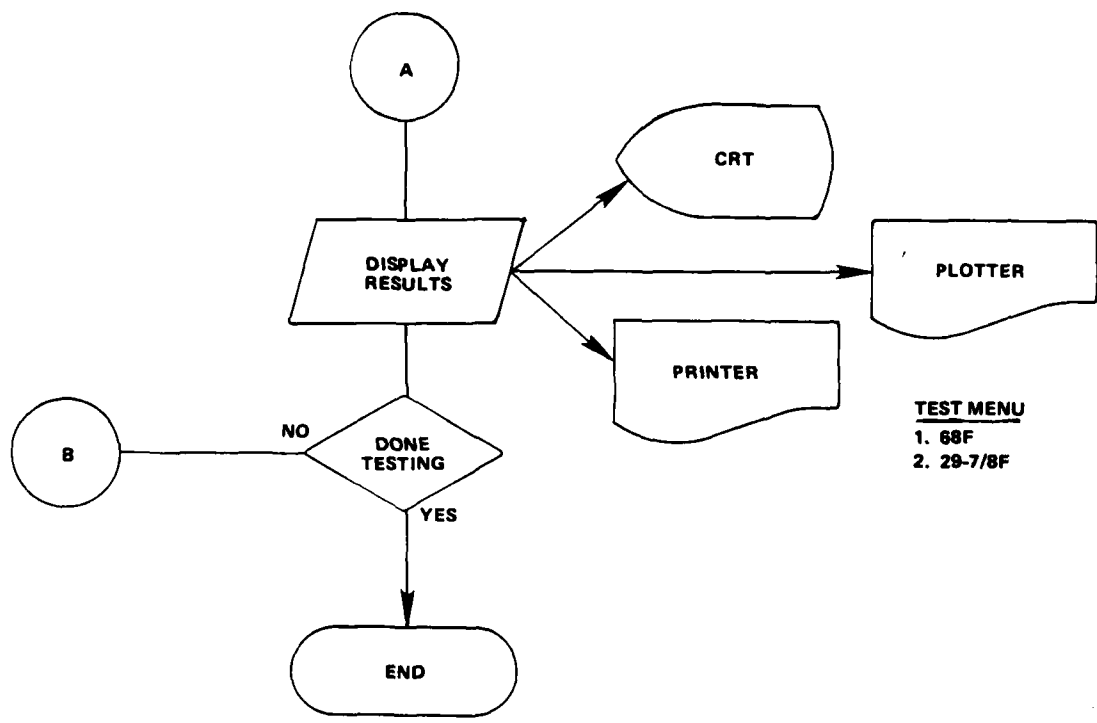
PROGRAM MODULE NAME: _____



VSWR AND IMPEDANCE MEASUREMENT

PROGRAM MODULE NAME: _____

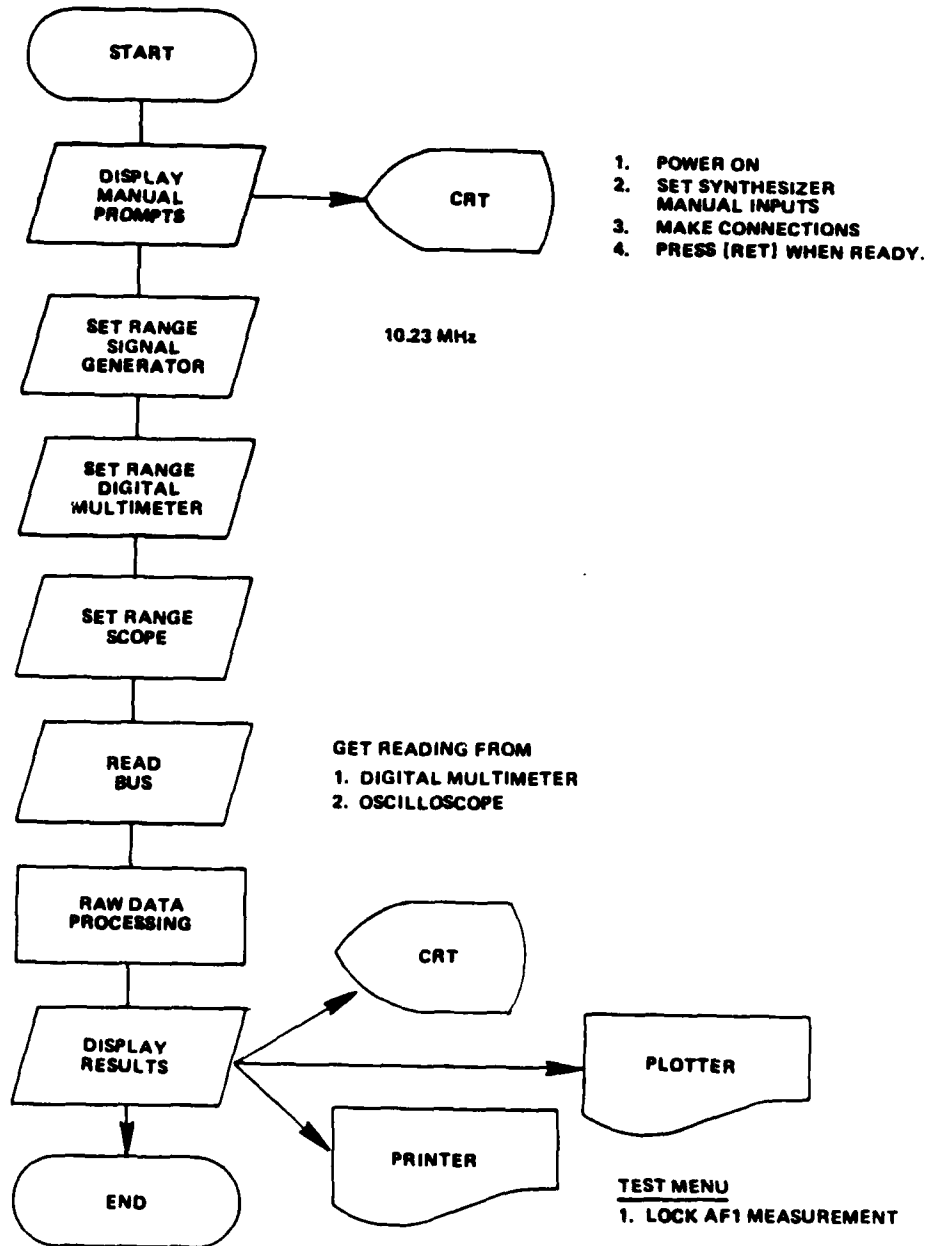




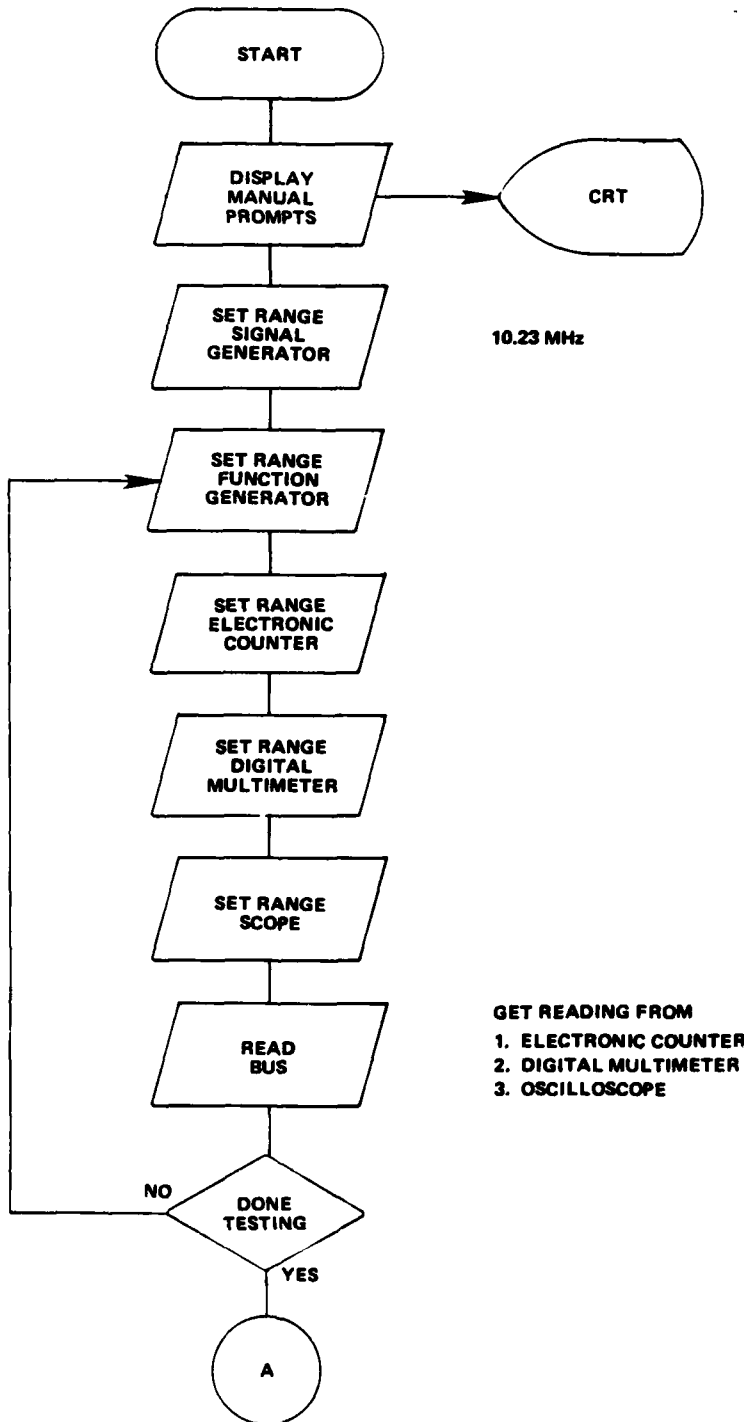
TEST MENU
1. 68F
2. 29-7/8F

MEASURE LOCK AF1

PROGRAM MODULE NAME: _____



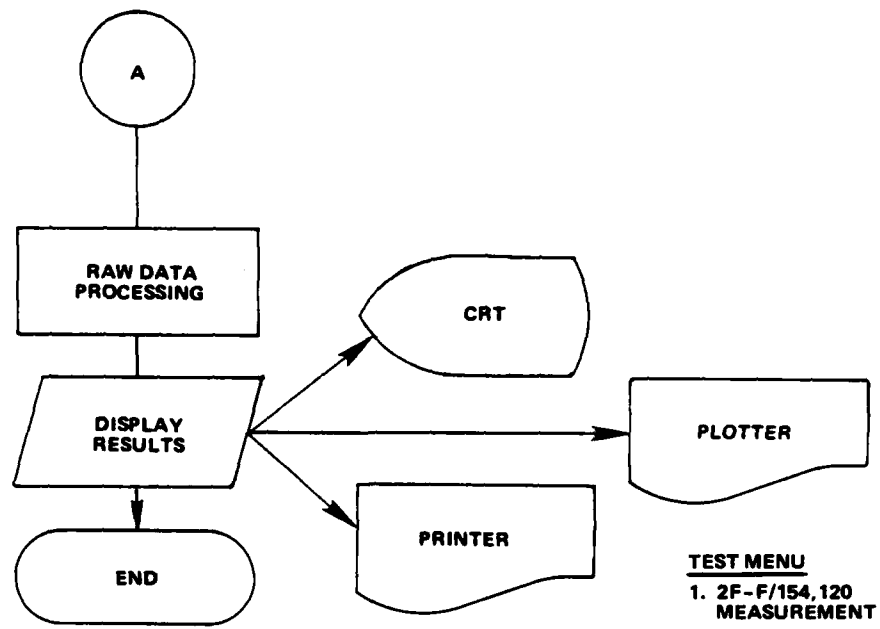
PROGRAM MODULE NAME: _____



1. POWER ON
2. SET SYNTHESIZER MANUAL INPUTS
3. MAKE CONNECTIONS
4. PRESS [RET] WHEN READY.

10.23 MHz

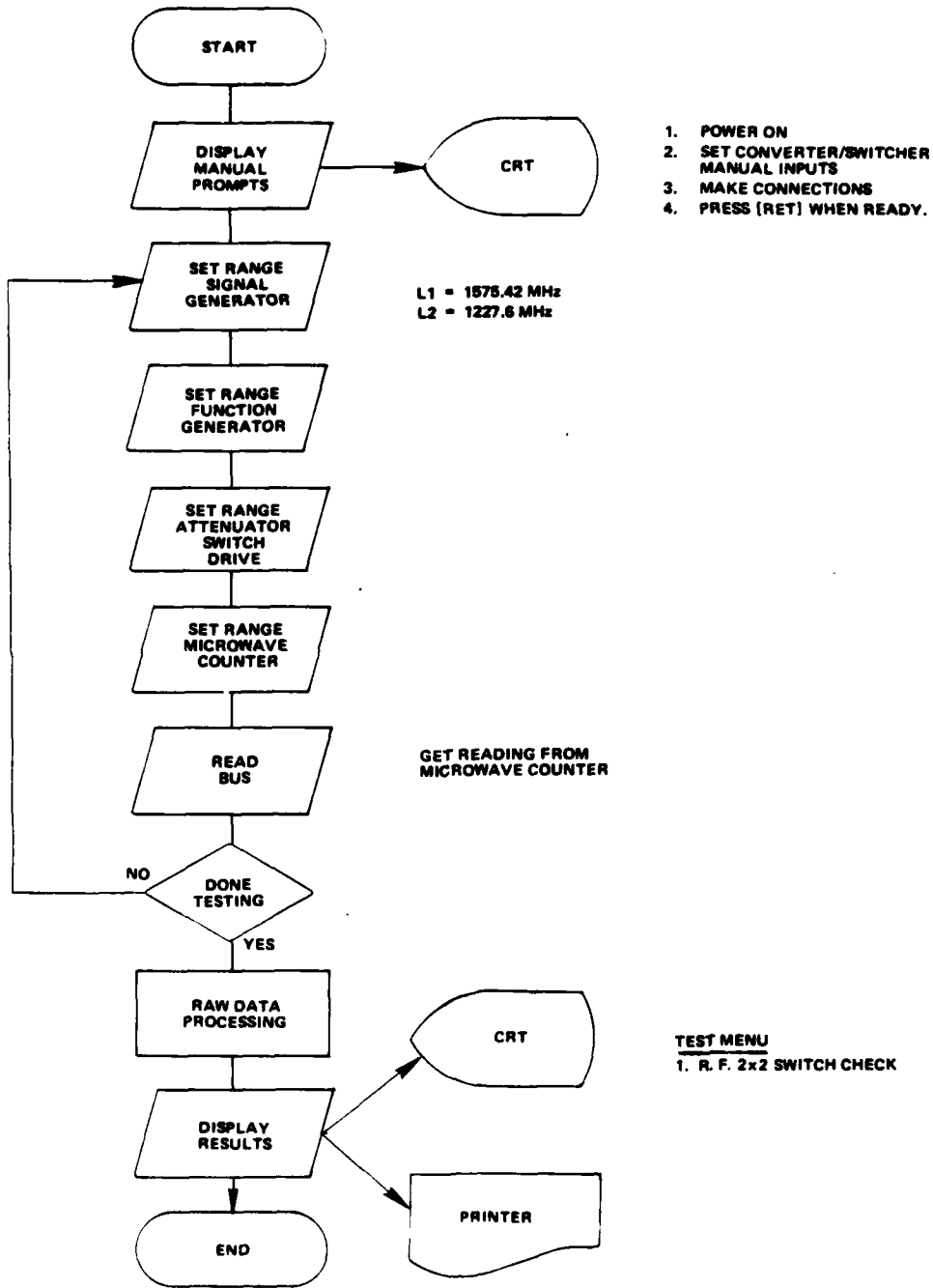
- GET READING FROM
1. ELECTRONIC COUNTER
 2. DIGITAL MULTIMETER
 3. OSCILLOSCOPE



4.1.2 CONVERTER/CHANNEL SWITCHER MODULE

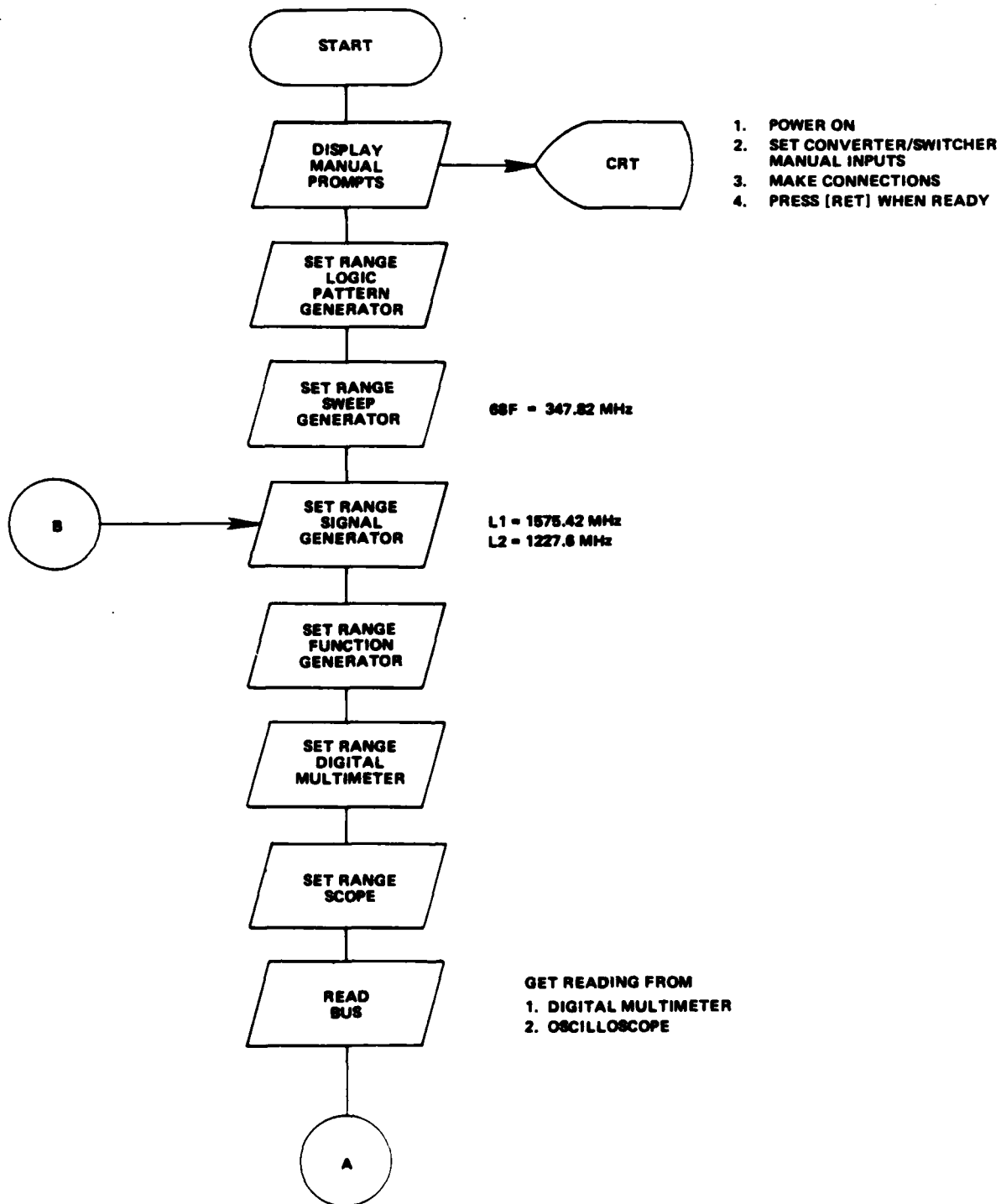
R. F. 2x2 SWITCH CHECK

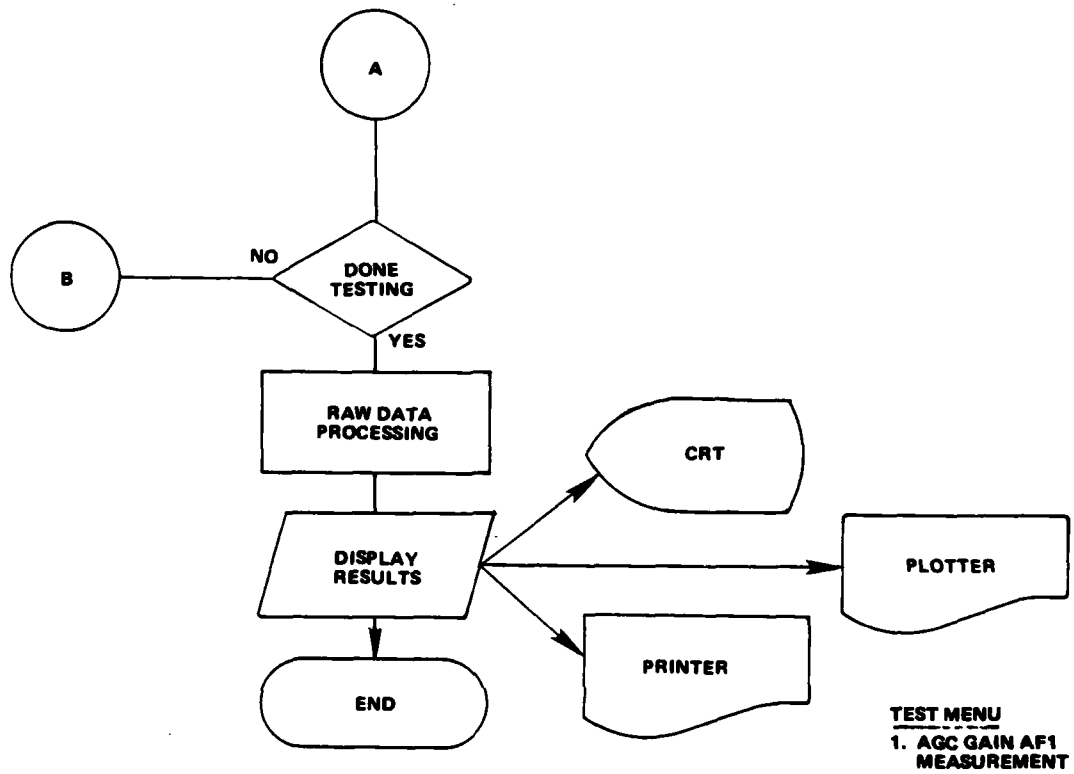
PROGRAM MODULE NAME: _____



MEASURE AGC GAIN AF1

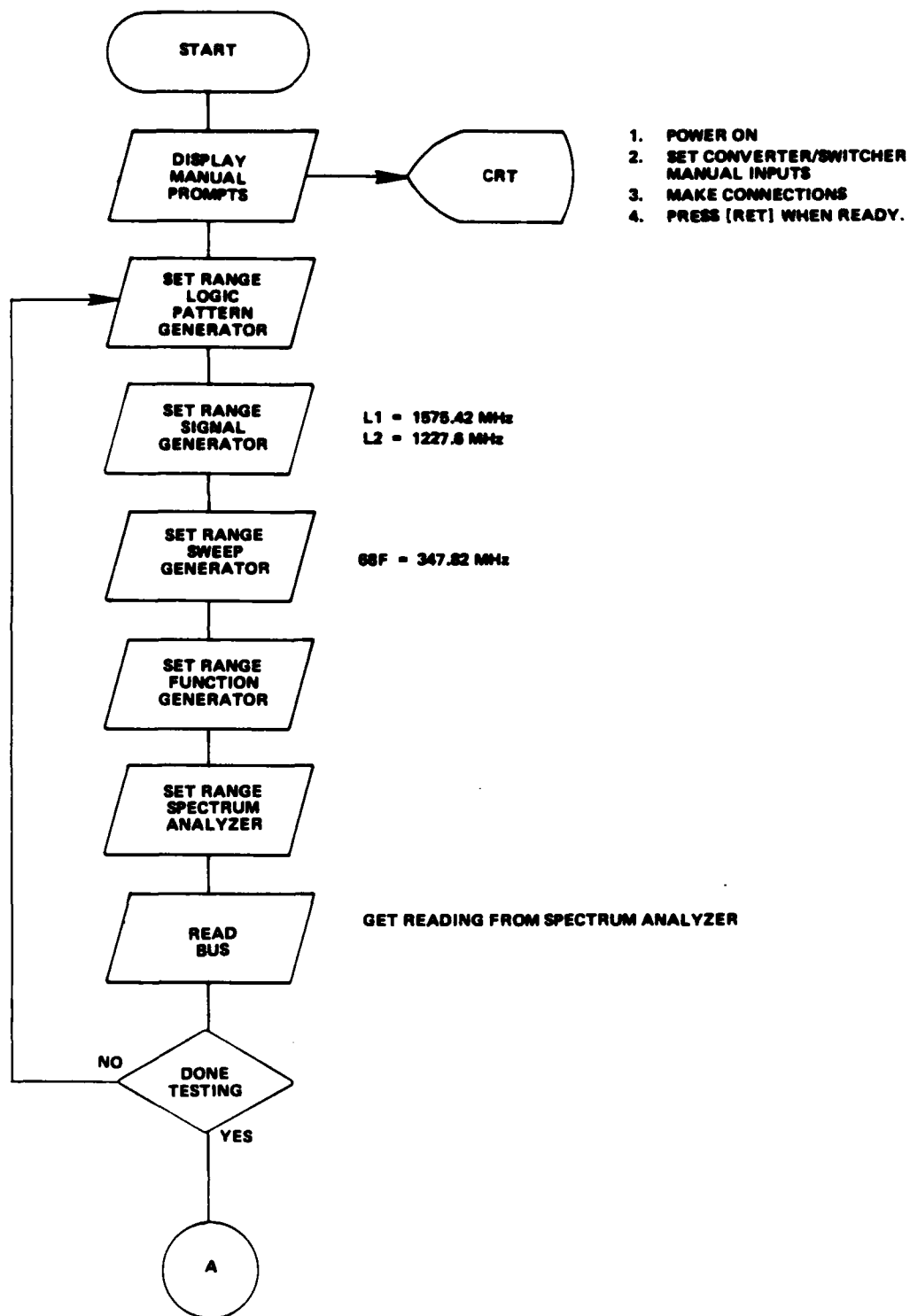
PROGRAM MODULE NAME: _____

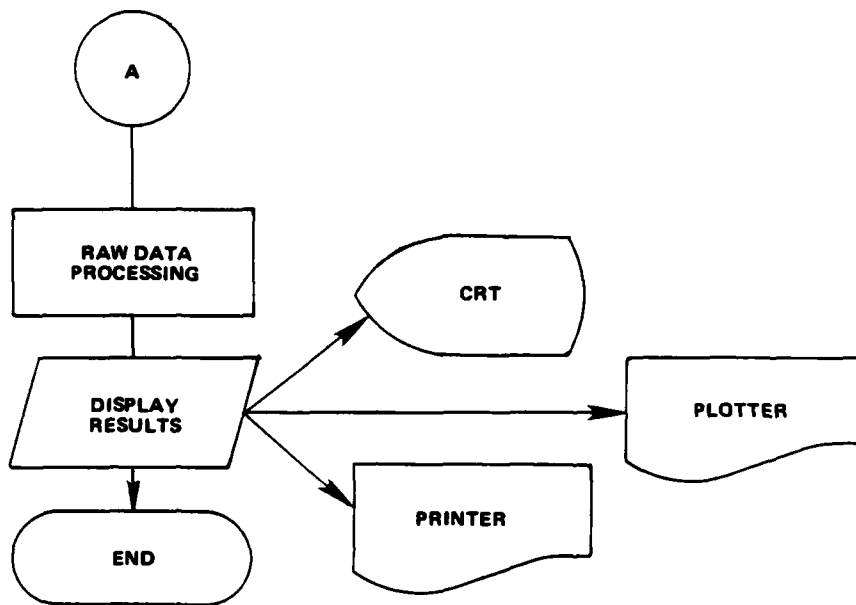




38F(IF) SWITCH OPERATION

PROGRAM MODULE NAME: _____



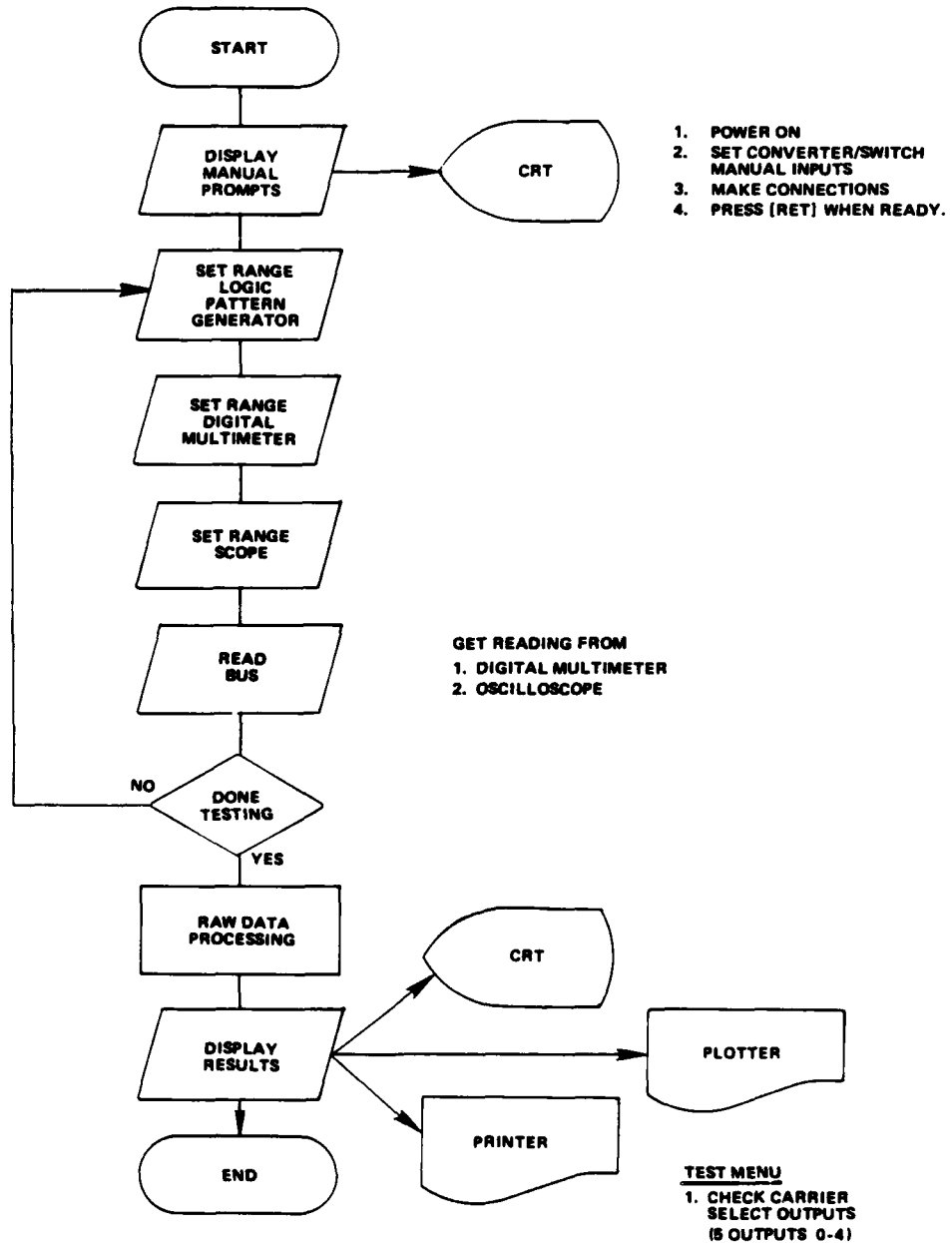


TEST MENU

1. 36F(IF) SWITCH OPERATION
5-CHANNEL (0 - 4)

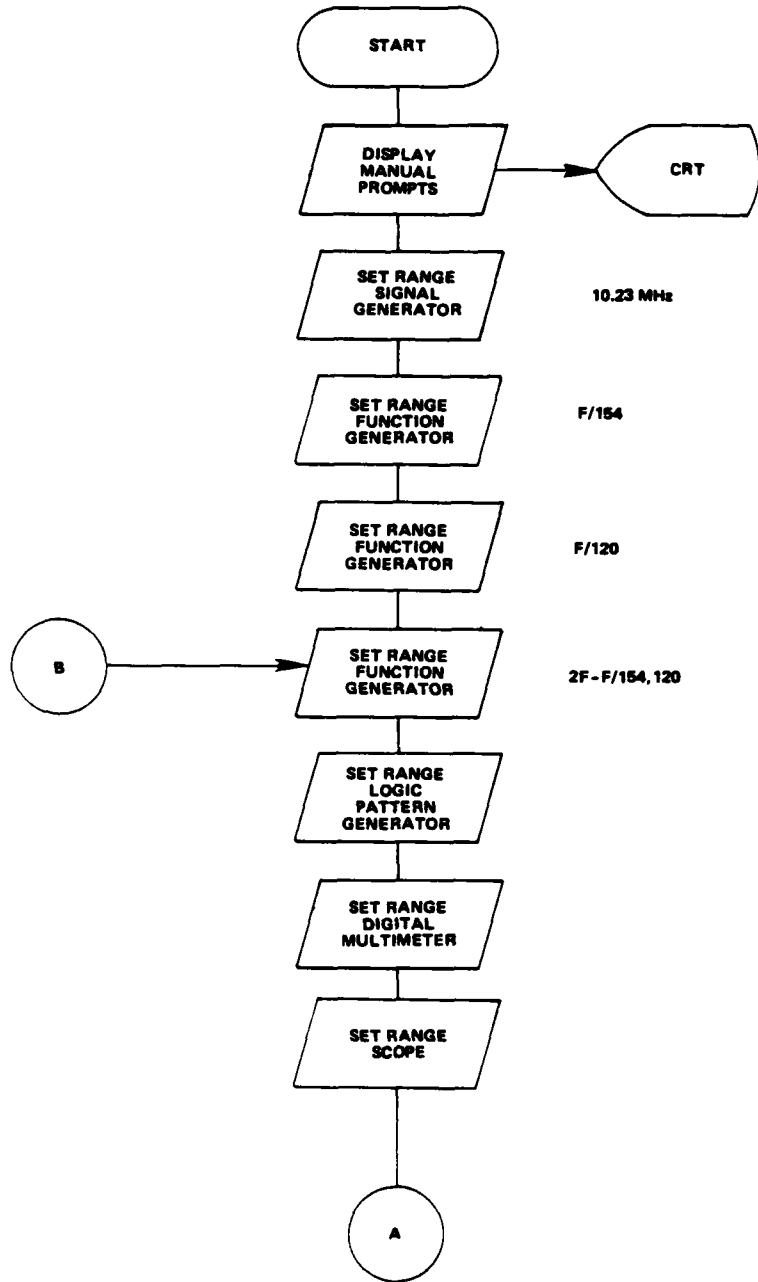
CHECK CARRIER SELECT OUTPUTS

PROGRAM MODULE NAME: _____



CHECK DELTA DIFF. OUTPUTS

PROGRAM MODULE NAME: _____



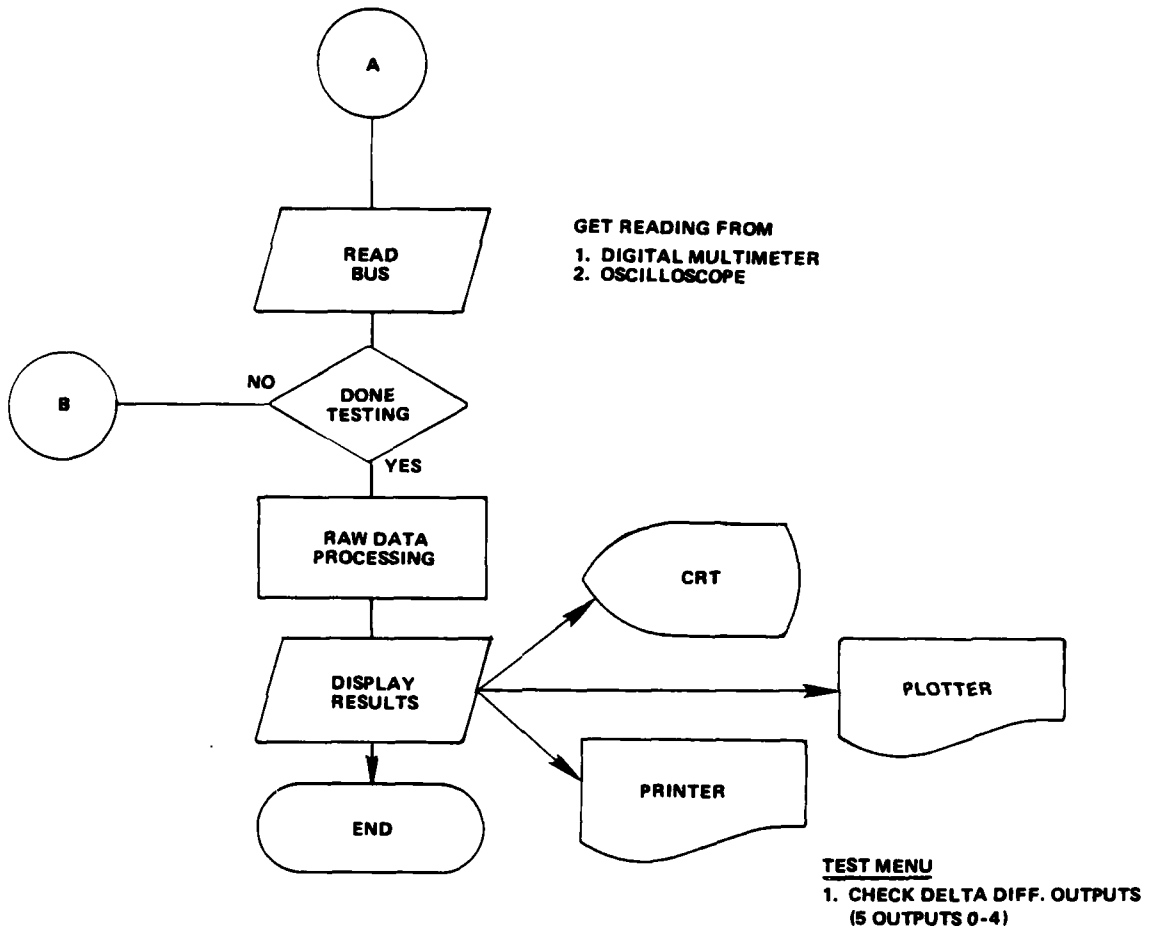
1. POWER ON
2. SET CONVERTER/SWITCHER MANUAL INPUTS
3. MAKE CONNECTIONS
4. PRESS [RET] WHEN READY.

10.23 MHz

F/154

F/120

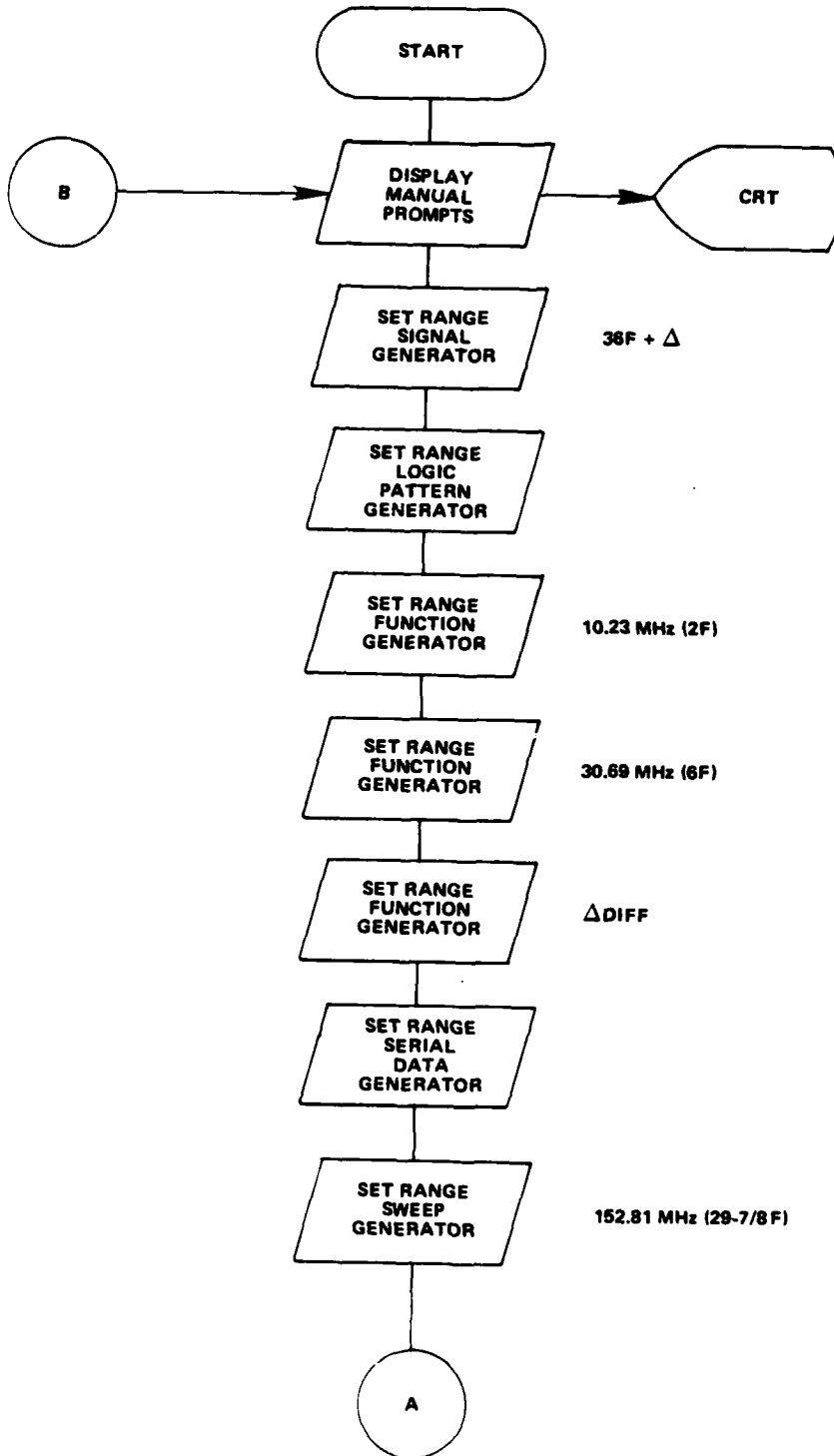
2F - F/154, 120



4.1.3 IF/BB/CODER MODULE

MEASURE DIGITAL OUTPUT SIGNALS

PROGRAM MODULE NAME: _____



1. POWER ON
2. SET IF/BB/CODER MANUAL INPUTS
3. MAKE CONNECTIONS
4. PRESS [RET] WHEN READY

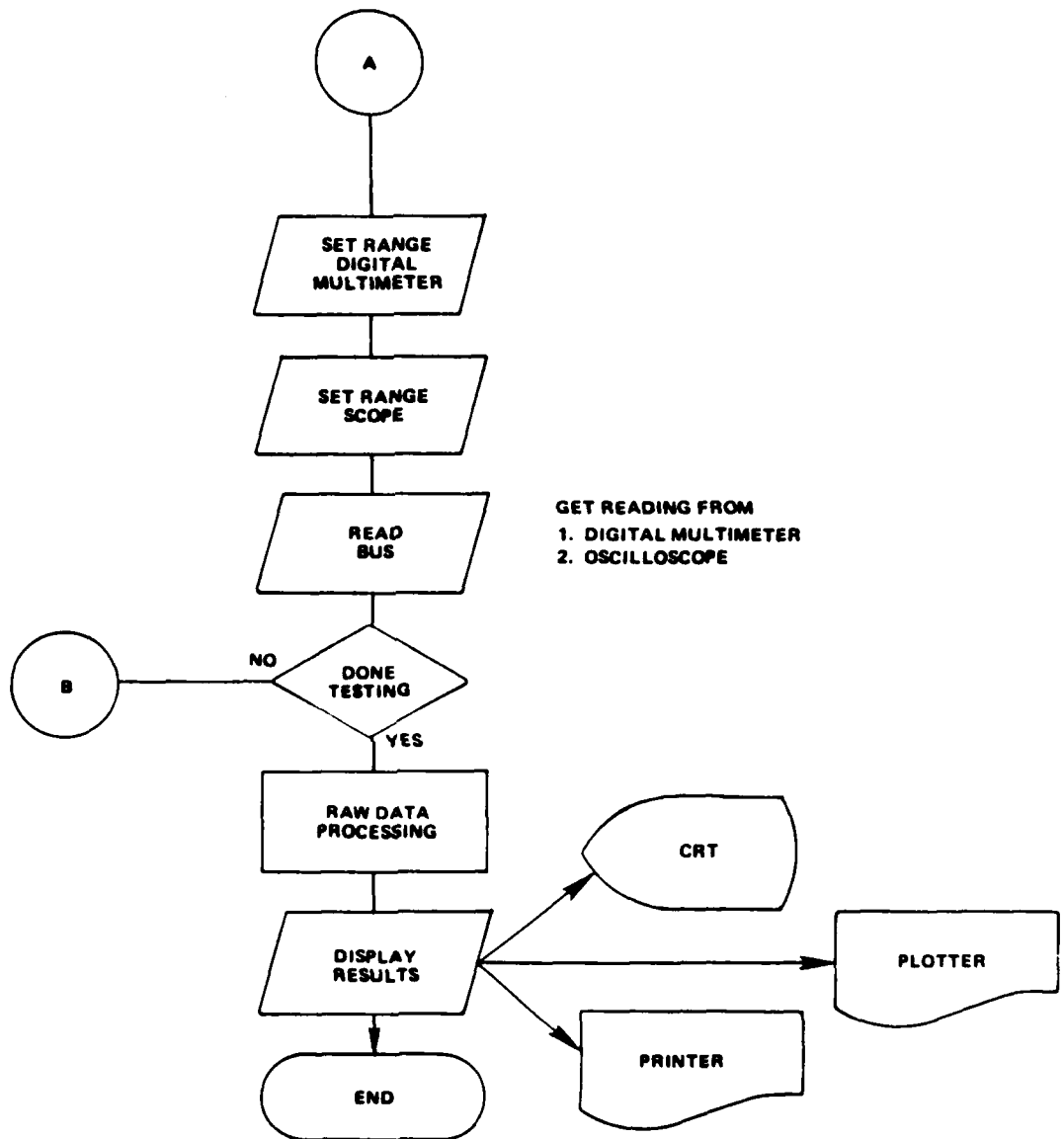
$36F + \Delta$

10.23 MHz (2F)

30.69 MHz (6F)

Δ DIFF

152.81 MHz (29-7/8 F)



GET READING FROM
 1. DIGITAL MULTIMETER
 2. OSCILLOSCOPE

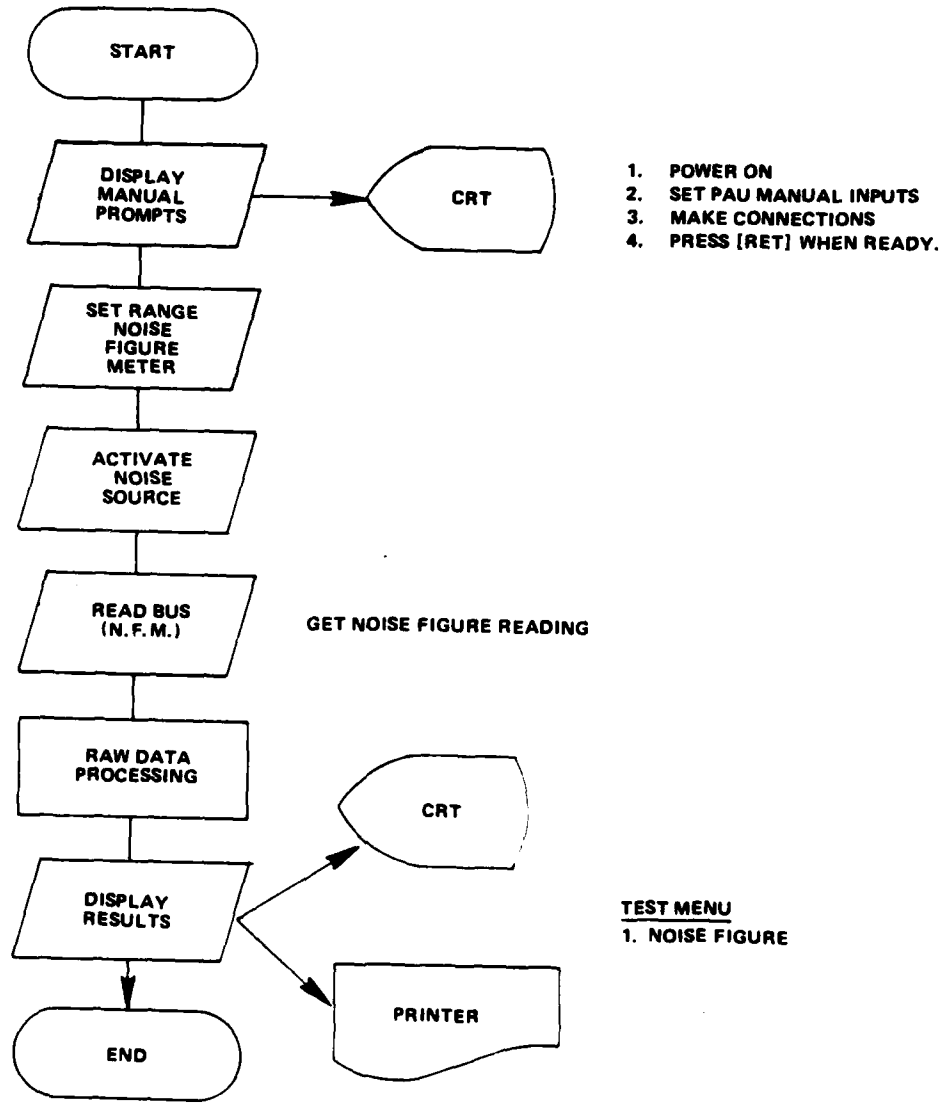
TEST MENU

1. MEASURE SLEW DNE SIGNAL
2. COARSE RANGE EPOCH (CRE)
3. FINE RANGE EPOCH (FRE)
4. AGC S/F1 SIGNAL
5. CARRIER SELECT A SIGNAL
6. CANCEL A SIGNAL
7. R. F. SELECT SIGNAL
8. NOISE ON/OFF SIGNAL
9. ANTENNA SELECT SIGNAL
10. BITE SW SIGNAL

4.1.4 PREAMPLIFIER UNIT MODULE

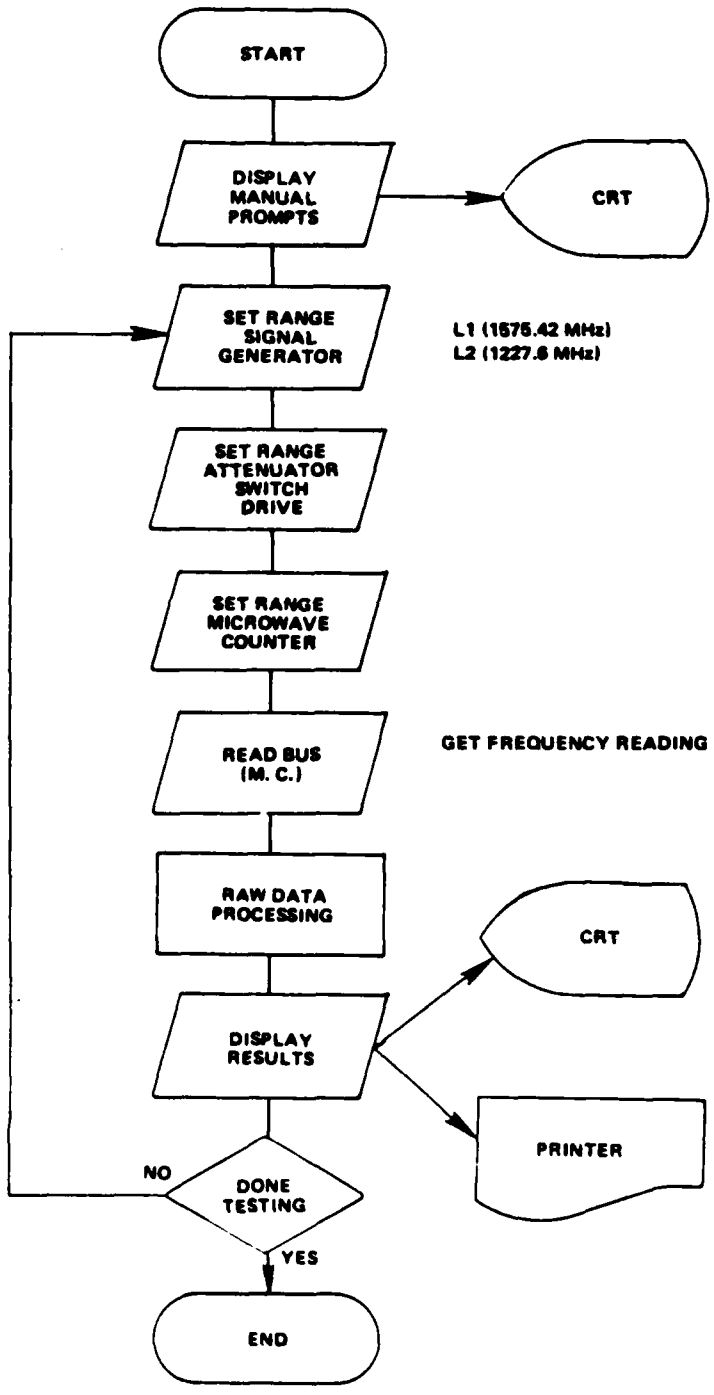
NOISE FIGURE

PROGRAM MODULE NAME: _____



FREQUENCY MEASUREMENT

PROGRAM MODULE NAME: _____



- 1. POWER ON
- 2. SET PAU MANUAL INPUTS
- 3. MAKE CONNECTIONS
- 4. PRESS [RET] WHEN READY

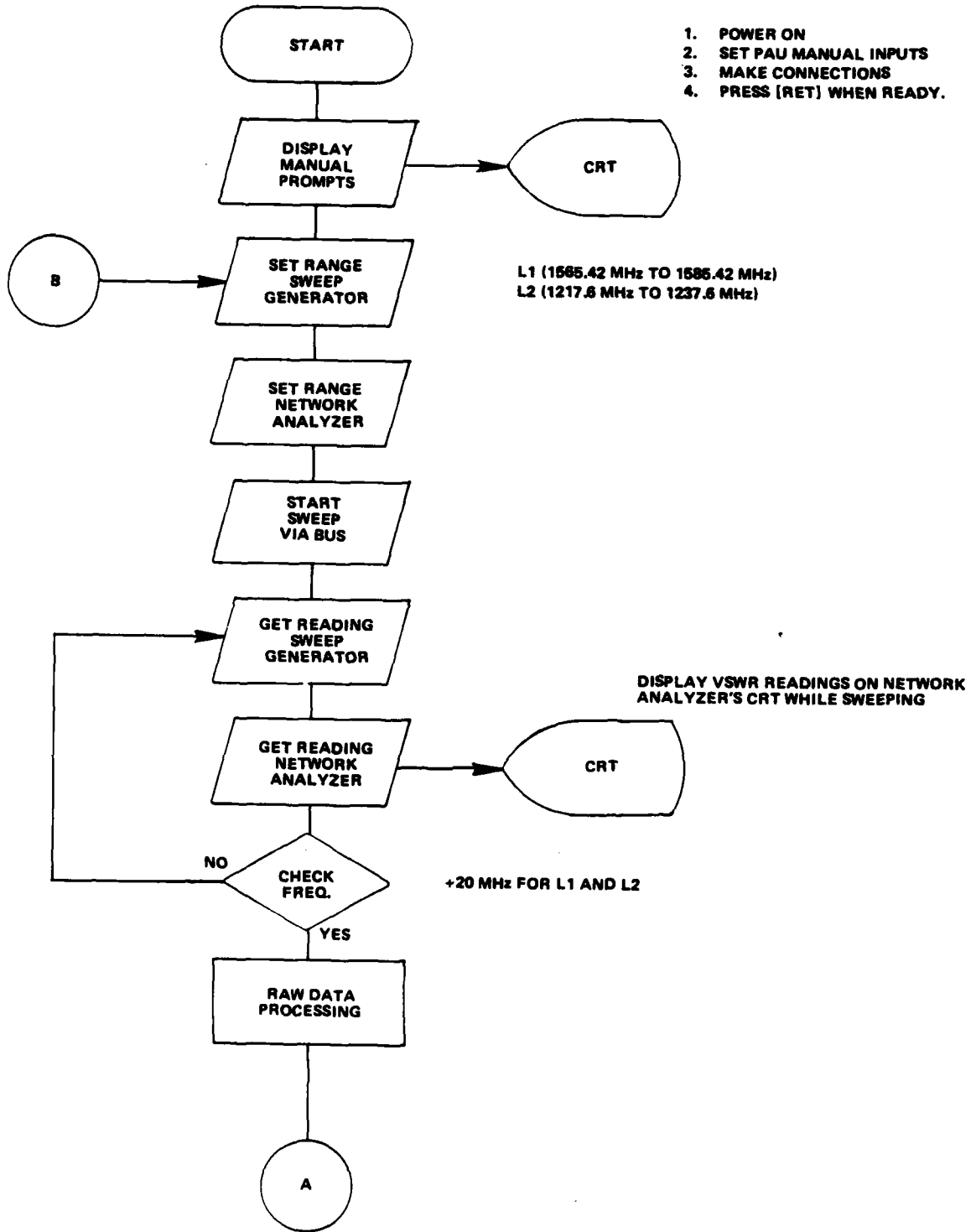
L1 (1575.42 MHz)
L2 (1227.6 MHz)

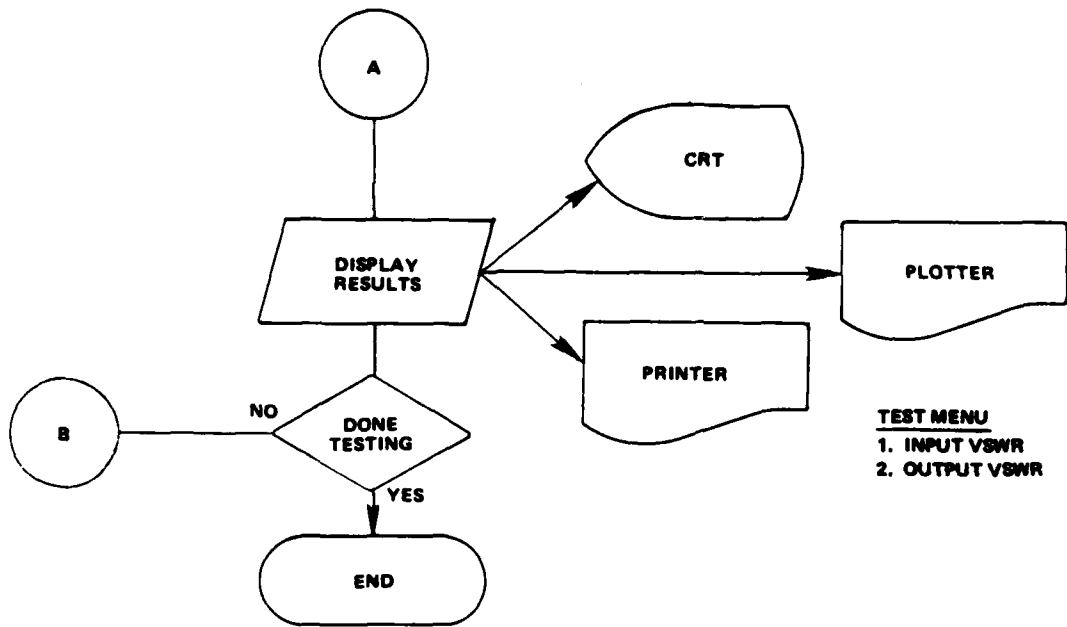
GET FREQUENCY READING

TEST MENU
1. FREQUENCY MEASUREMENT

INPUT/OUTPUT VSWR

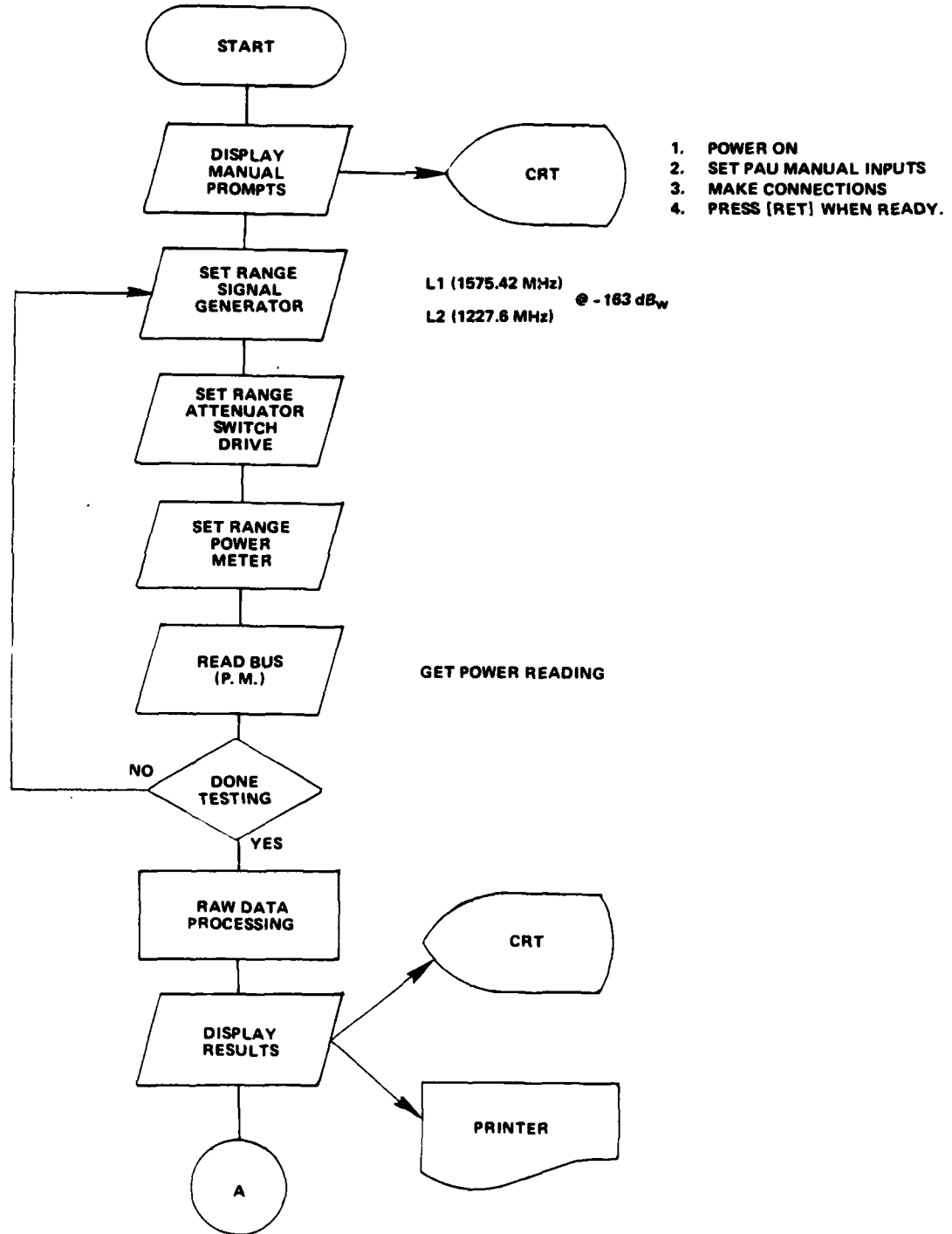
PROGRAM MODULE NAME: _____

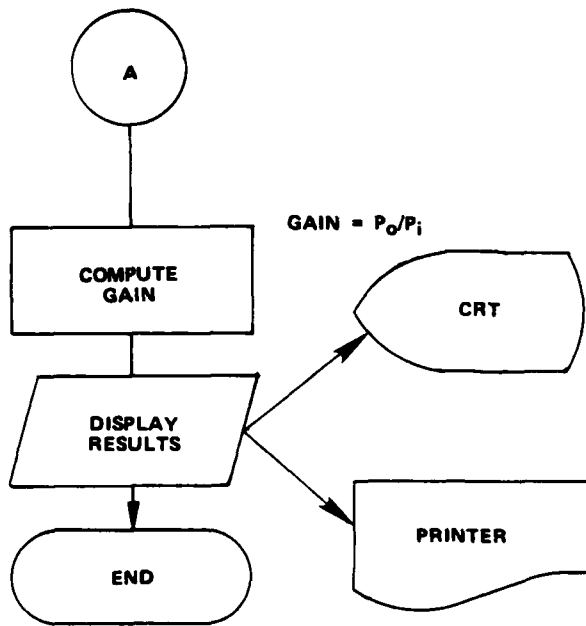




GAIN AND POWER MEASUREMENT

PROGRAM MODULE NAME: _____

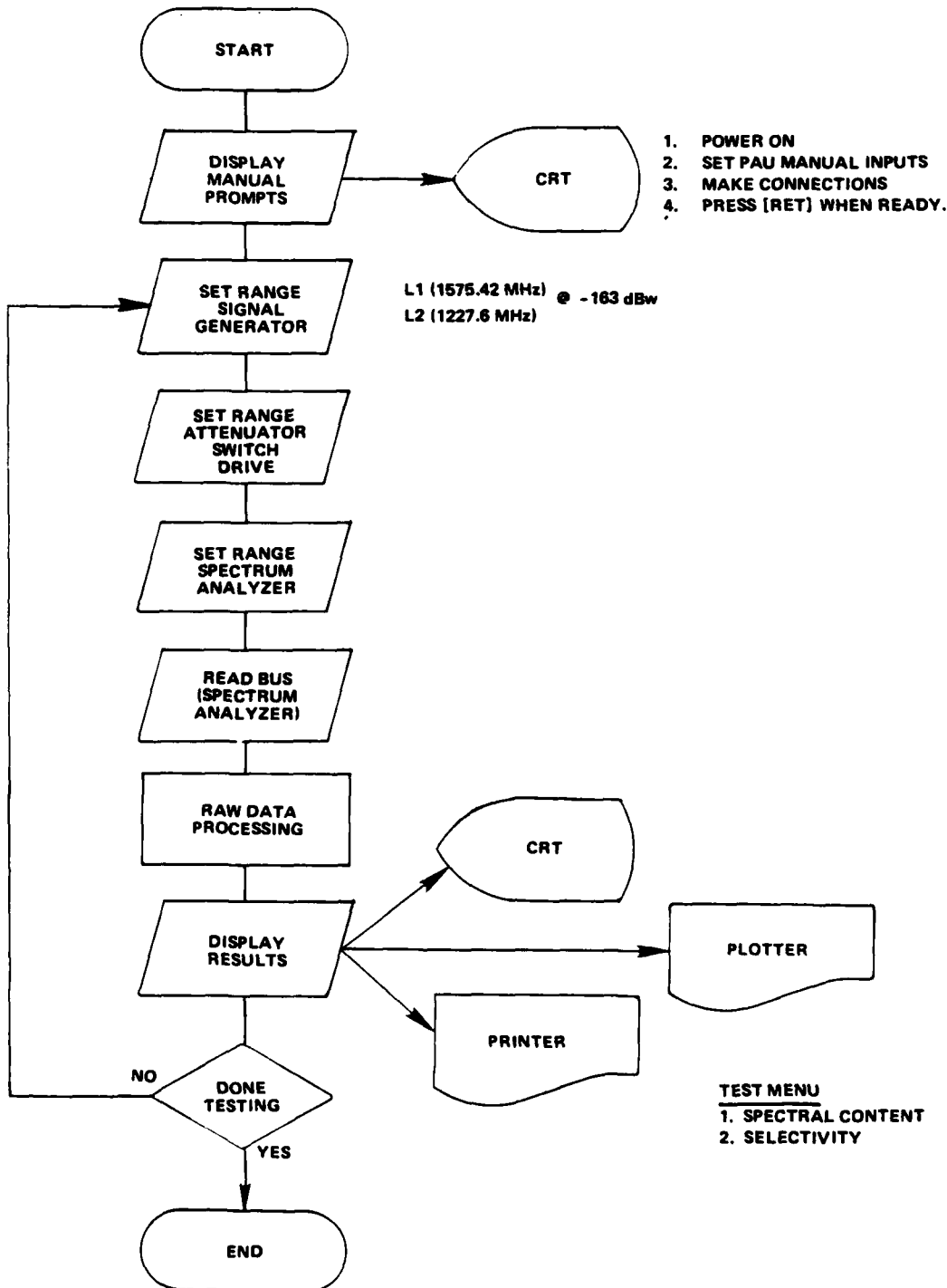




TEST MENU
1. POWER AND GAIN
MEASUREMENT

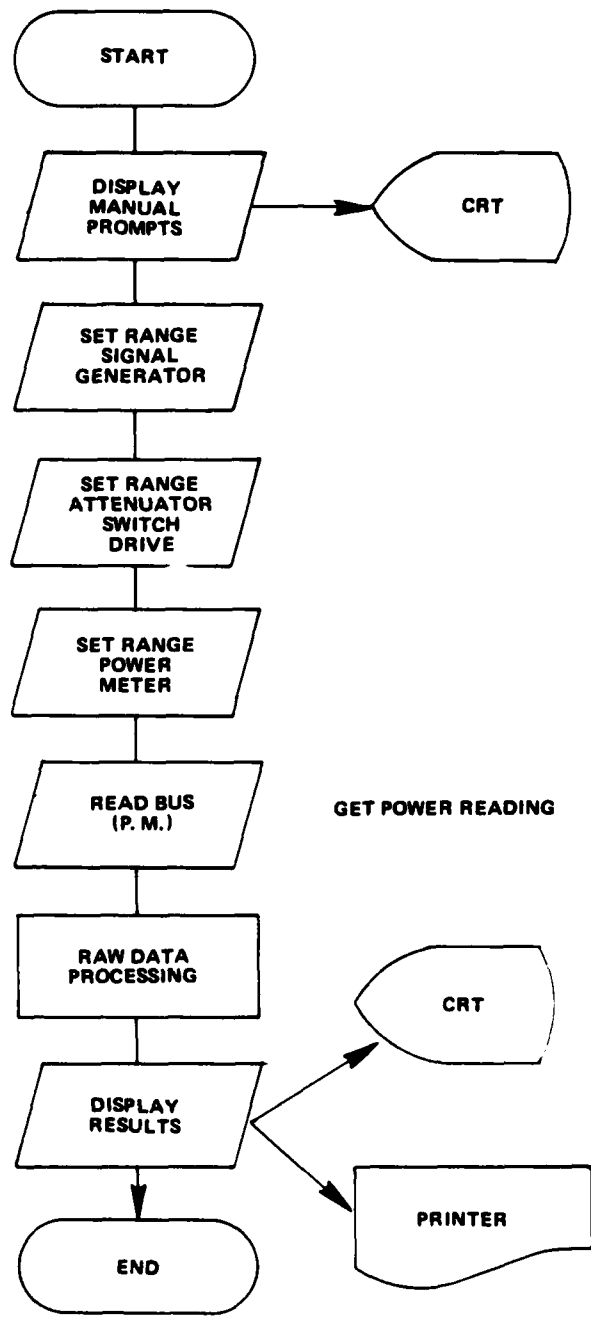
SPECTRAL CONTENT AND SELECTIVITY MEASUREMENT

PROGRAM MODULE NUMBER: _____



VERIFY BURNOUT PROTECTION

PROGRAM MODULE NAME: _____

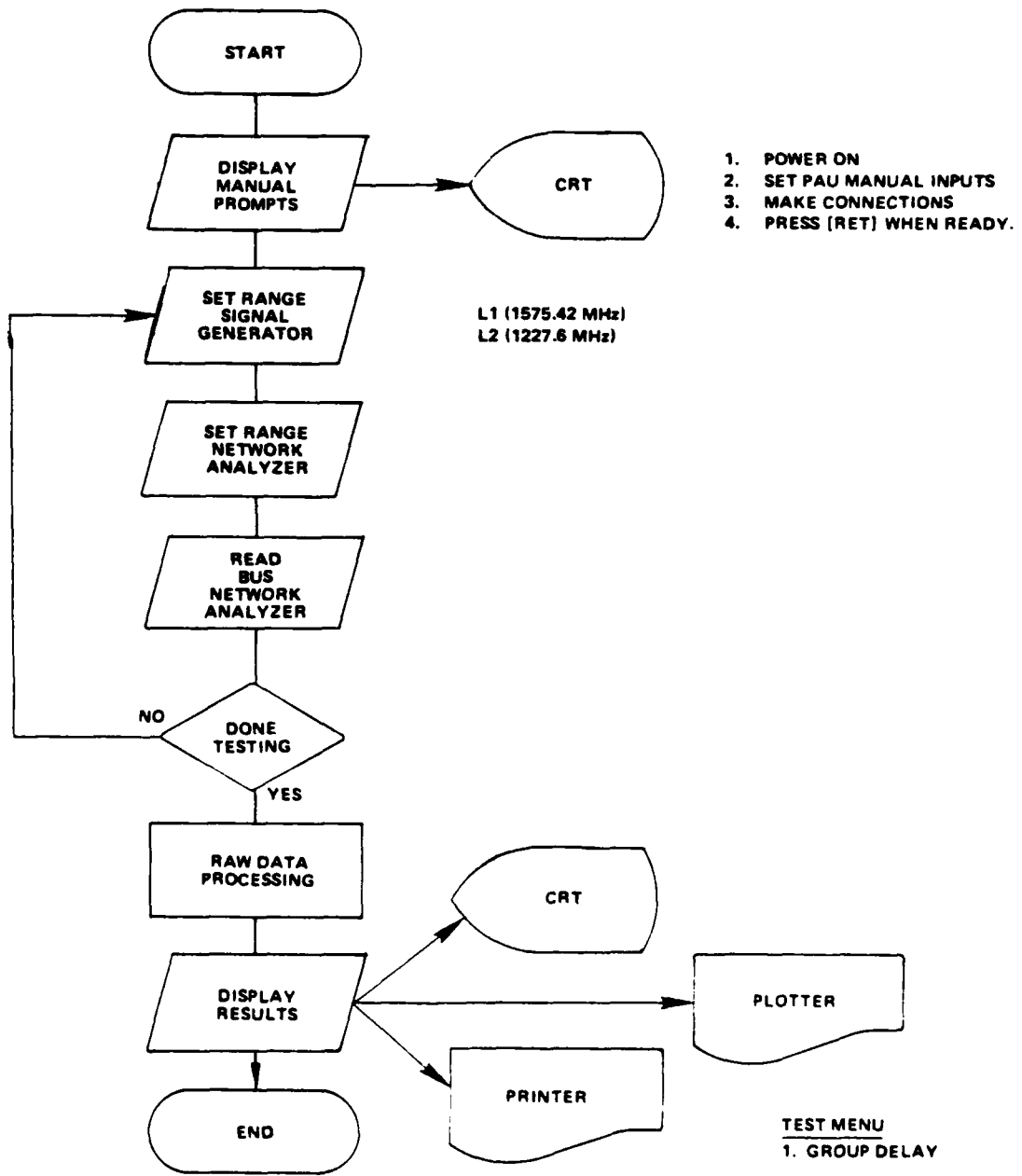


1. POWER ON
2. SET PAU MANUAL INPUTS
3. MAKE CONNECTIONS
4. PRESS [RET] WHEN READY.

TEST MENU
1. BURNOUT PROTECTION

GROUP DELAY

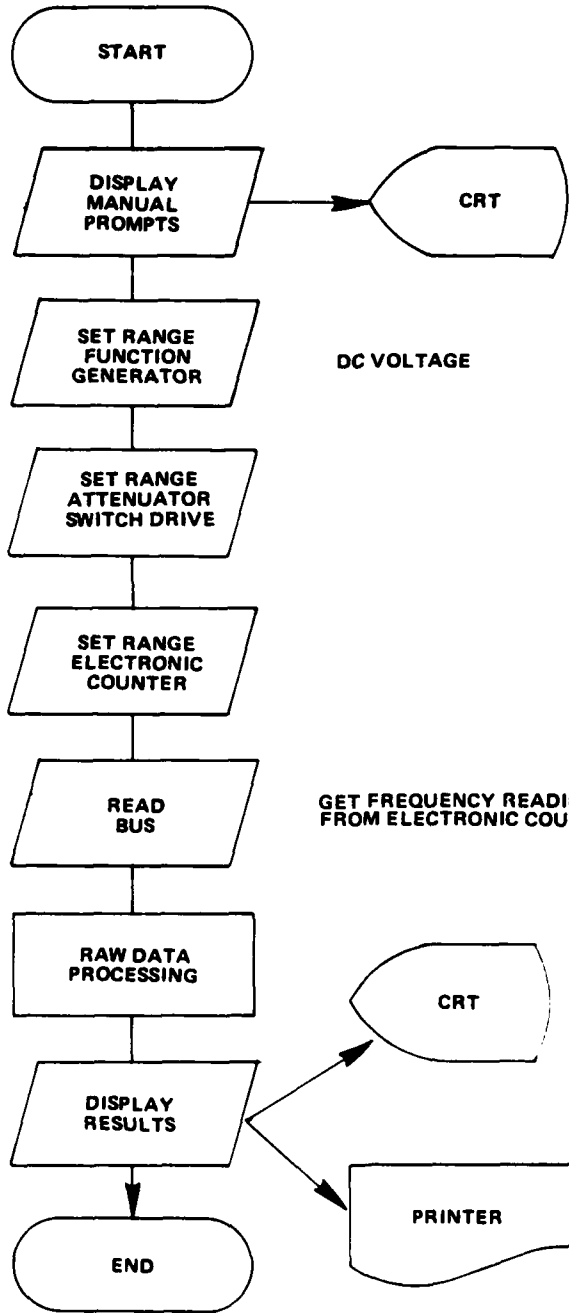
PROGRAM MODULE NAME: _____



4.1.5 REFERENCE OSCILLATOR MODULE

FREQUENCY MEASUREMENT

PROGRAM MODULE NAME: _____



1. POWER ON
2. SET OSCILLATOR MANUAL INPUTS
3. MAKE CONNECTIONS
4. PRESS [RET] WHEN READY.

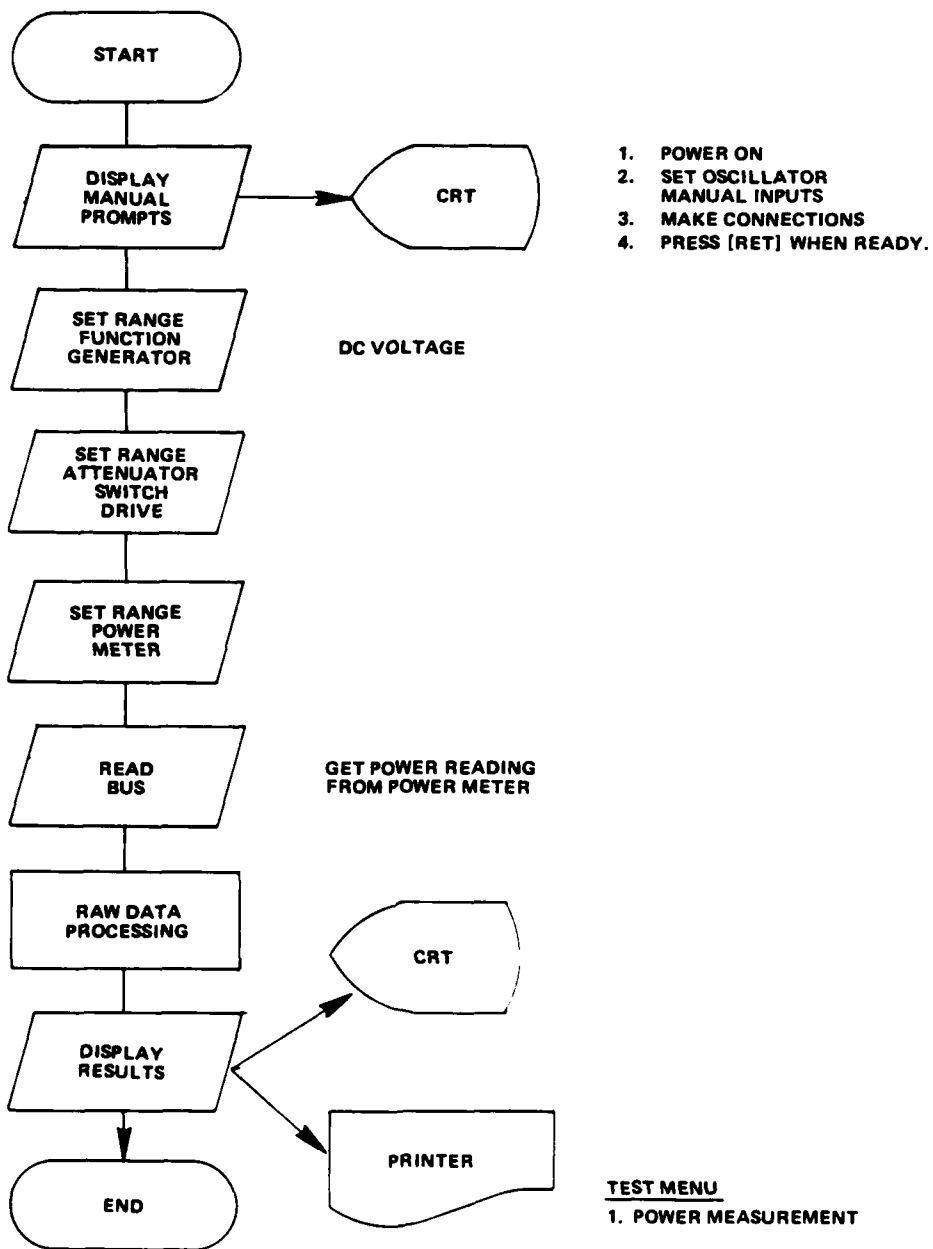
DC VOLTAGE

GET FREQUENCY READING FROM ELECTRONIC COUNTER

TEST MENU
1. FREQUENCY MEASUREMENT

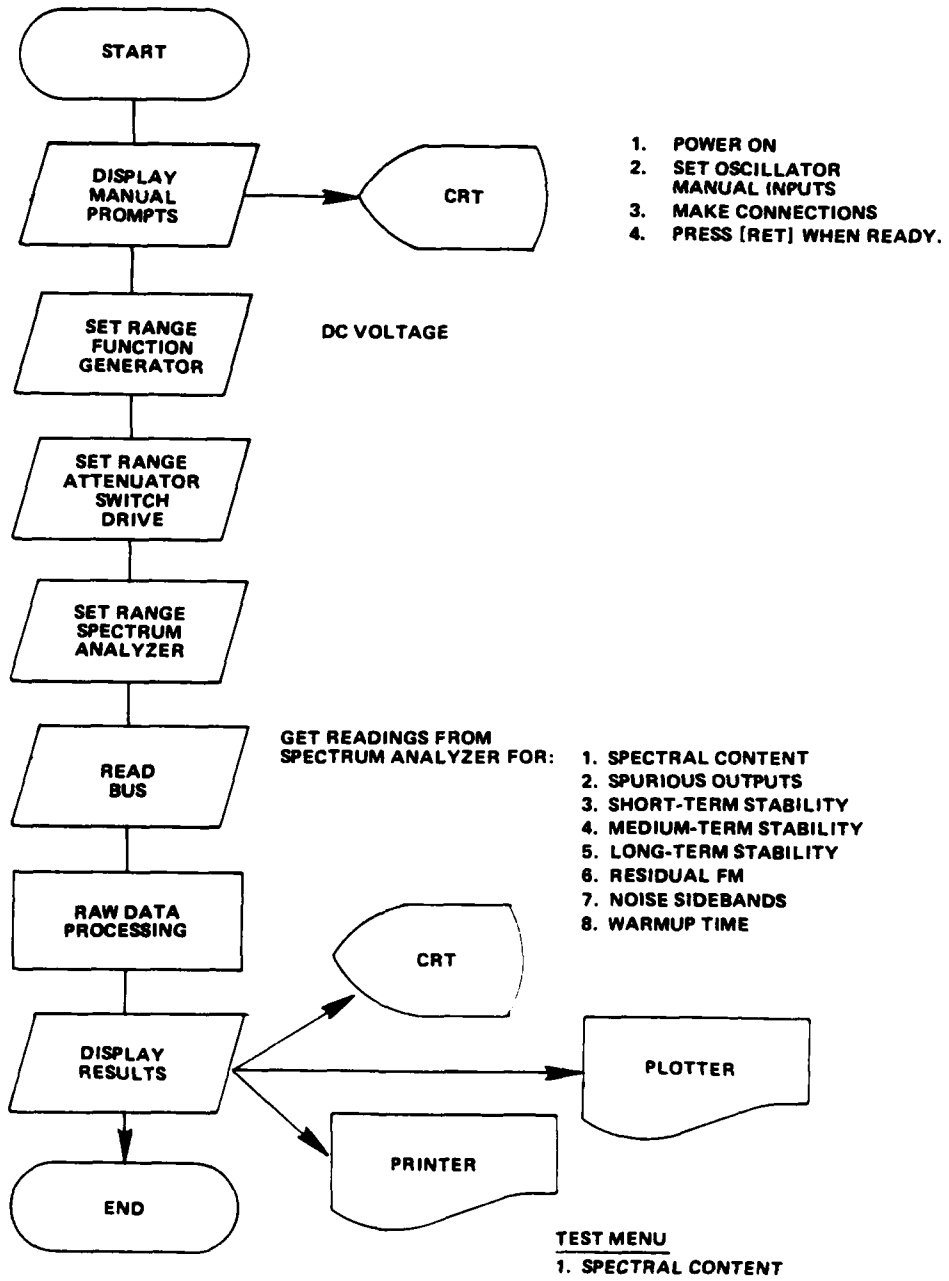
POWER MEASUREMENT

PROGRAM MODULE NAME: _____



CHECK SPECTRAL CONTENT

PROGRAM MODULE NAME: _____



END