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Verification and Validation of DTRA's Unified EM Design & Computer Demonstration

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March 7, 2008



Approved for public release

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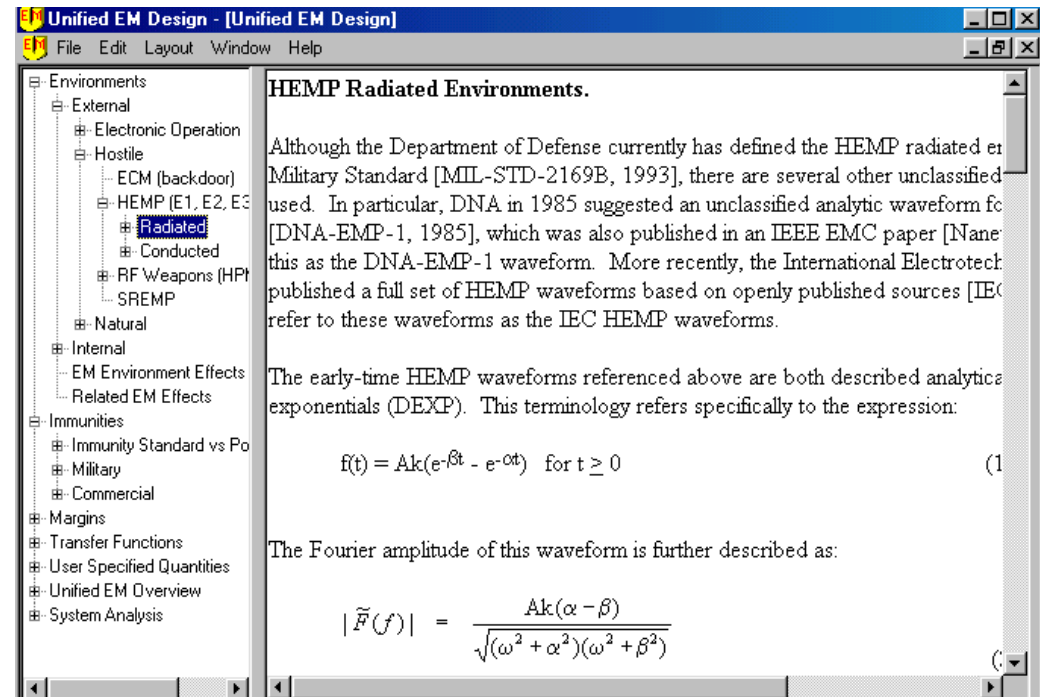
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Example UEM Design Screen

- Explorer style user's interface
- Detailed information on
 - Environments,
 - Immunities,
 - Margins, and
 - Transfer functions
- On-line help
- System design tool
 - Topology
 - Barrier Performance Requirements
 - Enclosure Ports
 - Penetration Ports



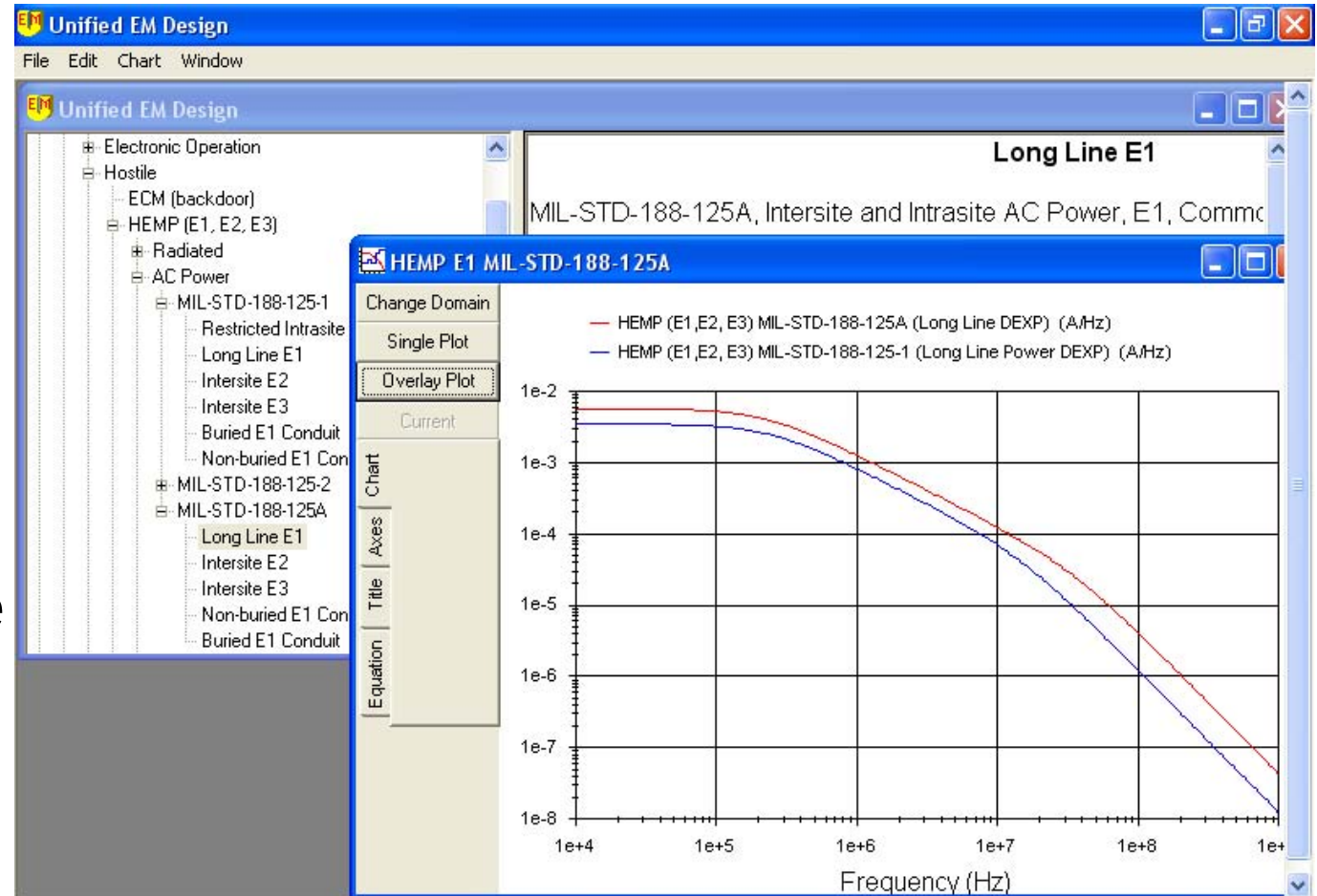
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Comparison capability

- Comparison in time or frequency domain
- Environment versus Immunity
- Copy & Paste Charts



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Summary

- Unified EM Design available free of charge to government agencies and their contractors
 - Request form at www.uemdesign.com
- Demonstration will cover all capabilities
- Poster paper on Verification and Validation of Version 2.3